FSS234



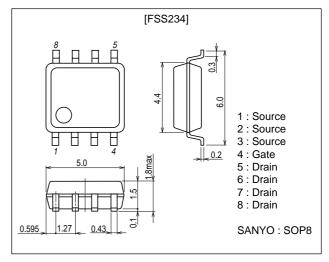
DC / DC Converter Applications

Features

- · Low ON-resistance.
- 4.0V drive.
- · Ultrahigh-speed switching.

Package Dimensions

unit : mm 2116



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		12	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	52	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1200mm ² X0.8mm)	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			11.7
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _G S=0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =12A	12.6	18		S

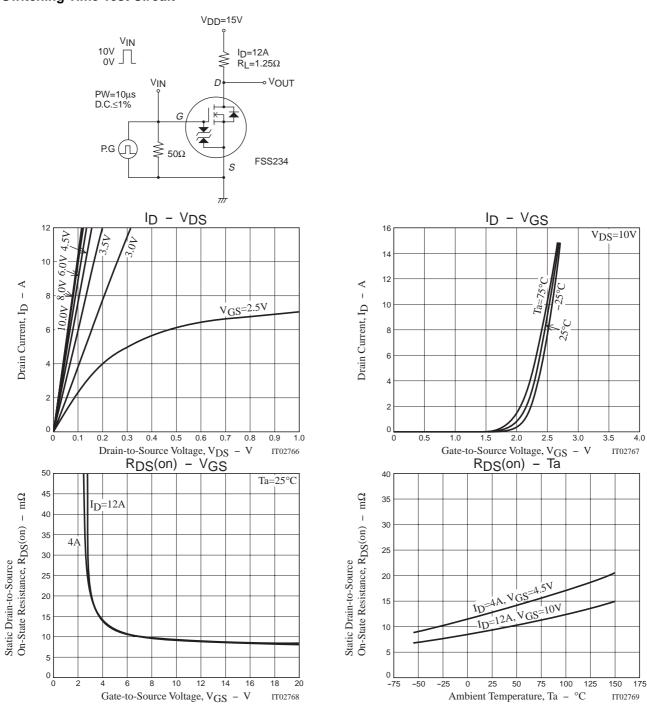
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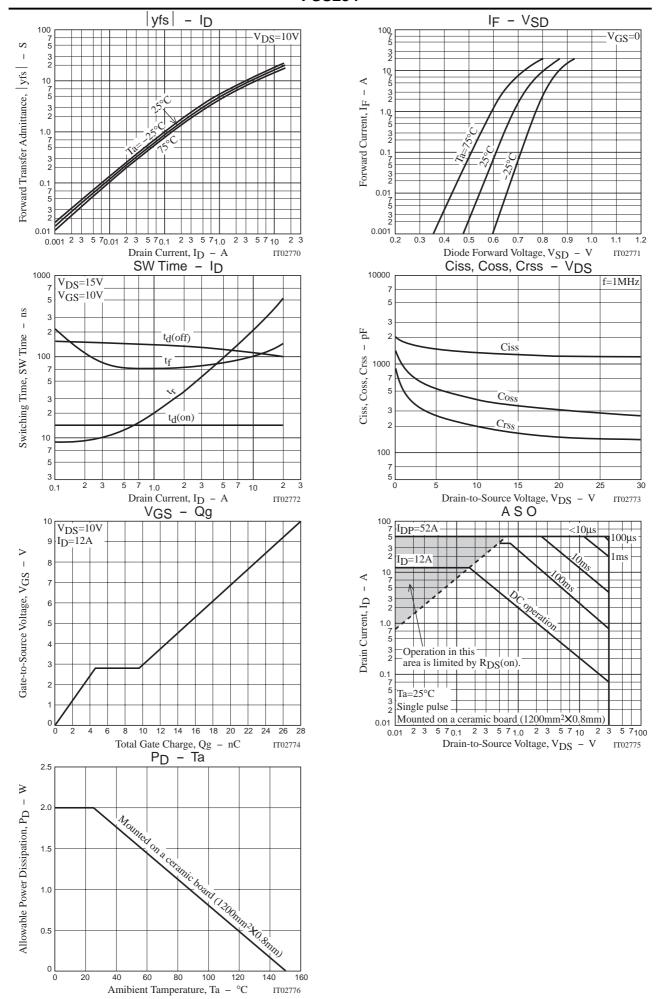
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Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =12A, V _{GS} =10V		9.5	13	mΩ
	RDS(on)2	I _D =4A, V _G S=4.5V		13	19	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		1450		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		420		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		210		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		14		ns
Rise Time	t _r	See specified Test Circuit		280		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		110		ns
Fall Time	tf	See specified Test Circuit		100		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =12A		28		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =12A		4.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =12A		5		nC
Diode Forward Voltage	V _{SD}	I _S =12A, V _{GS} =0		0.81	1.2	V

Switching Time Test Circuit





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