



# **Load Switching Applications**

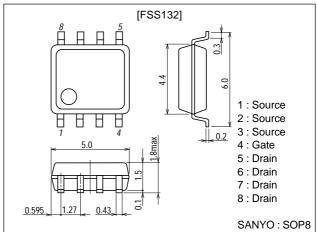
#### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

# **Package Dimensions**

unit:mm

2116



# **Specifications**

## **Absolute Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-30	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±20	V
Drain Current (DC)	I <sub>D</sub>		-7	Α
Drain Current (pulse)	I <sub>DP</sub>	PW≤10µs, duty cycle≤1%	-48	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1200mm <sup>2</sup> ×0.8mm)	1.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Drain-to-Source Breakdown Voltage	V(BR)DSS	$I_D=-1$ mA, $V_{GS}=0$	-30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0		-2.4	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-7A	8.4	12		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-7A, V <sub>GS</sub> =-10V		22	29	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-4A, V <sub>GS</sub> =-4.5V		34	48	mΩ
	R <sub>DS</sub> (on)3	I <sub>D</sub> =-4A, V <sub>GS</sub> =-4V		38	54	mΩ

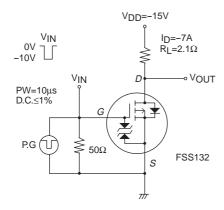
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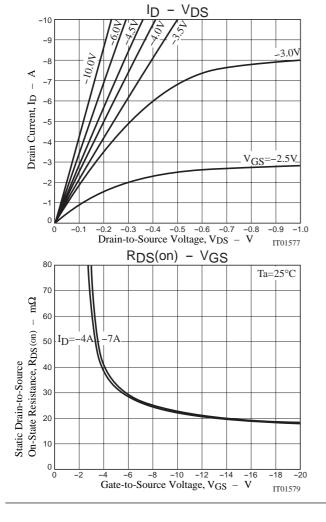
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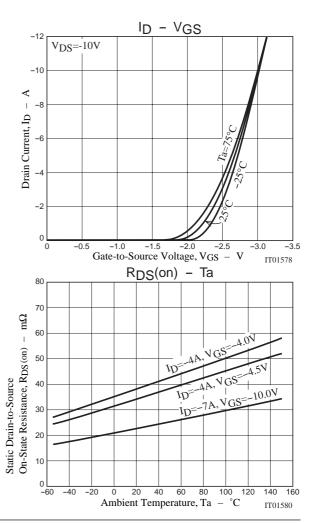
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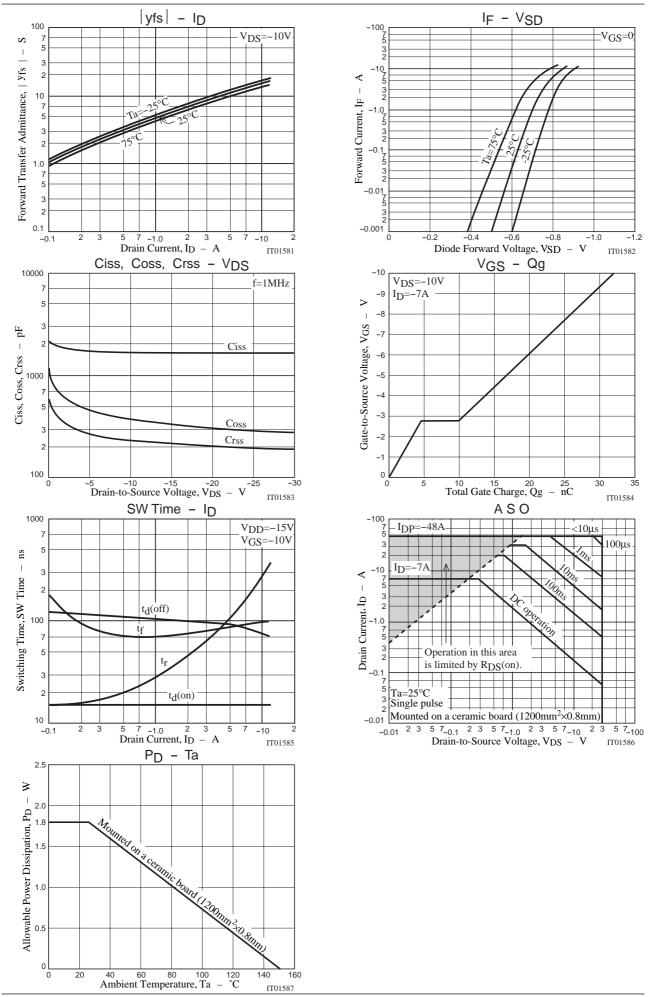
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		1700		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		380		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		240		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit		15		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		150		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		85		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		90		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A		32		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A		4.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A		5		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-7A, V <sub>GS</sub> =0		-0.82	-1.5	V

## **Switching Time Test Circuit**









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