

FP211

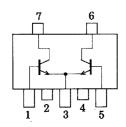
NPN Epitaxial Planar Silicon Transistor

Driver Applications

Features

- · Composite type with 2 transistors (NPN) contained in one package, facilitating high-density mounting.
- The FP211 is formed with 2 chips being equivalent to the 2SD1623, placed in one package.

Electrical Connection



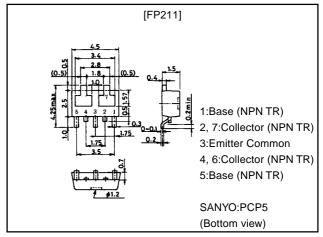
- 1:Base (NPN TR) 2, 7:Collector (NPN TR)
- 3:Emitter Common
- 4, 6:Collector (NPN TR) 5:Base (NPN TR)

(Top view)

Package Dimensions

unit:mm

2097A



Specifications

Absolute Maximum Ratings at Ta = 25°C

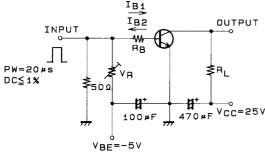
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		60	V
Collector-to-Emitter Voltage	V _{CEO}		50	V
Emitter-to-Base Voltage	V _{EBO}		6	V
Collector Current	IC		2	Α
Collector Current (Pulse)	I _{CP}		4	Α
Base Current	I _B		400	mA
Collector Dissipation	PC	Mounted on ceramic board (250mm ² ×0.8mm) 1 unit	0.8	W
Total Dissipation	PT	Mounted on ceramic board (250mm²×0.8mm)	1.1	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

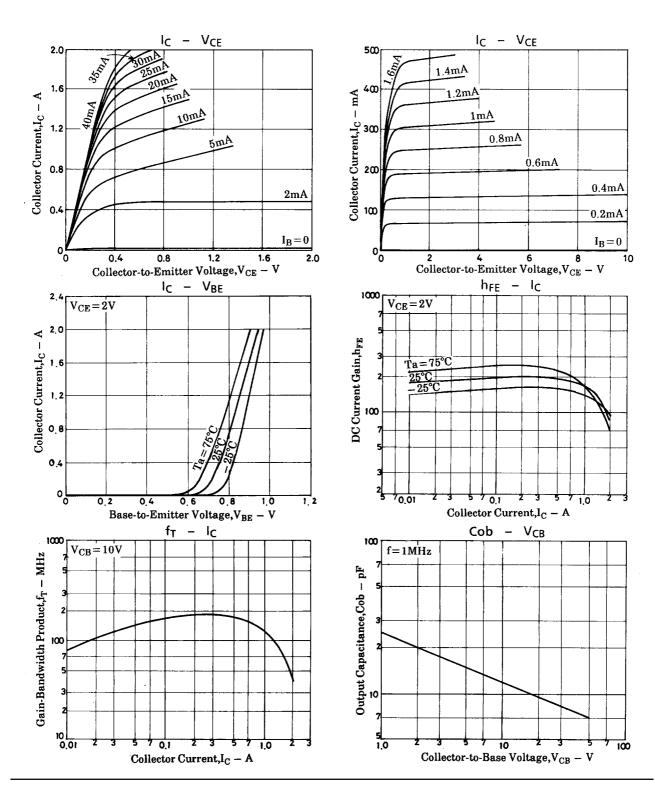
Parameter	Symbol	Conditons	Ratings			Unit
			min	typ	max	Uill
Collector Cutoff Current	ICBO	V _{CB} =50V, I _E =0			100	nA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V, I _C =0			100	nA
DC Current Gain	hFE	V _{CE} =2V, I _C =100mA	140		400	
Gain-Bandwidth Product	f _T	V _{CE} =10V, I _C =50mA		150		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		12		pF
C-E Saturation Voltage	VCE(sat)	I _C =1A, I _B =50mA		0.15	0.4	V
B-E Saturation Voltage	V _{BE(sat)}	I _C =1A, I _B =50mA		0.9	1.2	V
C-B Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0	60			V
C-E Breakdown Voltage	V(BR)CEO	I _C =1mA, R _{BE} =∞	50			V
E-B Breakdown Voltage	V _{(BR)EBO}	I _E =10μA, I _C =0	6			V
Turn-ON Time	t _{on}	See specified Test Circuit		60		ns
Storage Time	t _{stg}	See specified Test Circuit		550		ns
Fall Time	t _f	See specified Test Circuit		30		ns

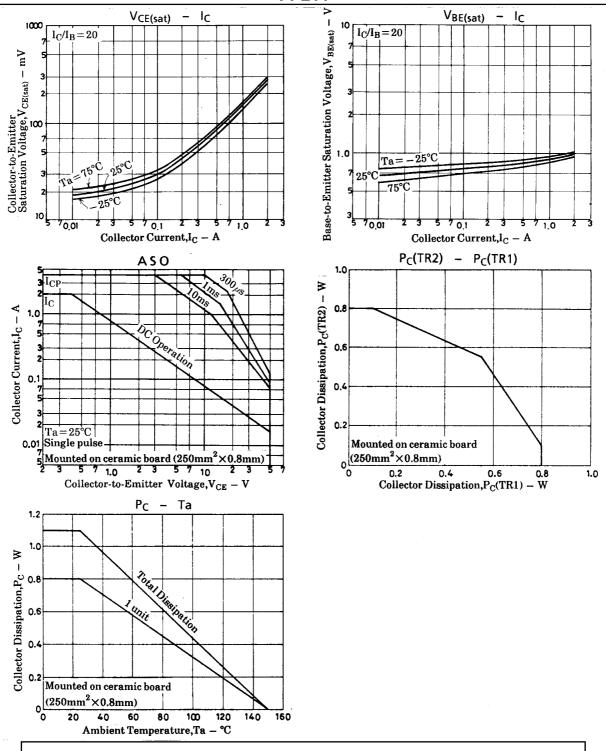
Marking:211

Switching Time Test Circuit



10181=-10182=IC=500mA





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