

FDD6690A

N-Channel, Logic Level, PowerTrench® MOSFET

General Description

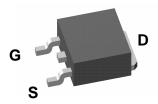
This N-Channel Logic level MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on state resistance and yet maintain low gate charge for superior switching performance.

Applications

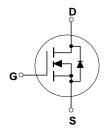
- DC/DC converter
- Motor drives

Features

- 46 A, 30 V. $R_{DS(ON)} = 0.0125~\Omega~$ @ $V_{GS} = 10~V$ $R_{DS(ON)} = 0.016~\Omega~$ @ $V_{GS} = 4.5~V.$
- Low gate charge (17nC typical).
- Fast switching speed.
- $\bullet \;\;$ High performance trench technology for extremely low $R_{\text{DS(ON)}}.$



TO-252



Absolute Maximum Ratings T _A =25°C unless otherwise noted							
Symbol	Parameter	Ratings	Units				
V_{DSS}	Drain-Source Voltage		30	V			
V _{GSS}	Gate-Source Voltage		<u>±</u> 20	V			
I _D	Drain Current - Continuous	(Note 1)	46	А			
		(Note 1a)	12				
	Drain Current - Pulsed		100				
P_D	Maximum Power Dissipation @ T _C = 25°C	(Note 1)	50	W			
	T _A = 25°C	(Note 1a)	2.8				
	$T_A = 25^{\circ}C$	(Note 1b)	1.3				
T_J,T_stg	Operating and Storage Junction Temperatu	re Range	-55 to +150	∘C			

Thermal Characteristics										
$R_{\theta^{JC}}$	Thermal Resistance, Junction-to-Case	(Note 1a)	2.5	°C/W						
$R_{\theta^{JA}}$	Thermal Resistance, Junction-to-Ambient	(Note 1b)	96	°C/W						

Package Marking and Ordering Information									
Device Marking	Device	Reel Size	Tape width	Quantity					
FDD6690A	FDD6690A	13"	16mm	2500					

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-So	urce Avalanche Ratings (Note 1)					
W _{DSS}	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 15 \text{ V}, I_{D} = 12 \text{ A}$			180	mJ
I _{AR}	Maximum Drain-Source Avalanche Cur	rent			12	Α
Off Char	acteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V
ΔBVnss ΔT	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		25		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	μд
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 20V, V_{DS} = 0 V$			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Chara	acteristics (Note 2)					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1	1.6	3	V
$\Delta V_{GS(th)}$ $\Delta T_{.1}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		-4		mV/°C
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$ $V_{GS} = 10 \text{ V}, I_D = 12$ $A, T_J = 125^{\circ}\text{C}$ $V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$.0009 .0015 .0120	0.0125 0.019 0.016	Ω
I _{D(on)}	On-State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 5 \text{ V}$	50			Α
g FS	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 12 \text{ A}$		44		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$		1700		pF
C _{oss}	Output Capacitance	f = 1.0 MHz		340		pF
C _{rss}	Reverse Transfer Capacitance			140		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	g Characteristics (Note 2) Turn-On Delay Time	$V_{DD} = 15 \text{ V}, I_{D} = 1 \text{ A},$		10	18	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		12	22	ns
t _{d(off)}	Turn-Off Delay Time	1		35	56	ns
t _f	Turn-Off Fall Time	1		10	18	ns
Q _g	Total Gate Charge	$V_{DS} = 15 \text{ V}, I_{D} = 12 \text{ A},$		17	23	nC
Q_{gs}	Gate-Source Charge	$V_{GS} = 5 V$,		5		nC
Q_{gd}	Gate-Drain Charge			6		nC
Drain-So	urce Diode Characteristics and	d Maximum Ratings				
l _s	Maximum Continuous Drain-Source Die				2.3	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 2.3 \text{ A(Note 2)}$		0.72	1.3	V

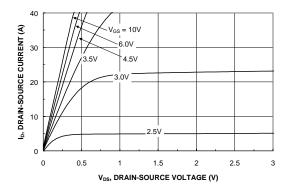
Notes:

1. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the drain tab. $\rm R_{\theta JC}$ is guaranteed by design while $\rm R_{\theta CA}$ is determined by the user's board design.



Scale 1 : 1 on letter size paper 2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

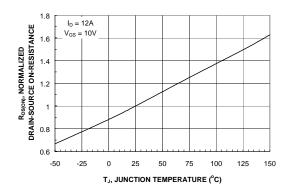
Typical Characteristics



2.6
Q.2.4
Q.2.4
Q.2.4
Q.2.4
Q.3.5
Q.

Figure 1. On-Region Characteristics.

Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.



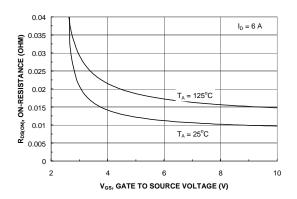
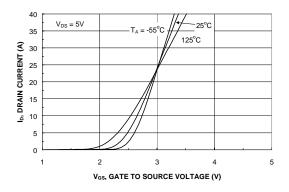


Figure 3. On-Resistance Variation with Temperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



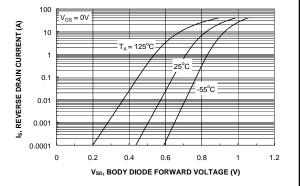
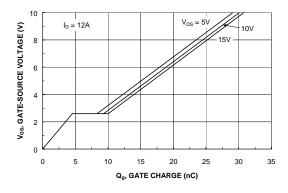


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics (continued)



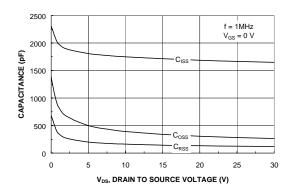
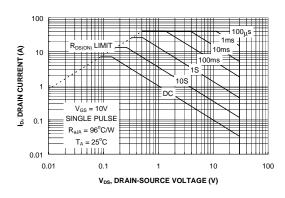


Figure 7. Gate-Charge Characteristics.





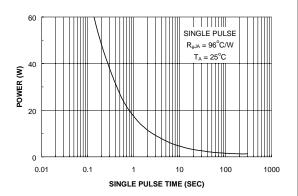


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

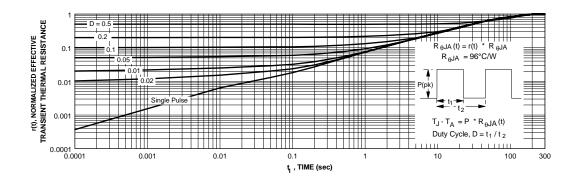
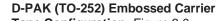


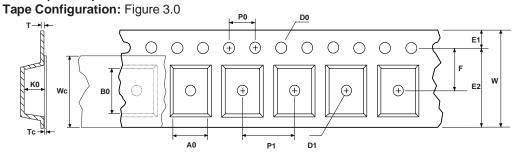
Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1b. Transient themal response will change depending on the circuit board design.

TO-252 Tape and Reel Data and Package Dimensions FAIRCHILD SEMICONDUCTOR TM D-PAK (TO-252) Packaging Configuration: Figure 1.0 Packaging Description: To-252 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2500 units per 13' or 330cm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (anti-static coated). This and some other options are further described in the Packaging Information table. Antistatic Cover Tape ESD Label These full reels are individually barcode labeled and placed inside a standard intermediate box (illustrated in figure 1.0) made of recyclable corrugated brown paper. One box contains two reels maximum. And these boxes are placed inside a barcode labeled shipping box which comes in different sizes depending on the number of parts shipped. Static Dissipative **Embossed Carrier Tape** F63TNR Label D-PAK (TO-252) Packaging Information Packaging Option D-PAK (TO-252) Unit Orientation Packaging type TNR Qty per Reel/Tube/Bag 2.500 Reel Size 13" Dia Box Dimension (mm) 359x359x57 5,000 Max qty per Box 359mm x 359mm x 57mm Weight per unit (gm) 0.300 Standard Intermediate box Weight per Reel(kg) 1.200 **ESD Label** F63TNR Label sample F63TNR Label D/C1: Z9942 D/C2: SPEC REV: CPN: QTY1: QTY2: TO-252 (D-PAK) Tape Leader and **Trailer Configuration:** Figure 2.0 \bigcirc \bigcirc \bigcirc \bigcirc \bigcirc 0 0 0 0 Components Trailer Tape 640mm minimum or 1680mm minimum or 80 empty pockets 210 empty pockets







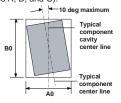
User Direction of Feed

	Dimensions are in millimeter													
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	т	Wc	Тс
TO252 (24mm)	6.90 +/-0.10	10.50 +/-0.10	16.0 +/-0.3	1.55 +/-0.05	1.5 +/-0.10	1.75 +/-0.10	14.25 min	7.50 +/-0.10	8.0 +/-0.1	4.0 +/-0.1	2.65 +/-0.10	0.30 +/-0.05	13.0 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation



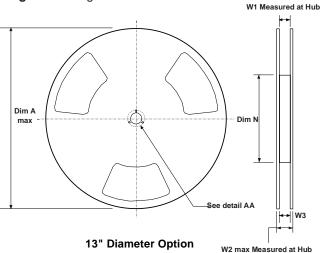
Sketch B (Top View)

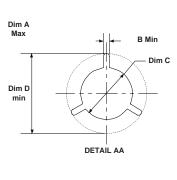
Component Rotation



Sketch C (Top View)
Component lateral movement

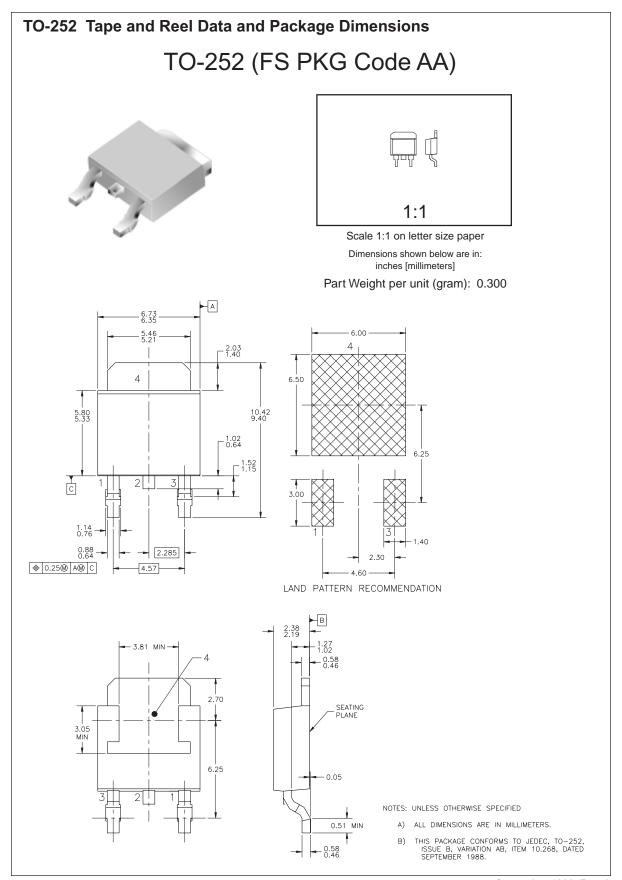
D-PAK (TO-252) Reel Configuration: Figure 4.0





3"	Diameter Option	W2 max Measured at Hu

Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
164mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.646 +0.078/-0.000 16.4 +2/0	0.882 22.4	0.626 - 0.764 15.9 - 19.4



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