

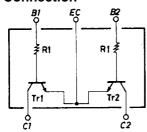
FC138

NPN Epitaxial Planar Silicon Composite Transistor
Switching Applications
(with Bias Resistance)

Features

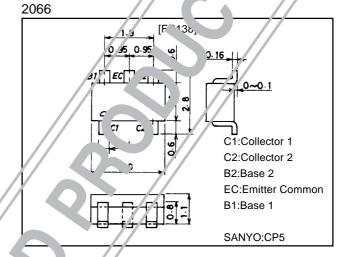
- · On-chip bias resistance (R1=4.7k Ω).
- · Composite type with 2 transistors contained in the CP package currently in use, improving the mounting efficiency greatly.
- The FC138 is formed with two chips, being equivalent to the 2SC3900, placed in one package.
- · Excellent in thermal equilibrium and pair capability.

Electrical Connection



Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25 C

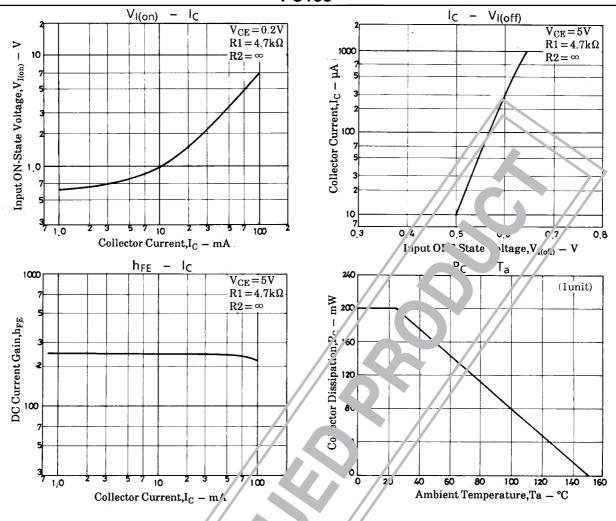
Parameter	Syribol	Co iditions	Ratings	Unit
Collector-to-Base Voltage	Усво		50	V
Collector-to-Emitter Voltage	V _{CEC}		50	V
Emitter-to-Base Voltage	V _E RO	//	5	V
Collector Current	'c	//	100	mA
Peak Collector Current	'CP	//	200	mA
Collector Dissipation	⊢ 1 unit	/	200	mW
Total Power Dissipation	PT		300	mW
Junction Temperature			150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics it Ta = 25°C

Parameter Parameter	Symoo!	Conditons	Ratings			Unit
ra al lietei			min	typ	max	Offic
Collector Cutoff Current	ICBO	V _{CB} =40V, I _E =0			0.1	μΑ
Emitter Cutoff Current	^I EBO	V _{EB} =5V, I _C =0			0.1	μΑ
DC Current Cair	hFE	V _{CE} =5V, I _C =10mA	100			
Gain-Bandwighth Produc	f _T	V _{CE} =10V, I _C =5mA		250		MHz
Output Caracitance	Cob	V _{CB} =10V, f=1MHz		3.3		pF
C-E Saturation V age	V _{CE(sat)}	I _C =10mA, I _B =0.5mA		0.1	0.3	V
C-B b.reaku'own Voi.	V(BR)CBO	I _C =10μA, I _E =0	50			V
C-E Breakdown Voltage	V(BR)CEO	I _C =100μA, R _{BE} =∞	50			V
Input OFF-State Voltage	V _{I(off)}	V _{CE} =5V, I _C =100μA	0.4	0.55	0.8	V
Input ON-State Voltage	V _{I(on)}	V _{CE} =0.2V, I _C =10mA	0.6	1.0	2.0	V
Input Resistance	R1		3.3	4.7	6.1	kΩ

Note: The specifications shown above are for each individual transistor.

Marking:138



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