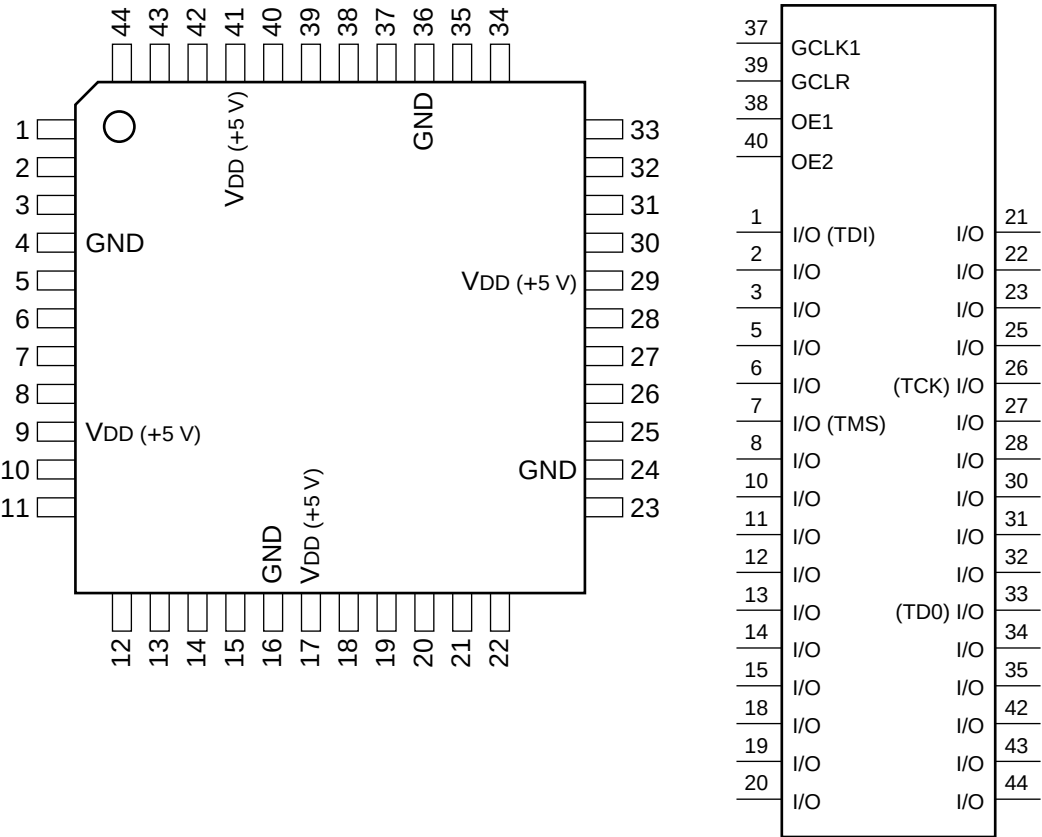


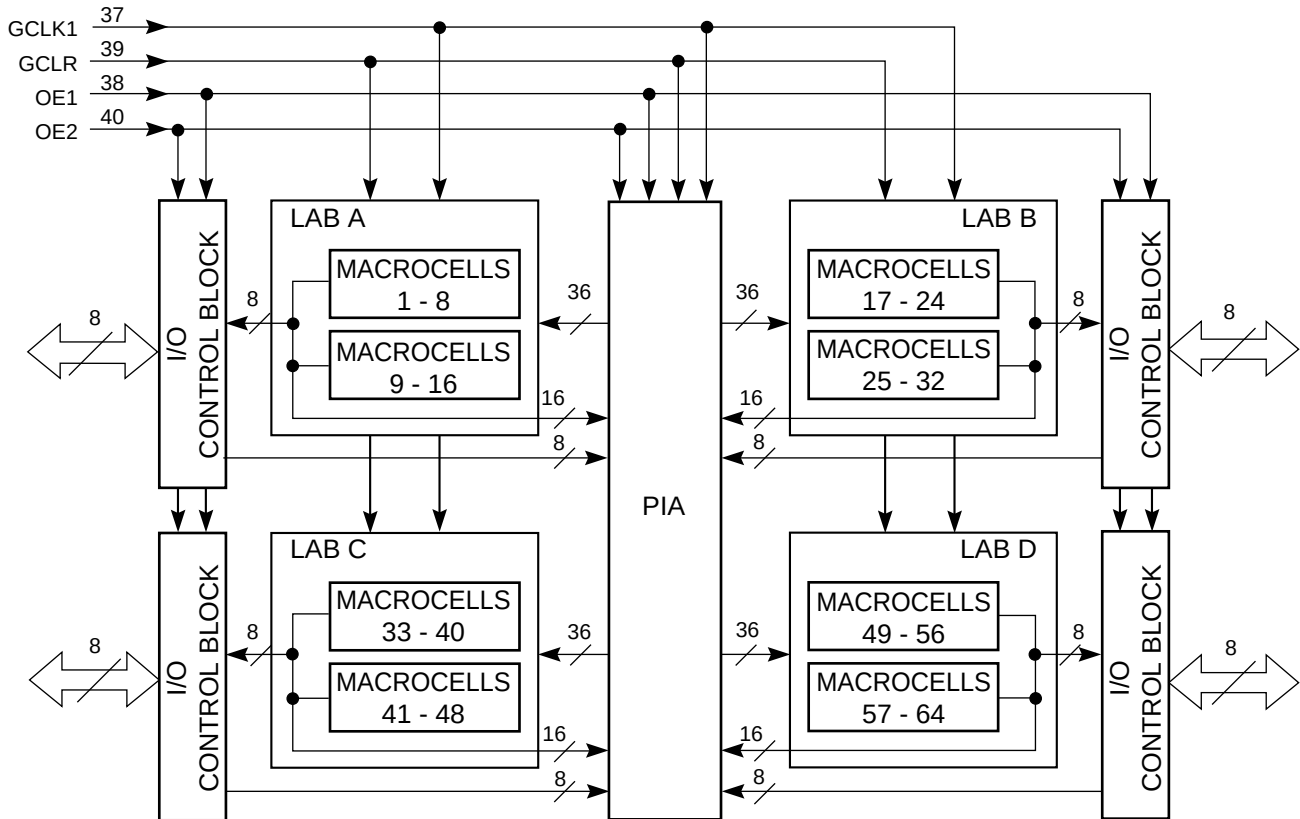
C-MOS EEPROM  
—TOP VIEW—



(VDD = +5 V)

| PIN NO. | I/O | SIGNAL    | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL    | PIN NO. | I/O | SIGNAL |
|---------|-----|-----------|---------|-----|--------|---------|-----|-----------|---------|-----|--------|
| 1       | I/O | I/O (TDI) | 12      | I/O | I/O    | 23      | I/O | I/O       | 34      | I/O | I/O    |
| 2       | I/O | I/O       | 13      | I/O | I/O    | 24      | —   | GND       | 35      | I/O | I/O    |
| 3       | I/O | I/O       | 14      | I/O | I/O    | 25      | I/O | I/O       | 36      | —   | GND    |
| 4       | —   | GND       | 15      | I/O | I/O    | 26      | I/O | I/O (TCK) | 37      | I   | GCLK1  |
| 5       | I/O | I/O       | 16      | —   | GND    | 27      | I/O | I/O       | 38      | I   | OE1    |
| 6       | I/O | I/O       | 17      | —   | VDD    | 28      | I/O | I/O       | 39      | I   | GCLR   |
| 7       | I/O | I/O (TMS) | 18      | I/O | I/O    | 29      | —   | VDD       | 40      | I   | OE2    |
| 8       | I/O | I/O       | 19      | I/O | I/O    | 30      | I/O | I/O       | 41      | —   | VDD    |
| 9       | —   | VDD       | 20      | I/O | I/O    | 31      | I/O | I/O       | 42      | I/O | I/O    |
| 10      | I/O | I/O       | 21      | I/O | I/O    | 32      | I/O | I/O       | 43      | I/O | I/O    |
| 11      | I/O | I/O       | 22      | I/O | I/O    | 33      | I/O | I/O (TD0) | 44      | I/O | I/O    |

GCLK1 : GLOBAL CLOCK  
 GCLR : GLOBAL CLEAR  
 I/O : DATA INPUTS/OUTPUTS  
 OE1 : OUTPUT ENABLE 1 INPUT  
 OE2 : OUTPUT ENABLE 2 INPUT



\*ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.