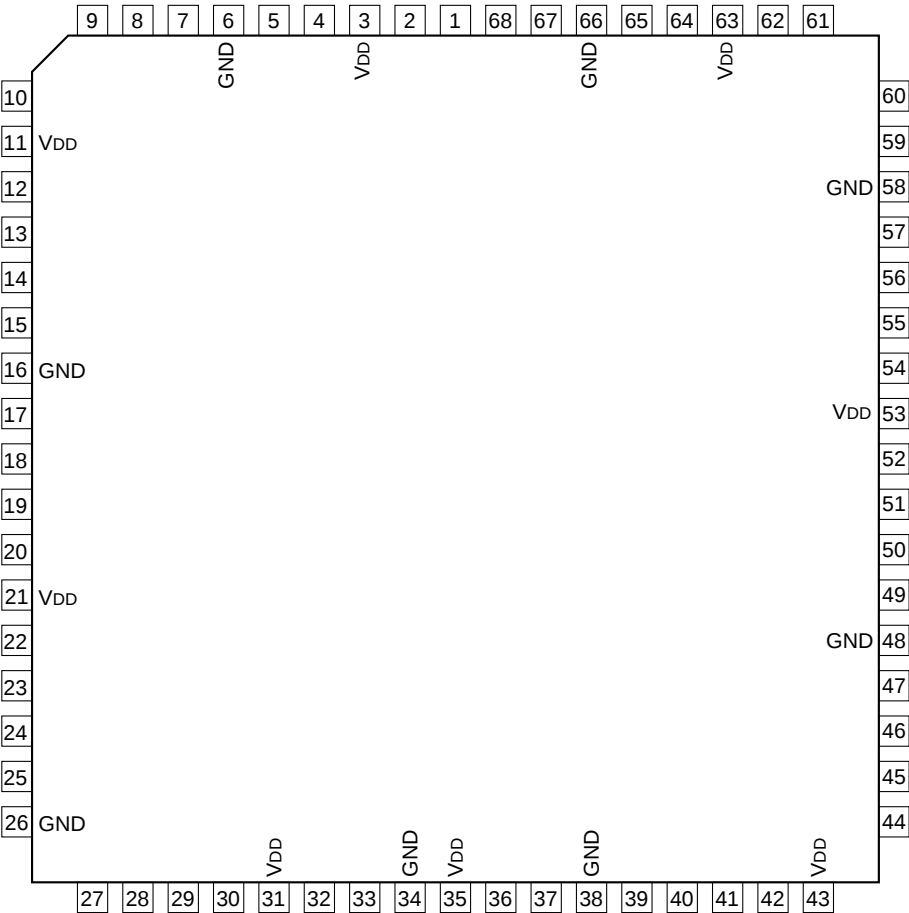


FIELD PROGRAMMABLE LOGIC CIRCUIT

—TOP VIEW—



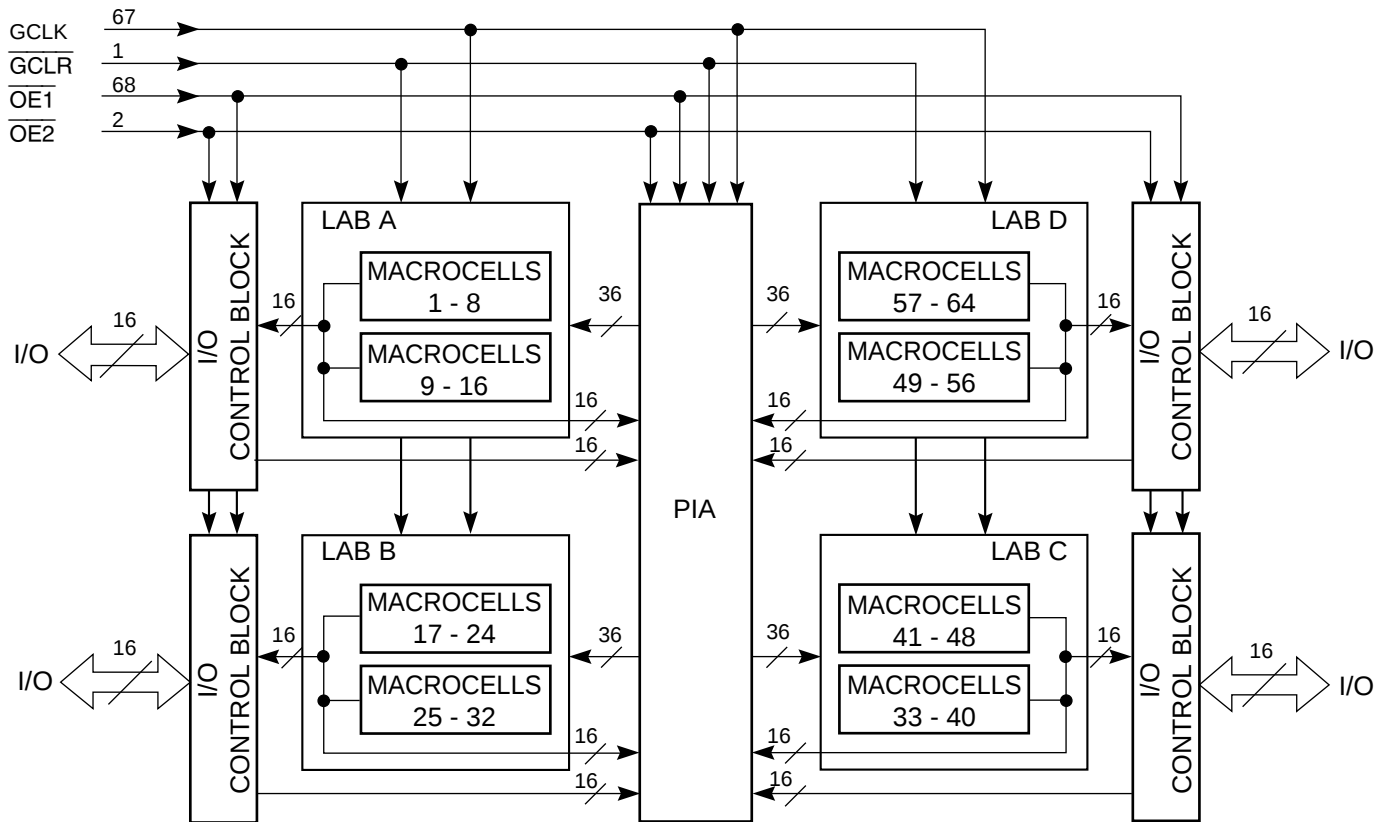
PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	GCLR	18	I/O	I/O	35	—	VDD	52	I/O	I/O
2	I	OE2	19	I/O	I/O	36	I/O	I/O	53	—	VDD
3	—	VDD	20	I/O	I/O	37	I/O	I/O	54	I/O	I/O
4	I/O	I/O	21	—	VDD	38	—	GND	55	I/O	I/O
5	I/O	I/O	22	I/O	I/O	39	I/O	I/O	56	I/O	I/O
6	—	GND	23	I/O	I/O	40	I/O	I/O	57	I/O	I/O
7	I/O	I/O	24	I/O	I/O	41	I/O	I/O	58	—	GND
8	I/O	I/O	25	I/O	I/O	42	I/O	I/O	59	I/O	I/O
9	I/O	I/O	26	—	GND	43	—	VDD	60	I/O	I/O
10	I/O	I/O	27	I/O	I/O	44	I/O	I/O	61	I/O	I/O
11	—	VDD	28	I/O	I/O	45	I/O	I/O	62	I/O	I/O
12	I/O	I/O	29	I/O	I/O	46	I/O	I/O	63	—	VDD
13	I/O	I/O	30	I/O	I/O	47	I/O	I/O	64	I/O	I/O
14	I/O	I/O	31	—	VDD	48	—	GND	65	I/O	I/O
15	I/O	I/O	32	I/O	I/O	49	I/O	I/O	66	—	GND
16	—	GND	33	I/O	I/O	50	I/O	I/O	67	I	GCLK
17	I/O	I/O	34	—	GND	51	I/O	I/O	68	I	OE1

INPUTS

GCLK : GLOBAL CLOCK
 GCLR : GLOBAL CLEAR
 OE1, OE2 : OUTPUT ENABLE 1, 2

INPUTS/OUTPUTS

I/O : DATA



*ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.