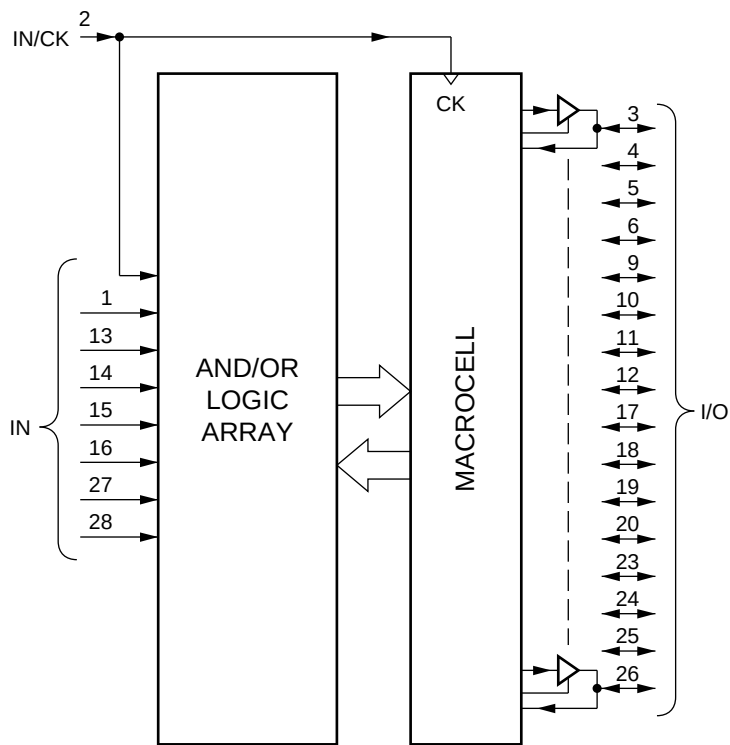
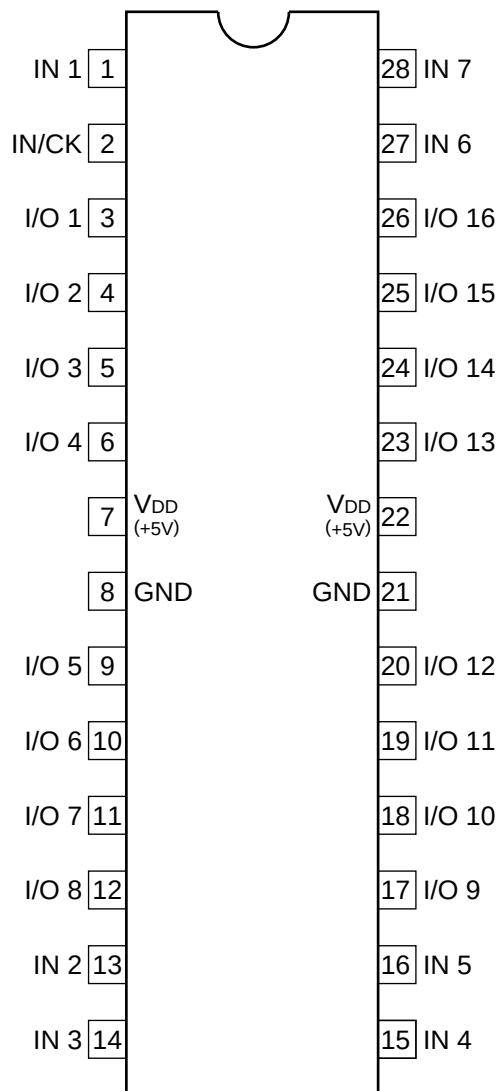

C-MOS ERASABLE PLD

-TOP VIEW-



*ABOVE DIAGRAM SHOWS CONDITION BEFORE PROGRAMMING.