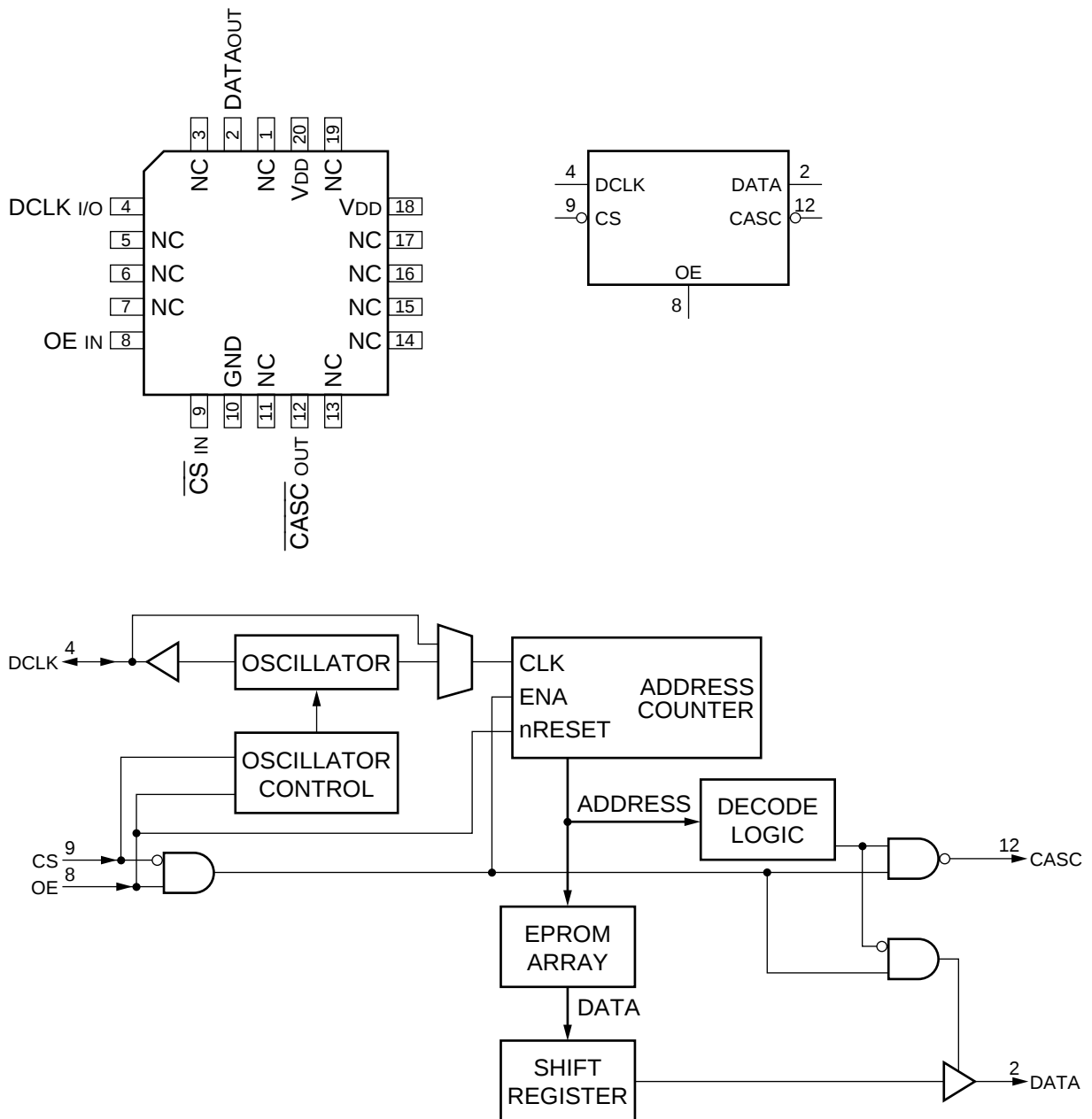


C-MOS SERIAL EPROM

—TOP VIEW—

**INPUT**

$\overline{\text{CS}}$: CHIP SELECT
 OE : OUTPUT ENABLE

INPUT/OUTPUT

DCLK : CLOCK

OUTPUT

$\overline{\text{CASC}}$: CASCADE SELECT