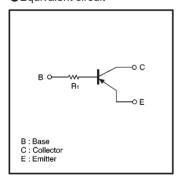
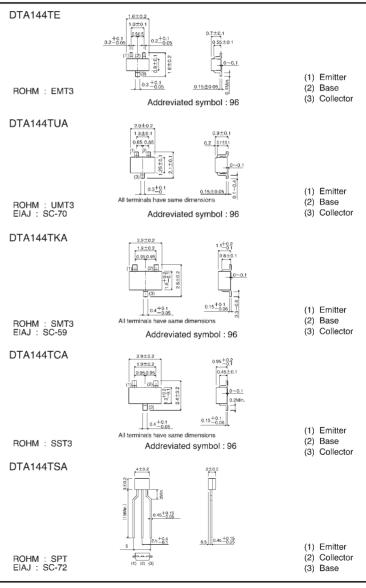
Digital transistors (built in resistor) DTA144TE / DTA144TUA / DTA144TKA / DTA144TCA / DTA144TSA

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thinfilm resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.
- StructurePNP digital transistor(Built-in resistor type)
- Equivalent circuit



External dimensions (Units: mm)



(96-283-A144T)

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits(DTA144T□)					
		E	UA	KA	CA	SA	Unit
Collector-base voltage	Vсво	-50					V
Collector-emitter voltage	VCEO	-50					
Emitter-base voltage	VEBO	-5					
Collector current	lc	-100					
Collector power dissipation	Pc	150	20	00	300	300	mW
Junction temperature	Tj	150					
Storage temperature	Tstg	−55∼ +150					

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	_	_	٧	Ic=-50 μ A
Collector-emitter breakdown voltage	BVcEo	-50	_	_	٧	Ic=-1mA
Emitter-base breakdown voltage	ВУЕВО	-5	_	_	V	Iε=−50 μ A
Collector cutoff current	Ісво	_	_	-0.5	μΑ	V _{CB} =-50V
Emitter cutoff current	ІЕВО	_	_	-0.5	μΑ	V _{EB} =-4V
Collector-emitter saturation voltage	VCE(sat)	_	_	-0.3	V	Ic/I _B =-5mA/-0.5mA
DC current transfer ratio	hre	100	250	600	_	VcE=-5V, Ic=-1mA
Input resistance	R ₁	32.9	47	61.1	kΩ	_
Transition frequency	fτ	_	250	_	MHz	VcE=-10V, IE=5mA, f=100MHz *

^{*} Transition frequency of the device

Packaging specifications

	Package	EMT3	UMT3	SMT3	SST3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	TL	T106	T146	T116	TP
Part No.	Basic ordering unit (pieces)	3000	3000	3000	3000	5000
DTA144TE		0	_	_	_	
DTA144TUA	4	_	0	_	_	_
DTA144TKA	1	_	_	0	_	_
DTA144TCA		_	_	_	0	_
DTA144TSA		_	_	_	_	0

Electrical characteristic curves

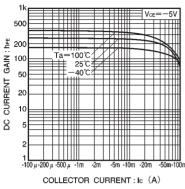


Fig.1 DC current gain vs.collector current

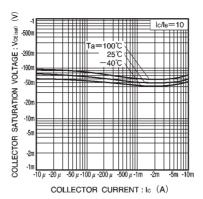


Fig.2 Collector-emitter saturation voltage vs.collector current