

**SANYO****DRA03T**

Silicon Planar Type

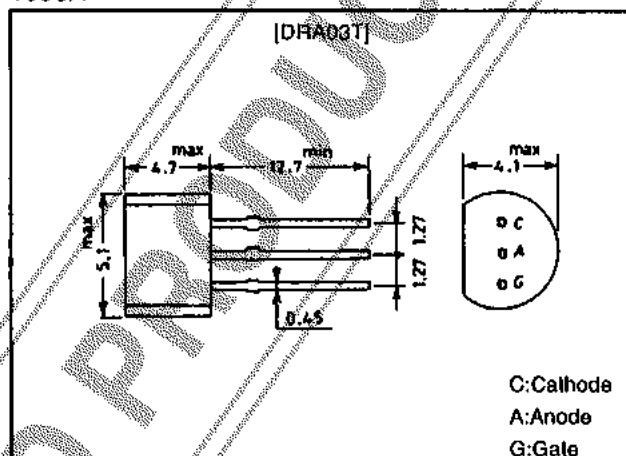
**0.3A Reverse Blocking Thyristor****Features**

- Low-power switching and control.
- Small size and light weight.
- Peak OFF-state (reverse) voltage : 100 to 600V.
- Average ON-state current : 0.3A.
- TO-92 package.

**Package Dimensions**

unit:mm

1096A

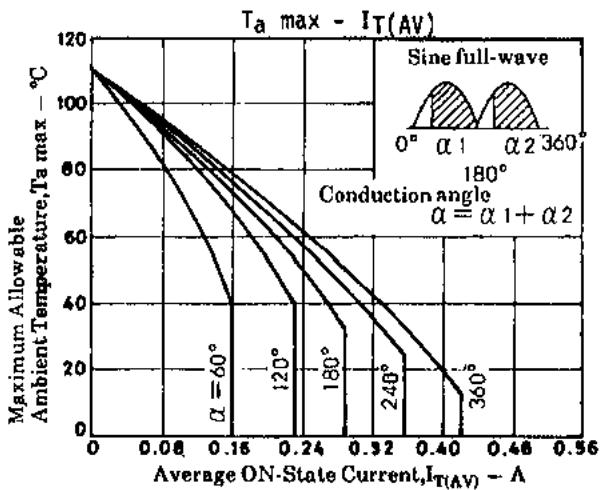
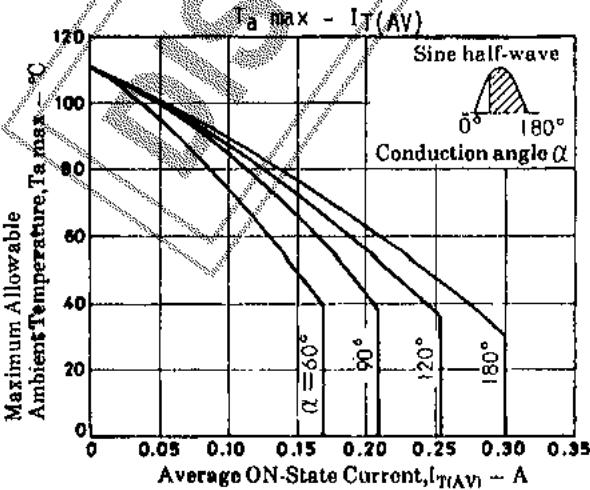
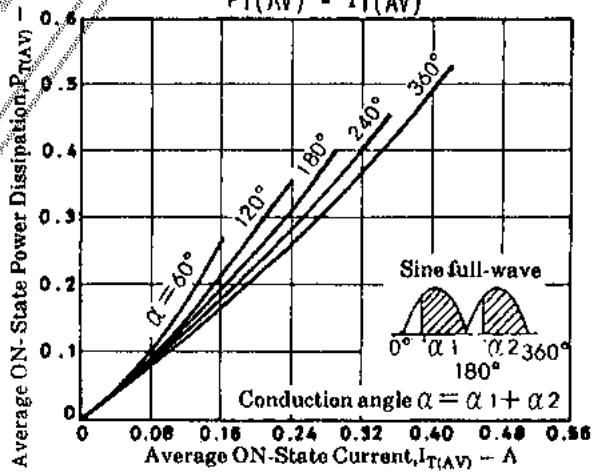
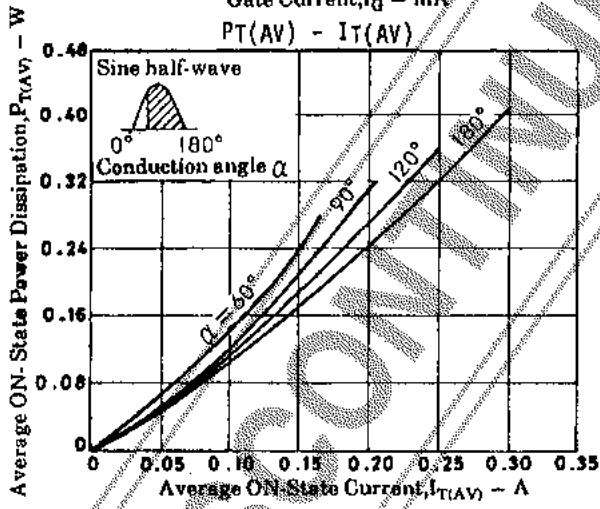
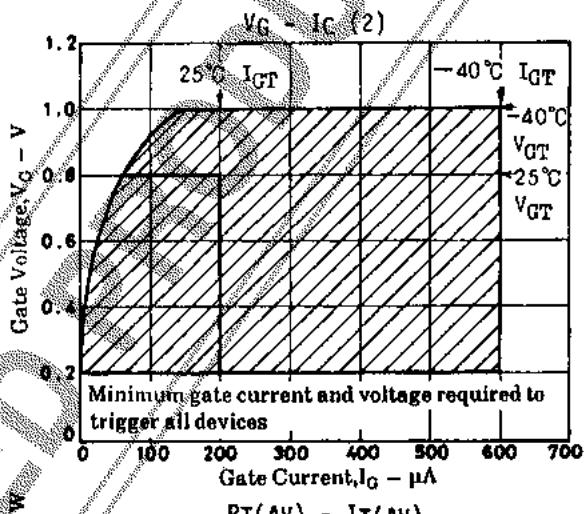
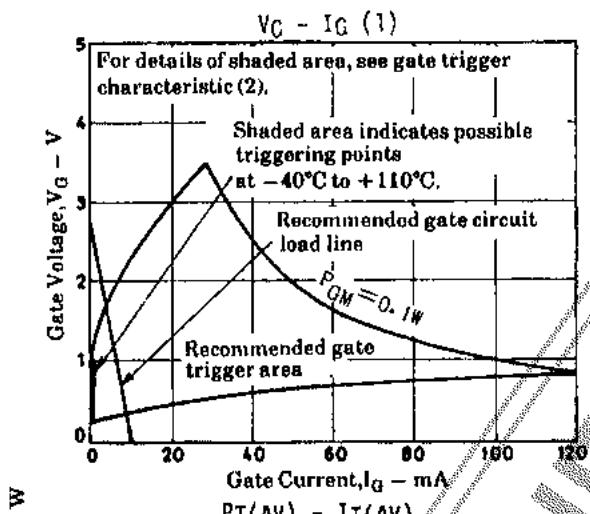
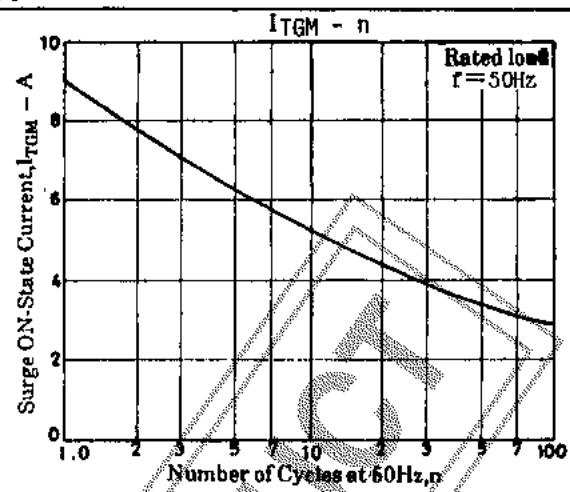
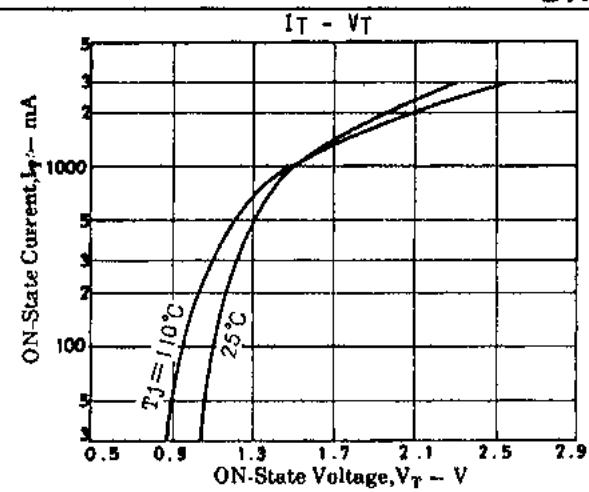
**Specifications****Absolute Maximum Ratings at Ta = 25°C**

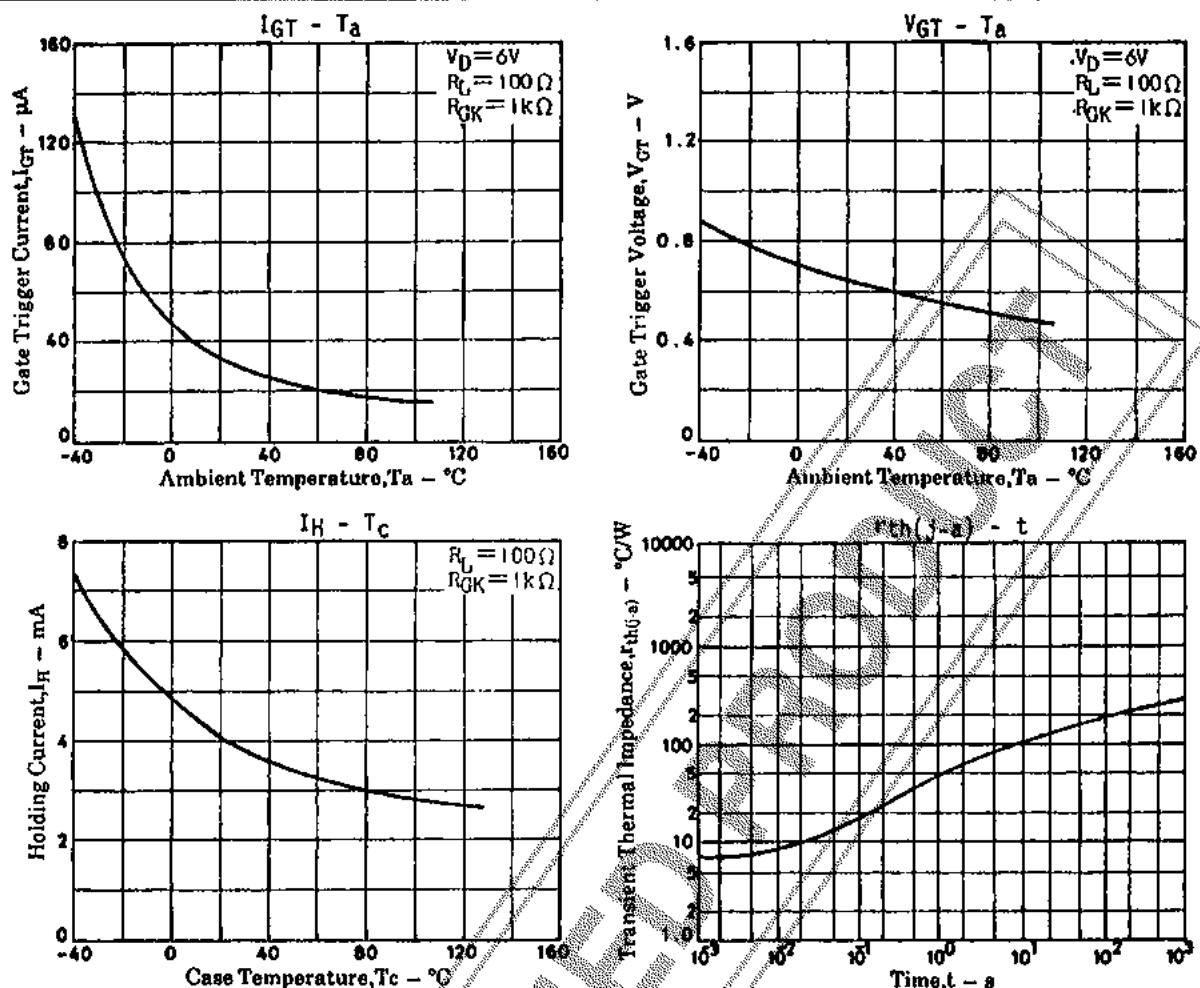
Parameter	Symbol	Conditions	DRA03TB	DRA03TC	DRA03TE	DRA03TG	Unit
Repetitive Peak OFF-State Voltage	V <sub>DRM</sub>	R <sub>GK</sub> =1Ω	100	200	400	600	V
Non-Repetitive Peak Reverse Voltage	V <sub>RSM</sub>	R <sub>GK</sub> =1Ω	150	300	500	720	V
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	R <sub>GK</sub> =1Ω	100	200	400	600	V
Average ON-State Current	I <sub>(AV)</sub>	T <sub>a</sub> =30°C, single phase half-wave	→	→	→	0.3	A
RMS ON-State Current	I <sub>T(RMS)</sub>		→	→	→	0.45	A
Surge ON-State Current	I <sub>TSM</sub>	Sine half-wave, 1 cycle, 50Hz	→	→	→	9	A
Amperes Squared-Seconds	I <sup>2</sup> T-di	1ms≤t≤10ms	→	→	→	0.4	A <sup>2</sup> S
Peak Gate Power Dissipation	P <sub>GM</sub>	f≥50Hz, dutys≤10%	→	→	→	0.1	W
Average Gate Power Dissipation	P <sub>G(AV)</sub>		→	→	→	0.01	W
Peak Gate Forward Current	I <sub>FGM</sub>	f≥50Hz, dutys≤10%	→	→	→	0.125	A
Peak Gate Forward Voltage	V <sub>FGM</sub>	f≥50Hz, dutys≤10%	→	→	→	3.5	V
Peak Gate Reverse Voltage	V <sub>RGM</sub>		→	→	→	5	V
Junction Temperature	T <sub>j</sub>		→	→	→	110	°C
Storage Temperature	T <sub>stg</sub>		→	→	→	-40 to +125	°C
Weight			→	→	→	0.2	g

**Electrical Characteristics at Ta = 25°C**

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Repetitive Peak OFF-State Current	I <sub>DRM</sub>	T <sub>j</sub> =110°C, V <sub>D</sub> =V <sub>DRM</sub> , R <sub>GK</sub> =1kΩ			0.1	mA
Repetitive Peak Reverse Current	I <sub>RRM</sub>	T <sub>j</sub> =110°C, V <sub>R</sub> =V <sub>RRM</sub> , R <sub>GK</sub> =1kΩ			0.1	mA
ON-State Voltage	V <sub>T</sub>	I <sub>T</sub> =2A			2.0	V
Holding Current	I <sub>H</sub>	R <sub>L</sub> =100Ω, R <sub>GK</sub> =1kΩ		4.0		mA
Gate Trigger Current	I <sub>GT</sub>	V <sub>D</sub> =6V, R <sub>L</sub> =100Ω, R <sub>GK</sub> =1kΩ			0.2	mA
Gate Trigger Voltage	V <sub>GT</sub>	V <sub>D</sub> =6V, R <sub>L</sub> =100Ω, R <sub>GK</sub> =1kΩ			0.8	V
Gate Nontrigger Voltage	V <sub>GD</sub>	T <sub>a</sub> =110°C, V <sub>D</sub> =6V, R <sub>GK</sub> =1kΩ	0.2			V
Thermal Resistance	R <sub>th(j-c)</sub>				250	°C/W

# DRA03T





- No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.
- Anyone purchasing any products described or contained herein for an above-mentioned use shall:
- (1) Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use;
  - (2) Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.
- Information (including circuit diagrams and circuit parameters) herein is for example only; It is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of May, 1998. Specifications and information herein are subject to change without notice.