

DG646BH25

GATE TURN-OFF THYRISTOR

APPLICATIONS

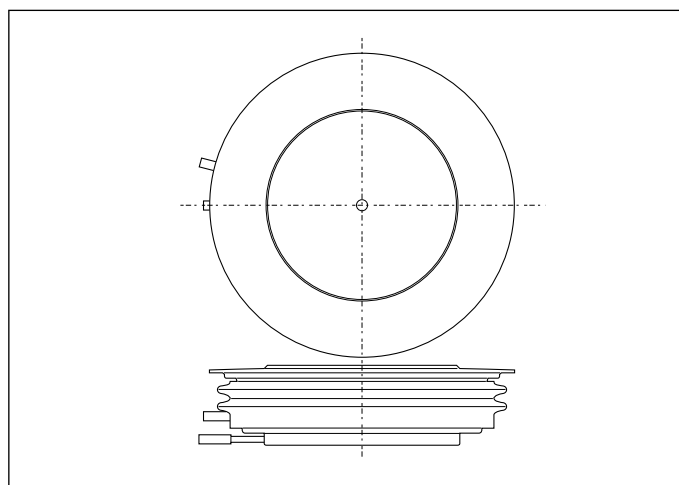
- Variable speed A.C. motor drive inverters (VSD-AC).
- Uninterruptable Power Supplies
- High Voltage Converters.
- Choppers.
- Welding.
- Induction Heating.
- DC/DC Converters.

FEATURES

- Double Side Cooling.
- High Reliability In Service.
- High Voltage Capability.
- Fault Protection Without Fuses.
- High Surge Current Capability.
- Turn-off Capability Allows Reduction In Equipment Size And Weight. Low Noise Emission Reduces Acoustic Cladding Necessary For Environmental Requirements.

KEY PARAMETERS

I_{TCM}	2000A
V_{DRM}	2500V
$I_{T(AV)}$	867A
dV_D/dt	1000V/ μ s
di_T/dt	300A/ μ s



Outline type code: H. Turn to page 18 for further information.

VOLTAGE RATINGS

Type Number	Repetitive Peak Off-state Voltage V_{DRM} V	Repetitive Peak Reverse Voltage V_{RRM} V	Conditions
DG646BH25	2500	16	$T_{vj} = 125^{\circ}\text{C}$, $I_{DM} = 50\text{mA}$, $I_{RRM} = 50\text{mA}$

CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
I_{TCM}	Repetitive peak controllable on-state current	$V_D = V_{DRM}$, $T_j = 125^{\circ}\text{C}$, $di_{GQ}/dt = 40\text{A}/\mu\text{s}$, $C_s = 2.0\mu\text{F}$	2000	A
$I_{T(AV)}$	Mean on-state current	$T_{HS} = 80^{\circ}\text{C}$. Double side cooled. Half sine 50Hz.	867	A
$I_{T(RMS)}$	RMS on-state current	$T_{HS} = 80^{\circ}\text{C}$. Double side cooled. Half sine 50Hz.	1360	A

SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I_{TSM}	Surge (non-repetitive) on-state current	10ms half sine. $T_j = 125^\circ\text{C}$	18.0	kA
I^2t	I^2t for fusing	10ms half sine. $T_j = 125^\circ\text{C}$	1.62×10^6	A^2s
di_T/dt	Critical rate of rise of on-state current	$V_D = 1500\text{V}$, $I_T = 2000\text{A}$, $T_j = 125^\circ\text{C}$, $I_{FG} > 30\text{A}$, Rise time $> 1.0\mu\text{s}$	300	$\text{A}/\mu\text{s}$
dV_D/dt	Rate of rise of off-state voltage	To 66% V_{DRM} ; $R_{GK} \leq 1.5\Omega$, $T_j = 125^\circ\text{C}$	135	$\text{V}/\mu\text{s}$
		To 66% V_{DRM} ; $V_{RG} = -2\text{V}$, $T_j = 125^\circ\text{C}$	1000	$\text{V}/\mu\text{s}$
L_S	Peak stray inductance in snubber circuit	$I_T = 2000\text{A}$, $V_{DM} = 2500\text{V}$, $T_j = 125^\circ\text{C}$, $di_{GQ}/dt = 40\text{A}/\mu\text{s}$, $C_s = 2.0\mu\text{F}$	200	nH

GATE RATINGS

Symbol	Parameter	Conditions	Min.	Max.	Units
V_{RGM}	Peak reverse gate voltage	This value maybe exceeded during turn-off	-	16	V
I_{FGM}	Peak forward gate current		20	100	A
$P_{FG(AV)}$	Average forward gate power		-	15	W
P_{RGM}	Peak reverse gate power		-	19	kW
di_{GQ}/dt	Rate of rise of reverse gate current		30	60	$\text{A}/\mu\text{s}$
$t_{ON(min)}$	Minimum permissible on time		50	-	μs
$t_{OFF(min)}$	Minimum permissible off time		100	-	μs

THERMAL RATINGS AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
R _{th(j-hs)}	DC thermal resistance - junction to heatsink surface	Double side cooled		-	0.018	°C/W
		Anode side cooled		-	0.03	°C/W
		Cathode side cooled		-	0.045	°C/W
R _{th(c-hs)}	Contact thermal resistance	Clamping force 20.0kN With mounting compound	per contact	-	0.006	°C/W
T _{vj}	Virtual junction temperature			-	125	°C
T _{OP} /T _{stg}	Operating junction/storage temperature range			-40	125	°C
-	Clamping force			18.0	22.0	kN

CHARACTERISTICS

$T_j = 125^\circ\text{C}$ unless stated otherwise					
Symbol	Parameter	Conditions	Min.	Max.	Units
V_{TM}	On-state voltage	At 2000A peak, $I_{G(ON)} = 7\text{A d.c.}$	-	2.6	V
I_{DM}	Peak off-state current	$V_{DRM} = 2500\text{V}$, $V_{RG} = 0\text{V}$	-	100	mA
I_{RRM}	Peak reverse current	At V_{RRM}	-	50	mA
V_{GT}	Gate trigger voltage	$V_D = 24\text{V}$, $I_T = 100\text{A}$, $T_j = 25^\circ\text{C}$	-	1.0	V
I_{GT}	Gate trigger current	$V_D = 24\text{V}$, $I_T = 100\text{A}$, $T_j = 25^\circ\text{C}$	-	3.0	A
I_{RGM}	Reverse gate cathode current	$V_{RGM} = 16\text{V}$, No gate/cathode resistor	-	50	mA
E_{ON}	Turn-on energy	$V_D = 15000\text{V}$	-	1188	mJ
t_d	Delay time	$I_T = 2000\text{A}$, $di_T/dt = 300\text{A}/\mu\text{s}$	-	1.2	μs
t_r	Rise time	$I_{FG} = 30\text{A}$, rise time $< 1.0\mu\text{s}$	-	3.0	μs
E_{OFF}	Turn-off energy	$I_T = 2000\text{A}$, $V_{DM} = 2500\text{V}$ Snubber Cap $C_s = 2.0\mu\text{F}$, $di_{GQ}/dt = 40\text{A}/\mu\text{s}$	-	4000	mJ
t_{gs}	Storage time		-	17.0	μs
t_{gf}	Fall time		-	2.0	μs
t_{gq}	Gate controlled turn-off time		-	19.0	μs
Q_{GQ}	Turn-off gate charge		-	6600	μC
Q_{GQT}	Total turn-off gate charge		-	13200	μC
I_{GQM}	Peak reverse gate current		-	650	A

CURVES

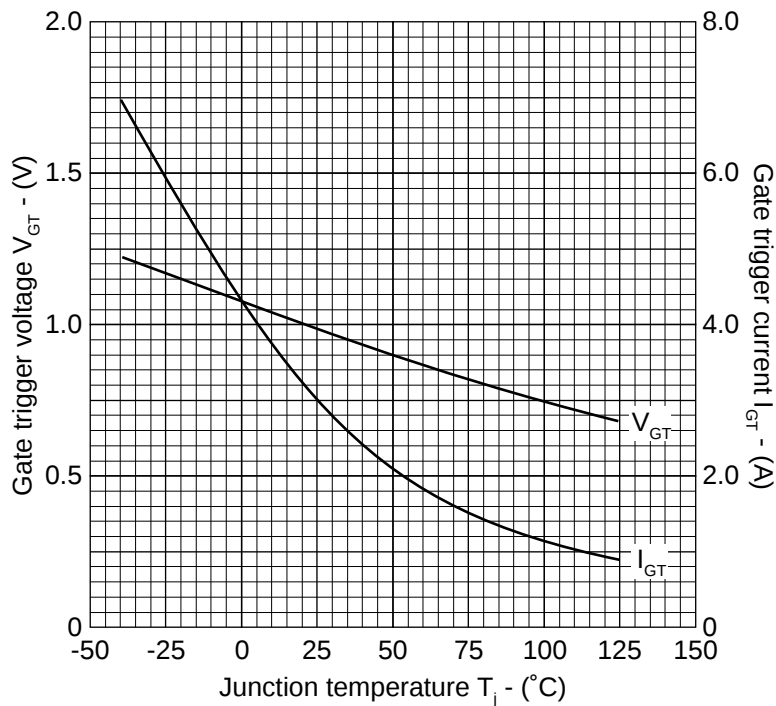


FIG. 1 MAXIMUM GATE TRIGGER VOLTAGE/CURRENT vs JUNCTION TEMPERATURE

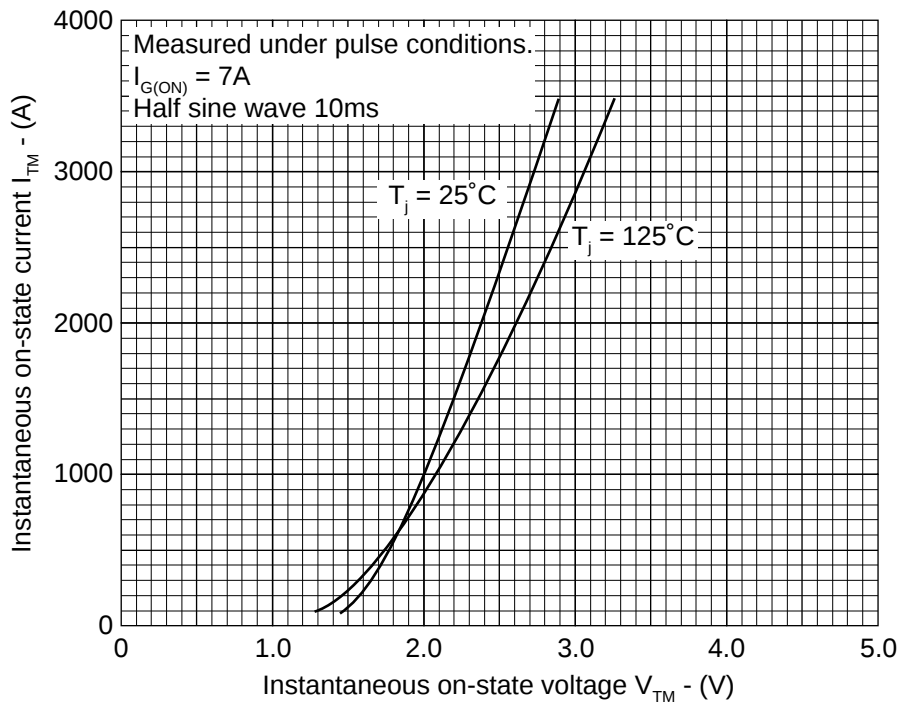


FIG. 2 ON-STATE CHARACTERISTICS

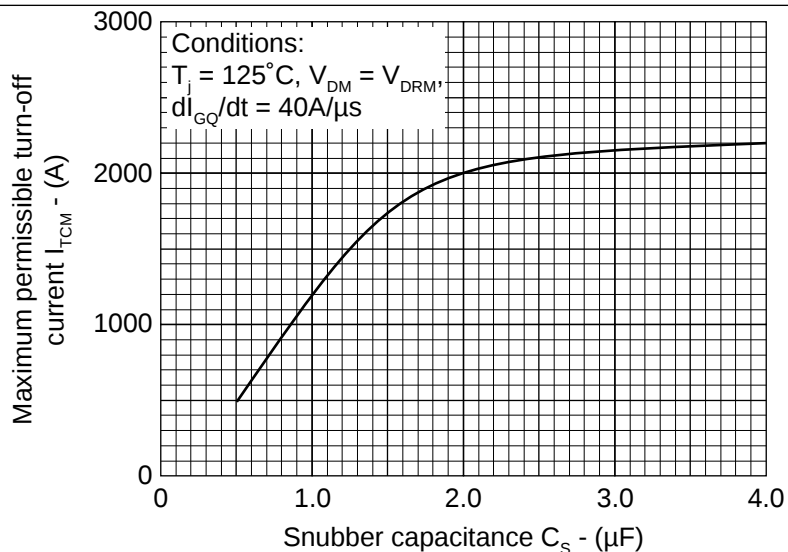


FIG. 3 MAXIMUM DEPENDENCE OF I_{TCM} ON C_S

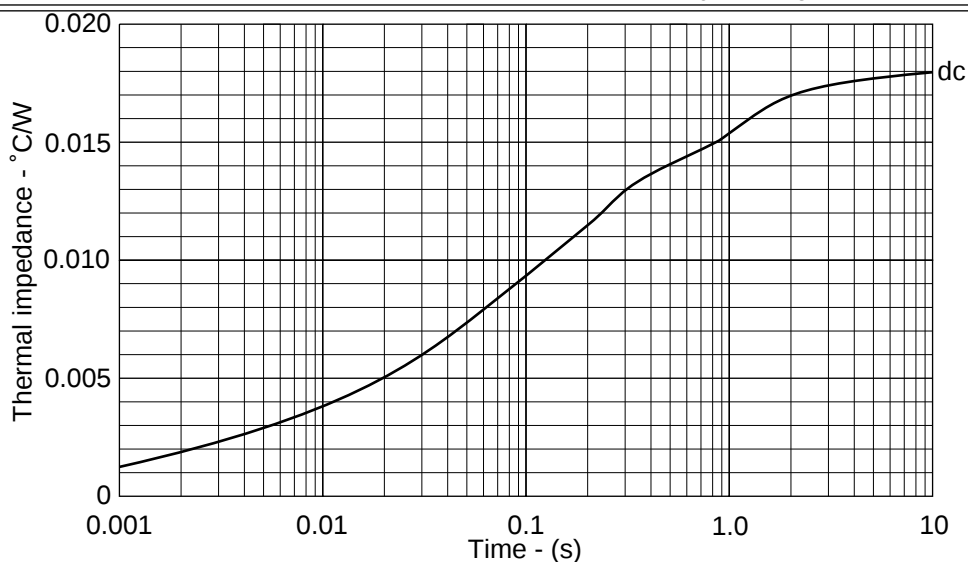


FIG. 4 MAXIMUM (LIMIT) TRANSIENT THERMAL IMPEDANCE - DOUBLE SIDE COOLED

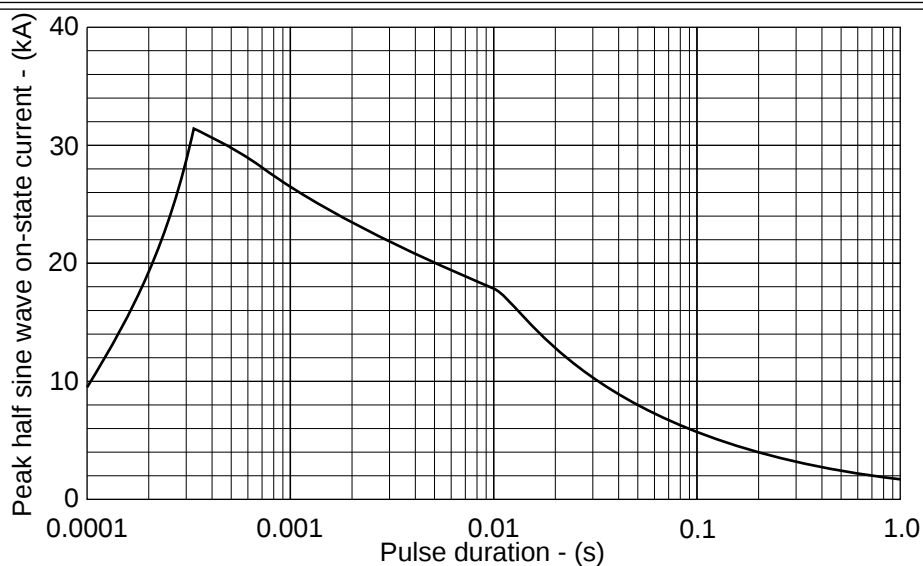
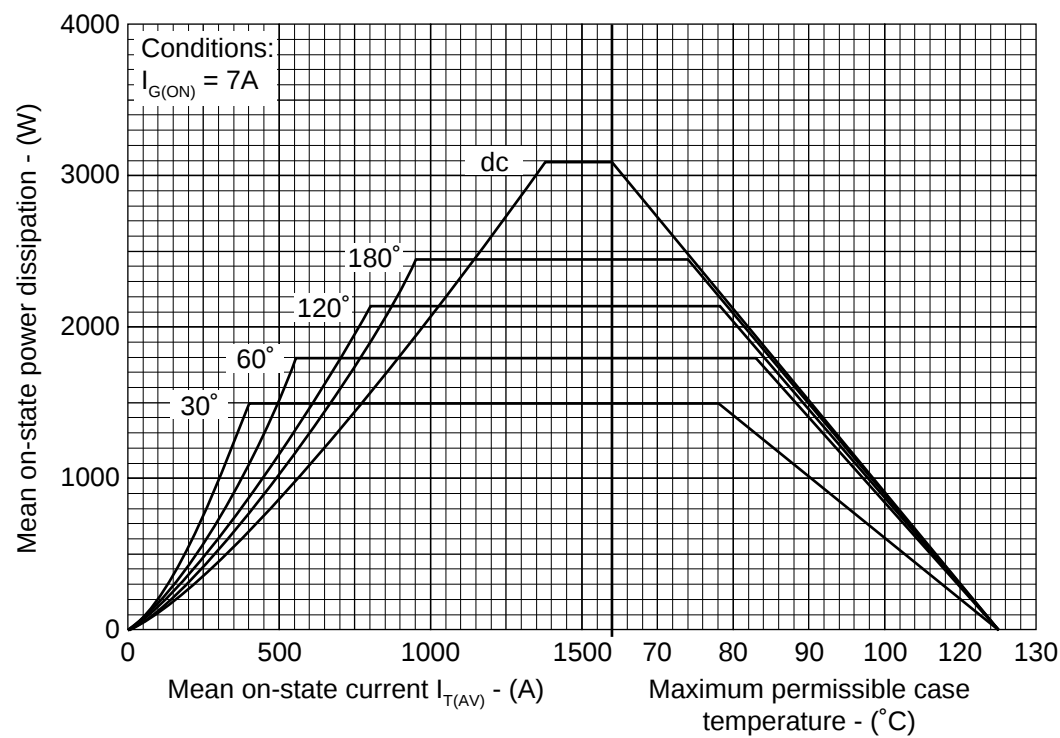
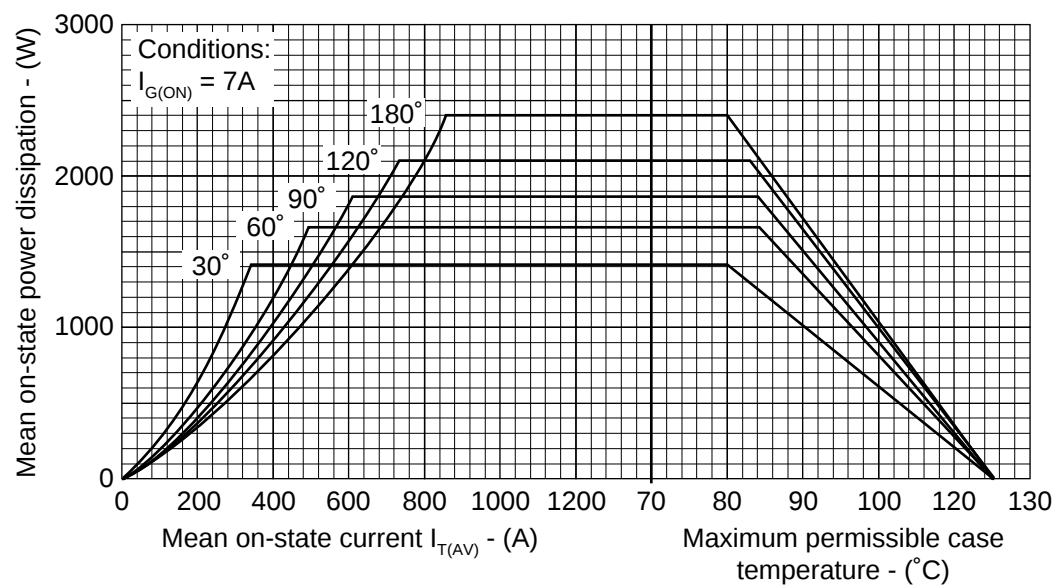


FIG. 5 SURGE (NON-REPETITIVE) ON-STATE CURRENT vs TIME



**FIG. 6 STEADY STATE RECTANGULAR WAVE CONDUCTION LOSS
- DOUBLE SIDE COOLED**



**FIG. 7 STEADY STATE SINUSOIDAL WAVE CONDUCTION LOSS
- DOUBLE SIDE COOLED**

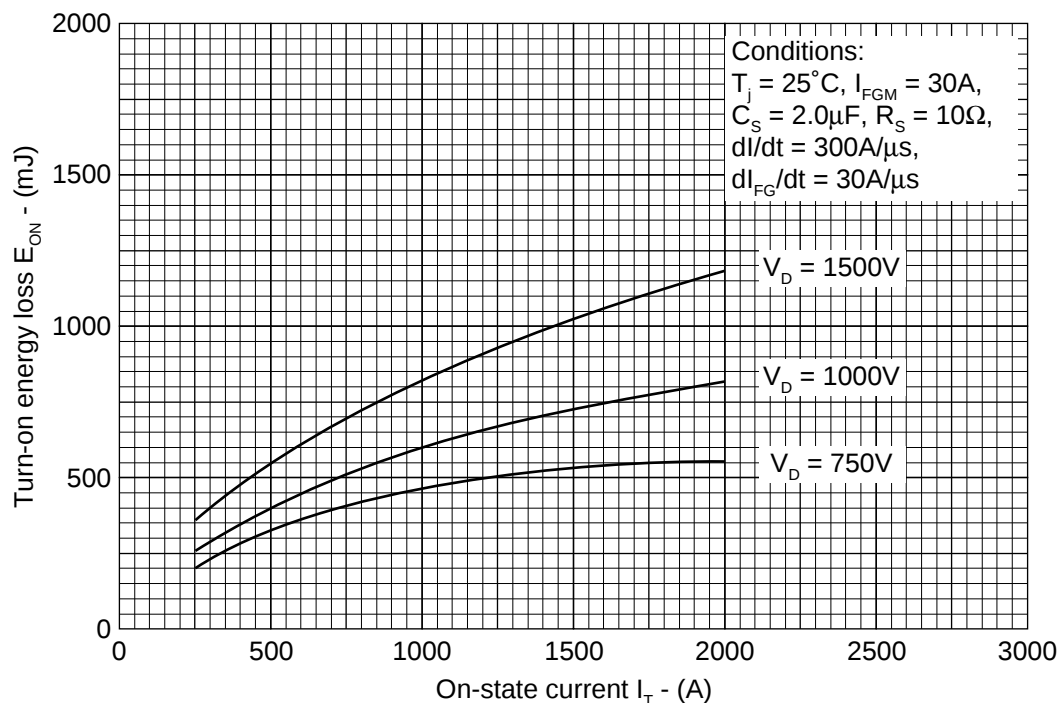


FIG. 8 TURN-ON ENERGY vs ON-STATE CURRENT

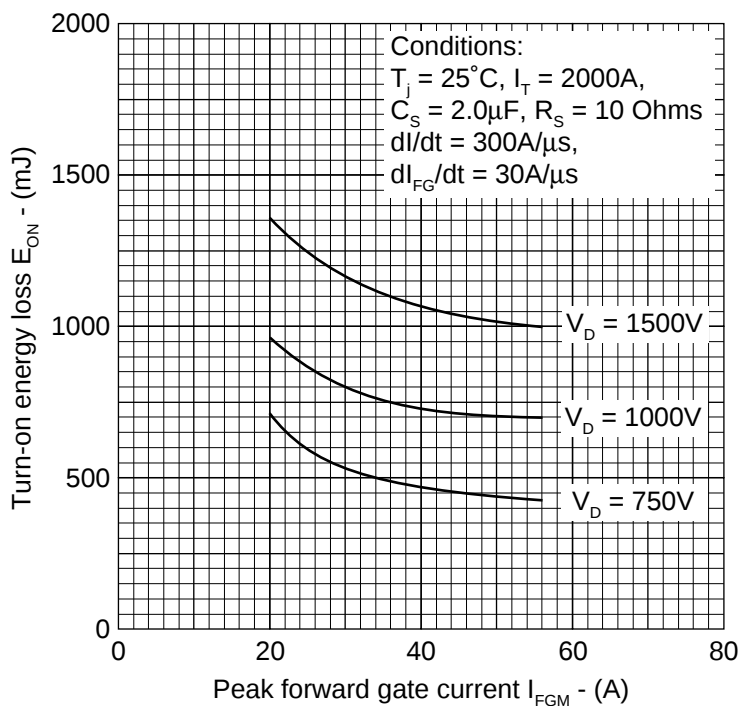


FIG. 9 TURN-ON ENERGY vs PEAK FORWARD GATE CURRENT

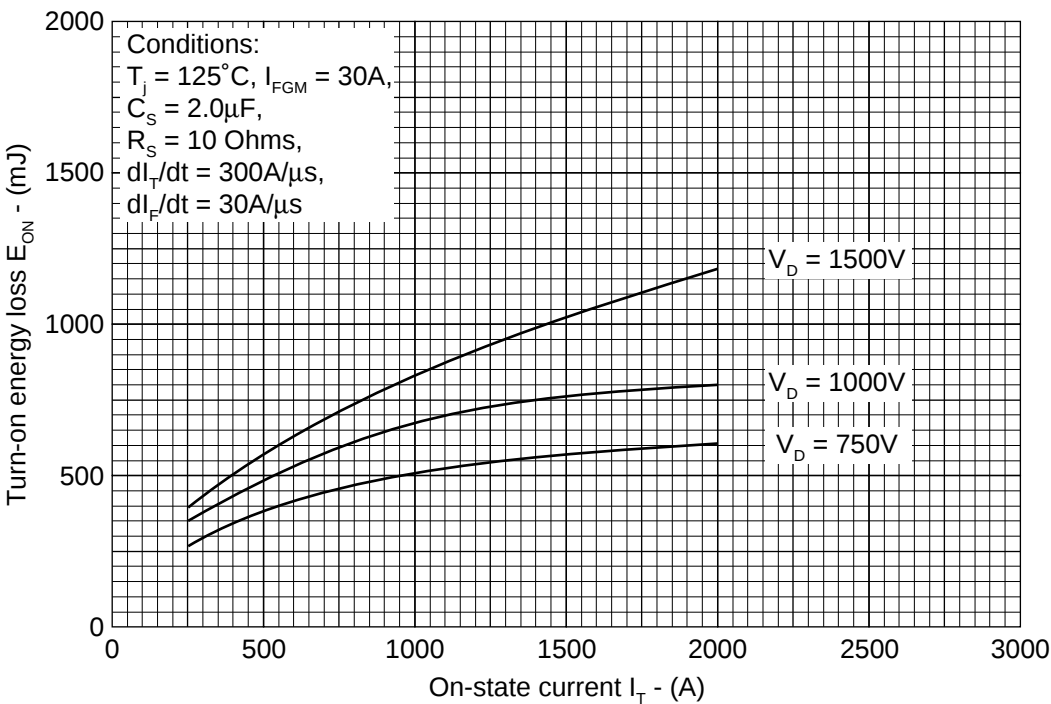


FIG. 10 TURN-ON ENERGY vs ON-STATE CURRENT

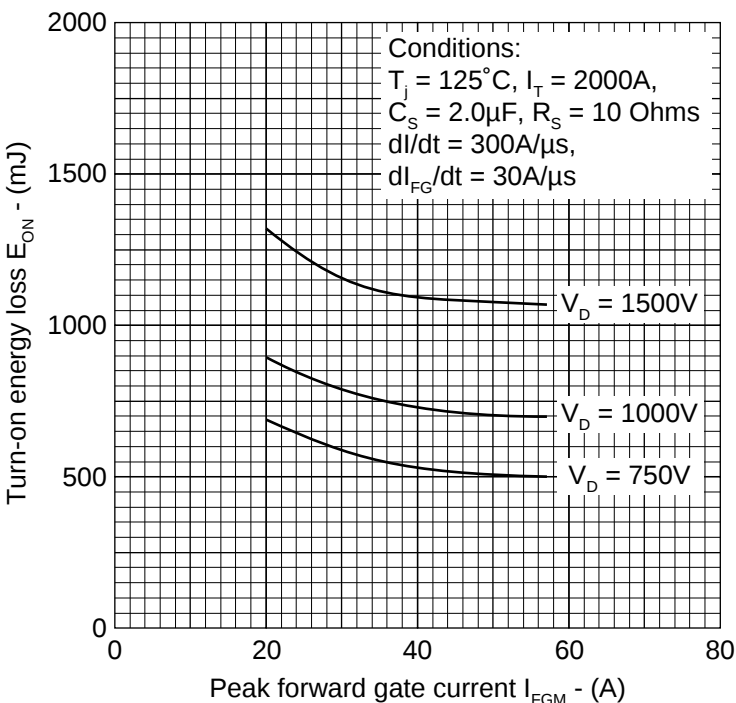


FIG. 11 TURN-ON ENERGY vs PEAK FORWARD GATE CURRENT

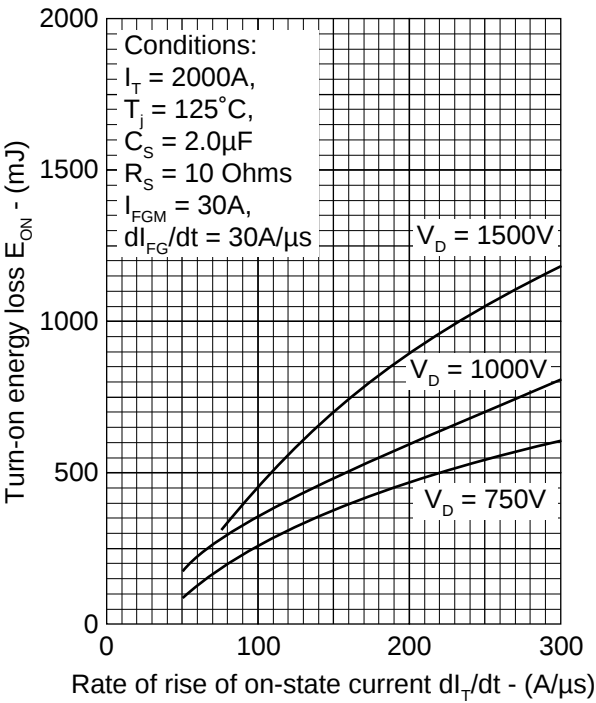


FIG. 12 TURN-ON ENERGY vs RATE OF RISE OF ON-STATE CURRENT

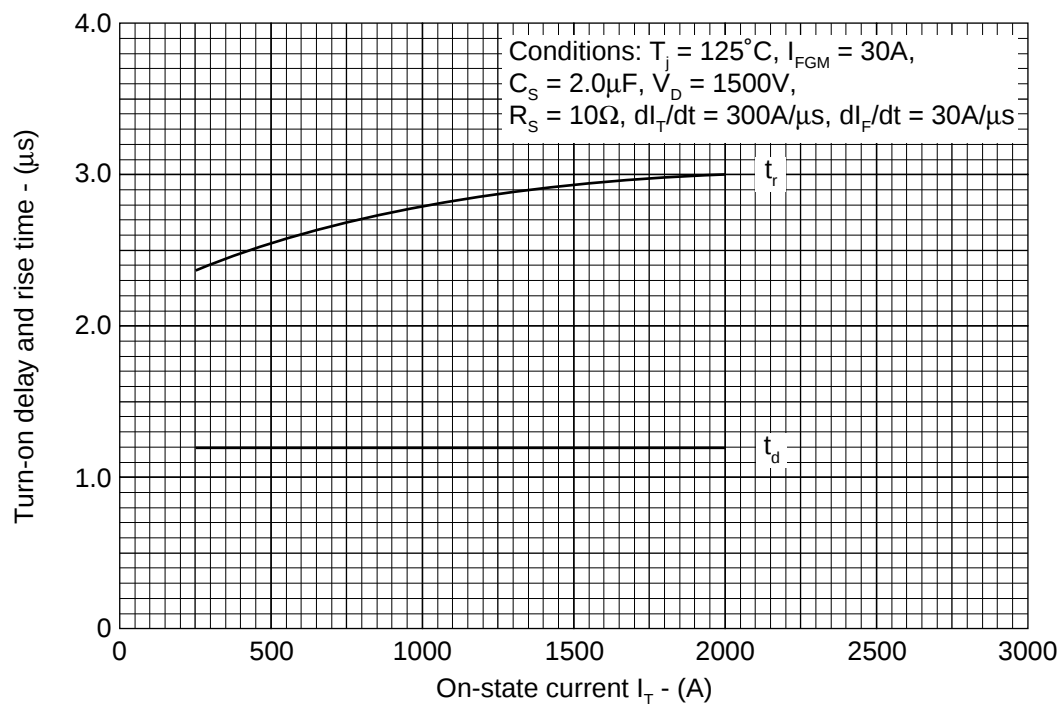


FIG. 13 DELAY & RISE TIME vs TURN-ON CURRENT

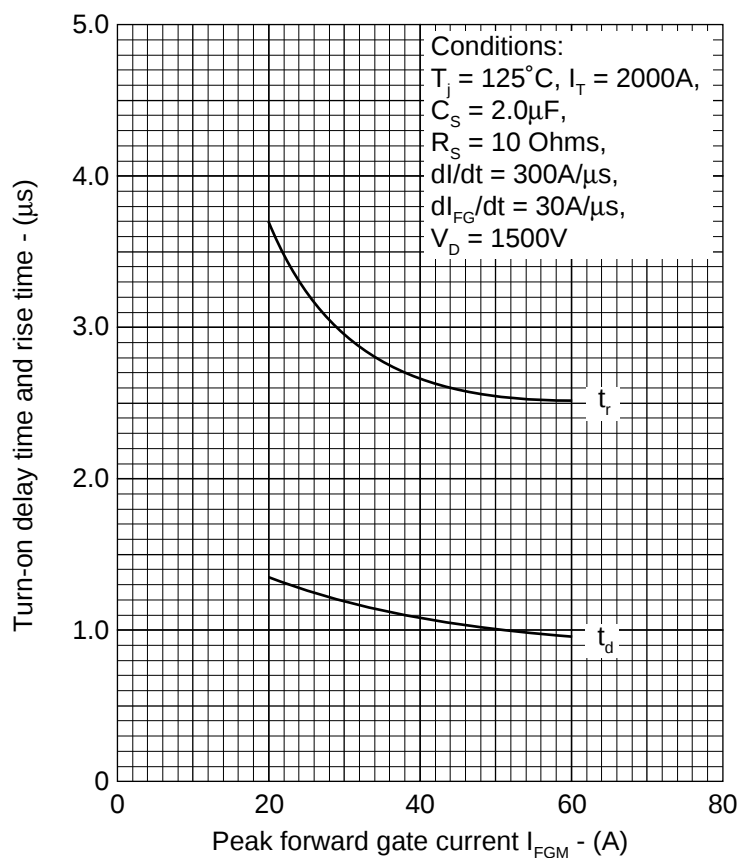


FIG. 14 DELAY TIME & RISE TIME vs PEAK FORWARD GATE CURRENT

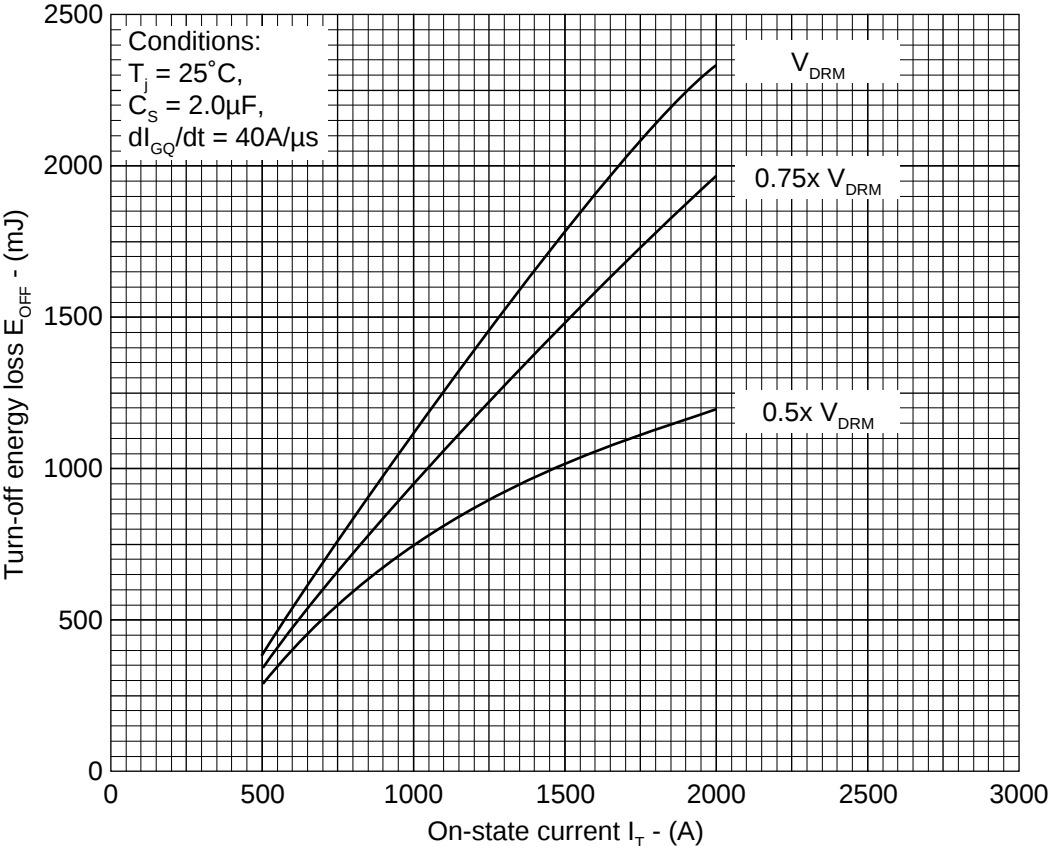


FIG. 15 TURN-OFF ENERGY vs ON-STATE CURRENT

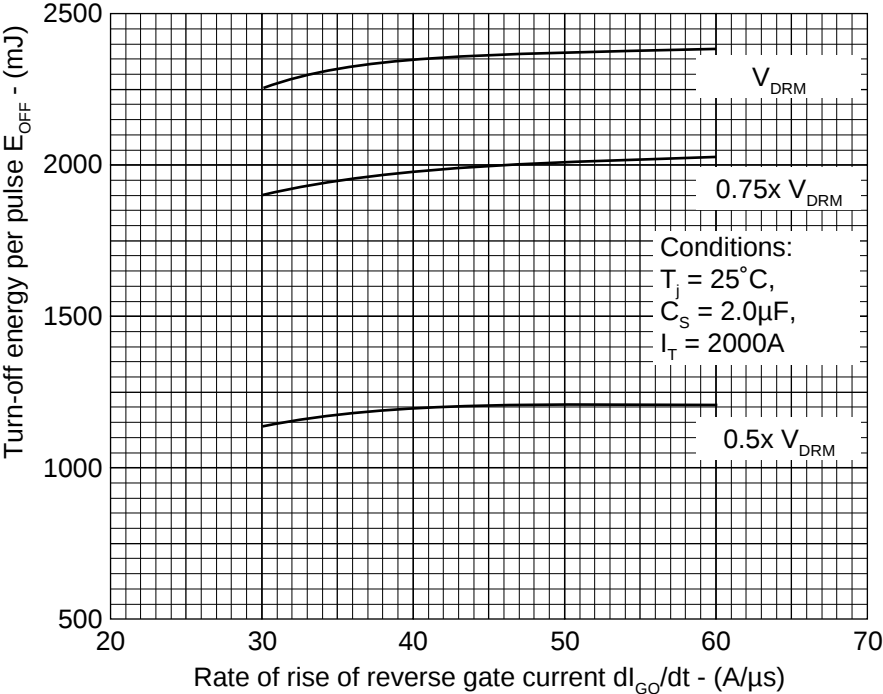


FIG. 16 TURN-OFF ENERGY vs RATE OF RISE OF REVERSE GATE CURRENT

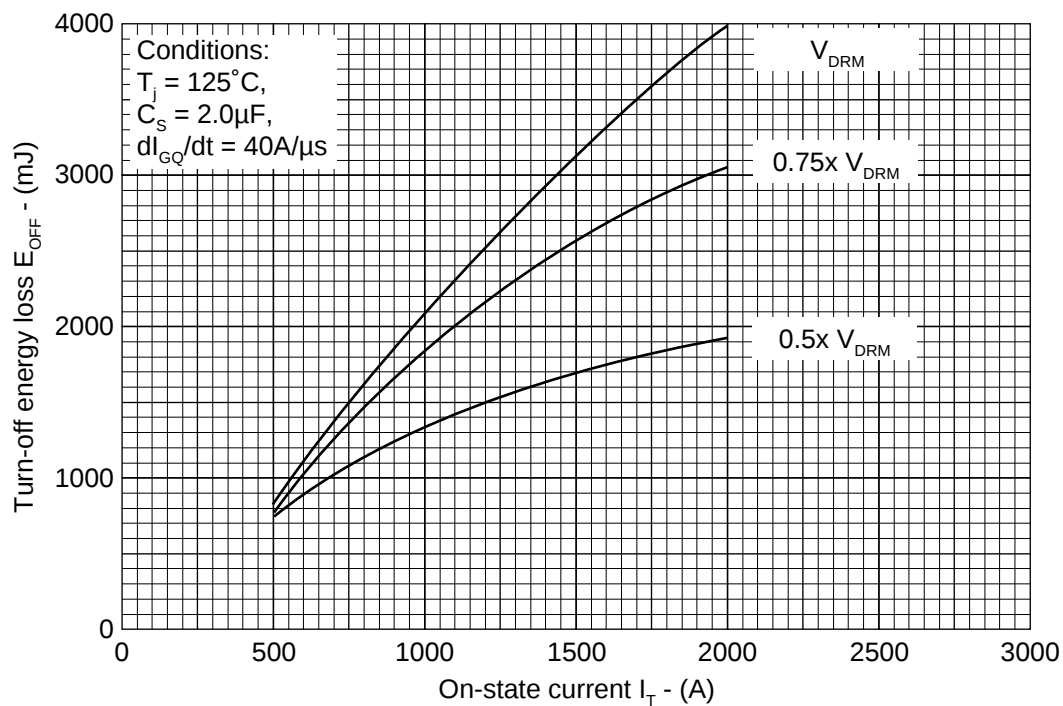


FIG. 17 TURN-OFF ENERGY vs ON-STATE CURRENT

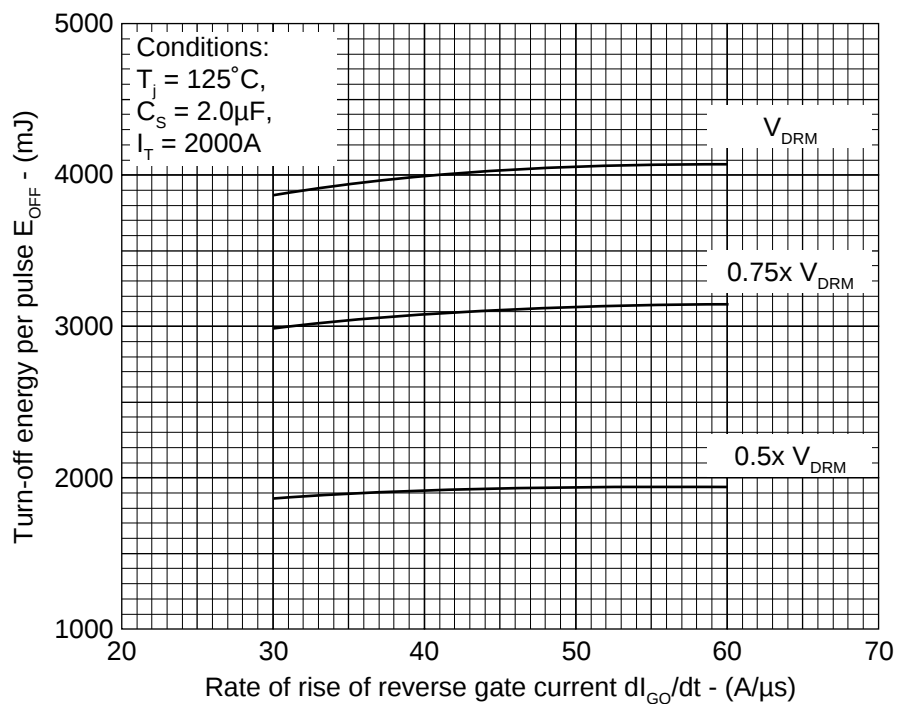


FIG. 18 TURN-OFF ENERGY LOSS vs RATE OF RISE OF REVERSE GATE CURRENT

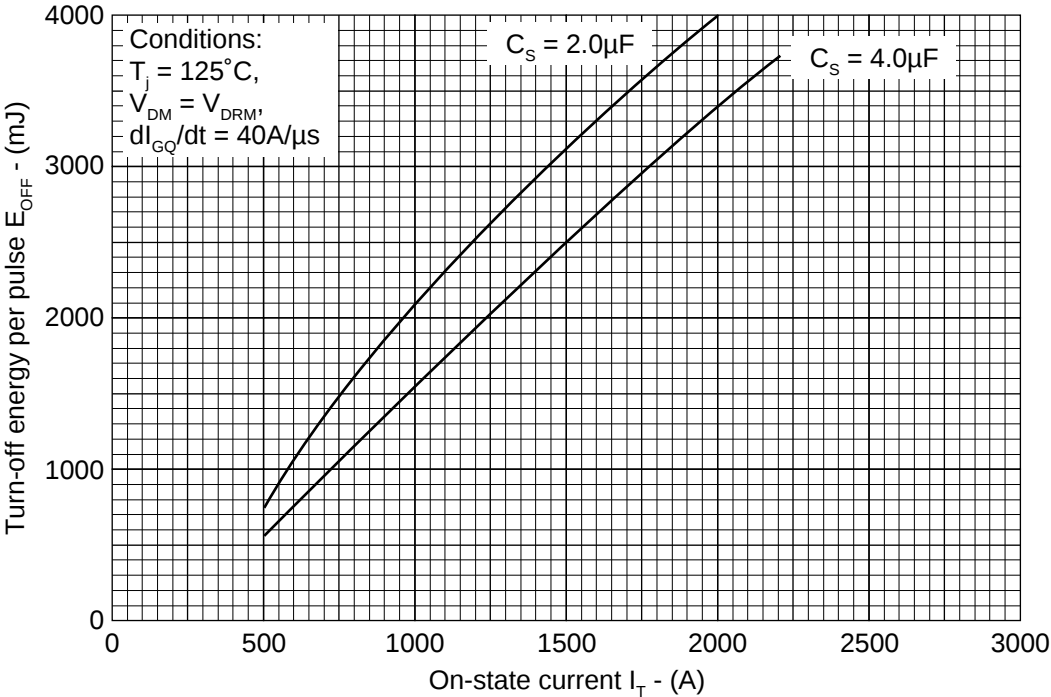


FIG. 19 TURN-OFF ENERGY vs ON-STATE CURRENT

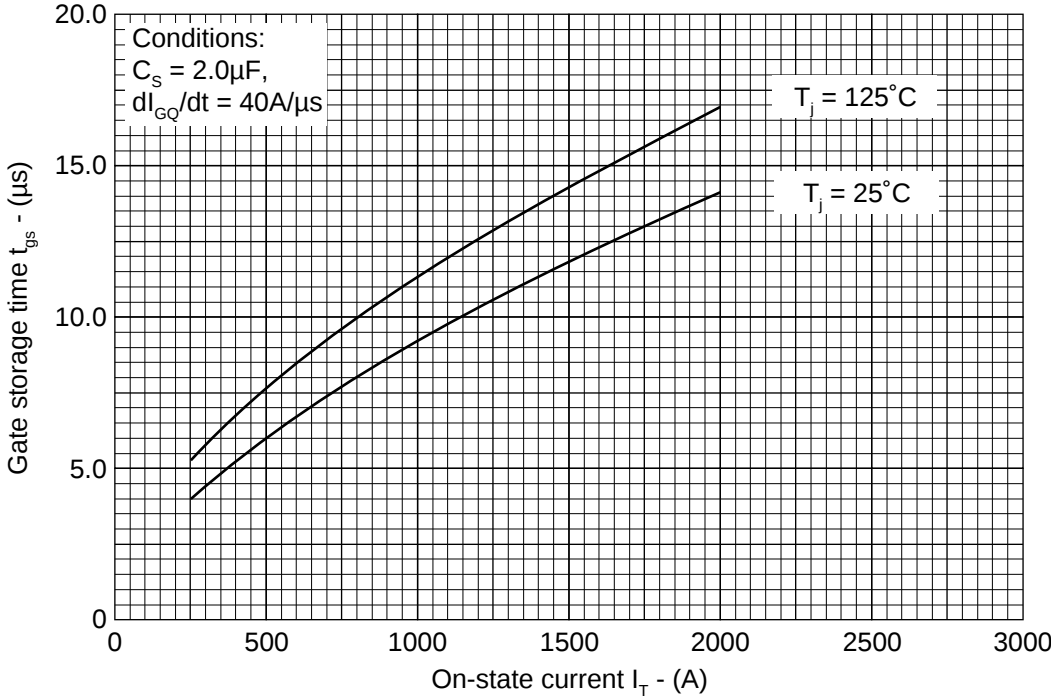


FIG. 20 GATE STORAGE TIME vs ON-STATE CURRENT

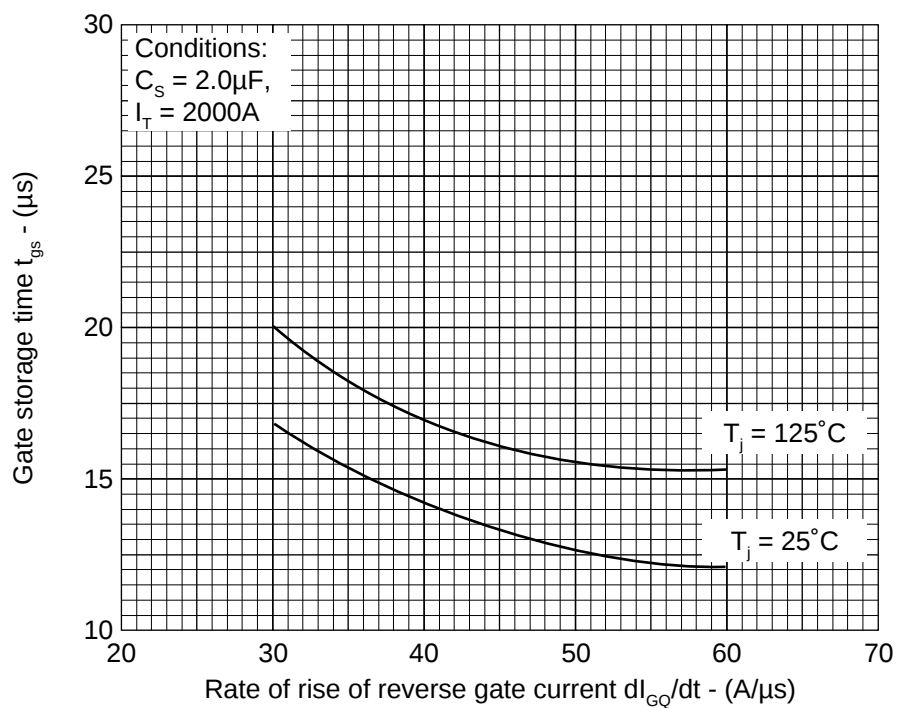


FIG. 21 GATE STORAGE TIME vs RATE OF RISE OF REVERSE GATE CURRENT

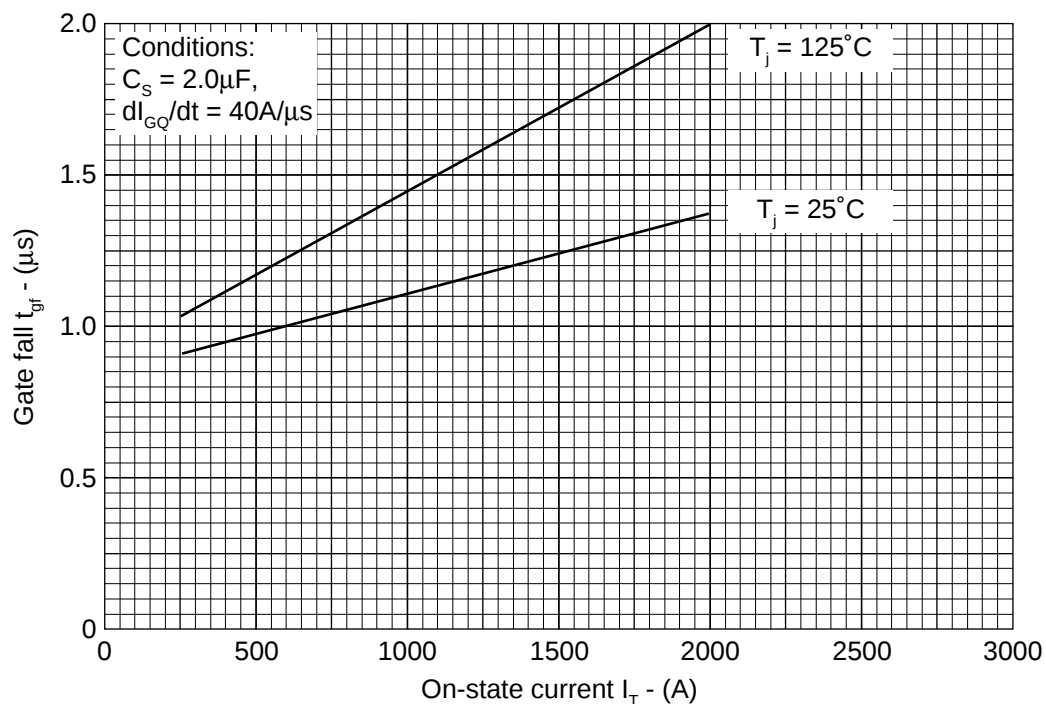


FIG. 22 GATE FALL TIME vs ON-STATE CURRENT

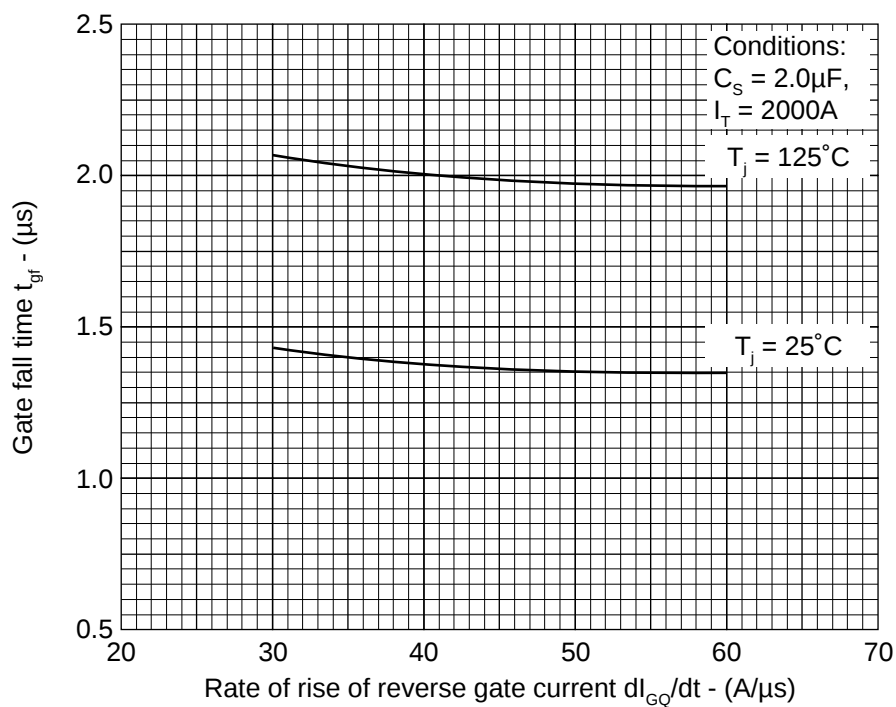


FIG. 23 GATE FALL TIME vs RATE OF RISE OF REVERSE GATE CURRENT

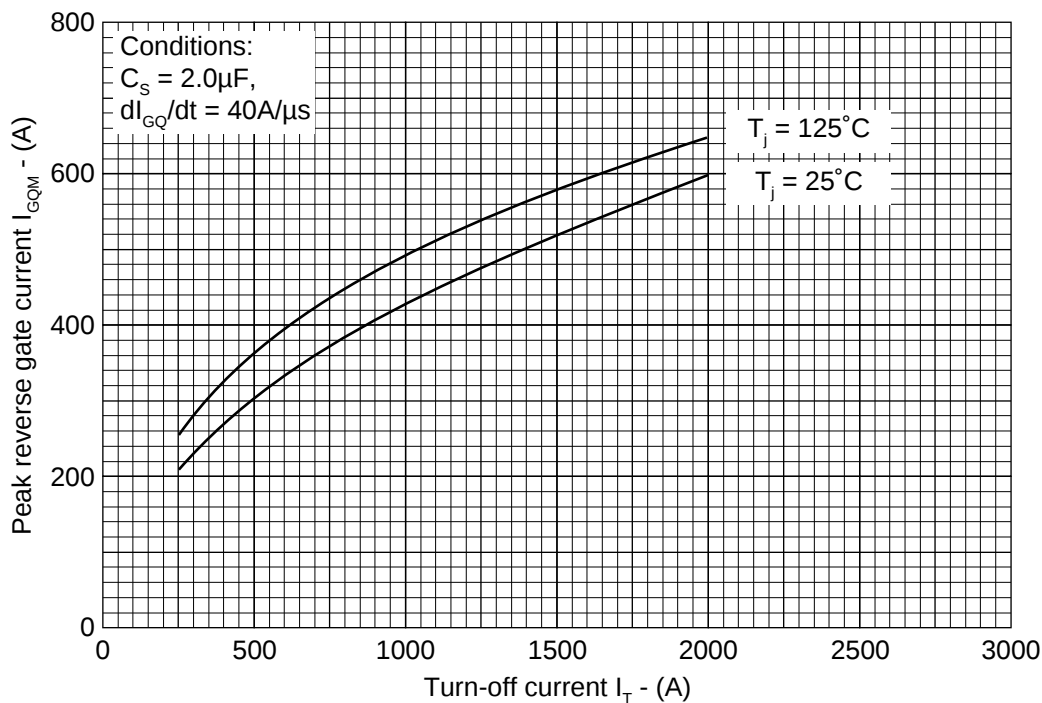


FIG. 24 PEAK REVERSE GATE CURRENT vs TURN-OFF CURRENT

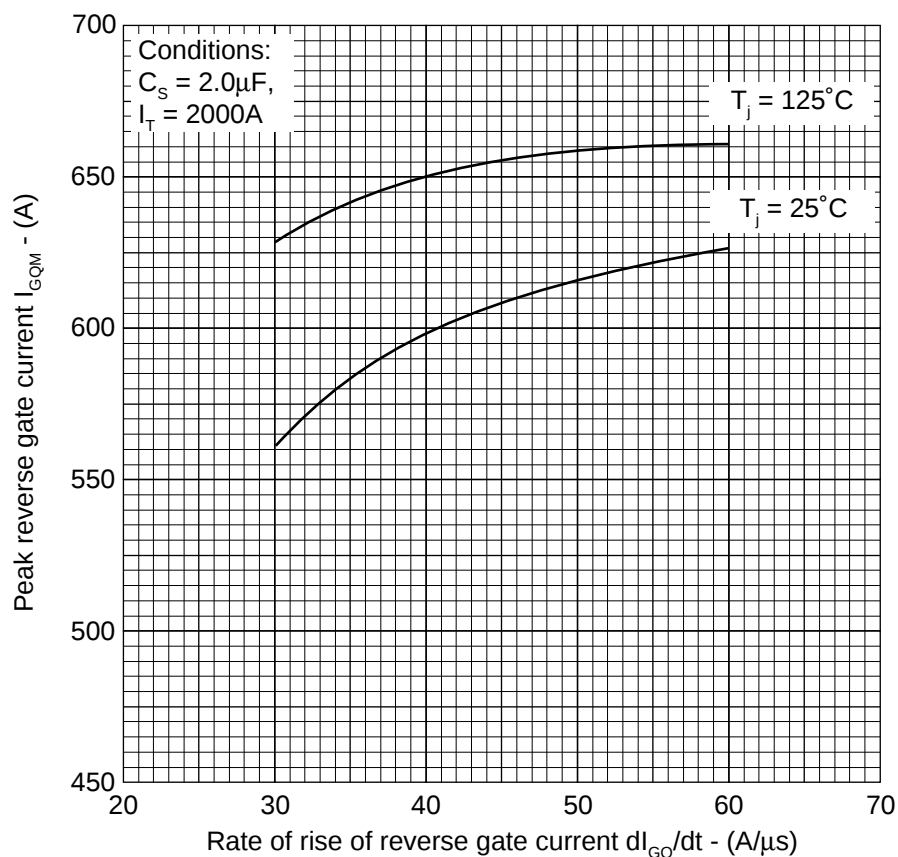


FIG. 25 PEAK REVERSE GATE CURRENT vs RATE OF RISE OF REVERSE GATE CURRENT

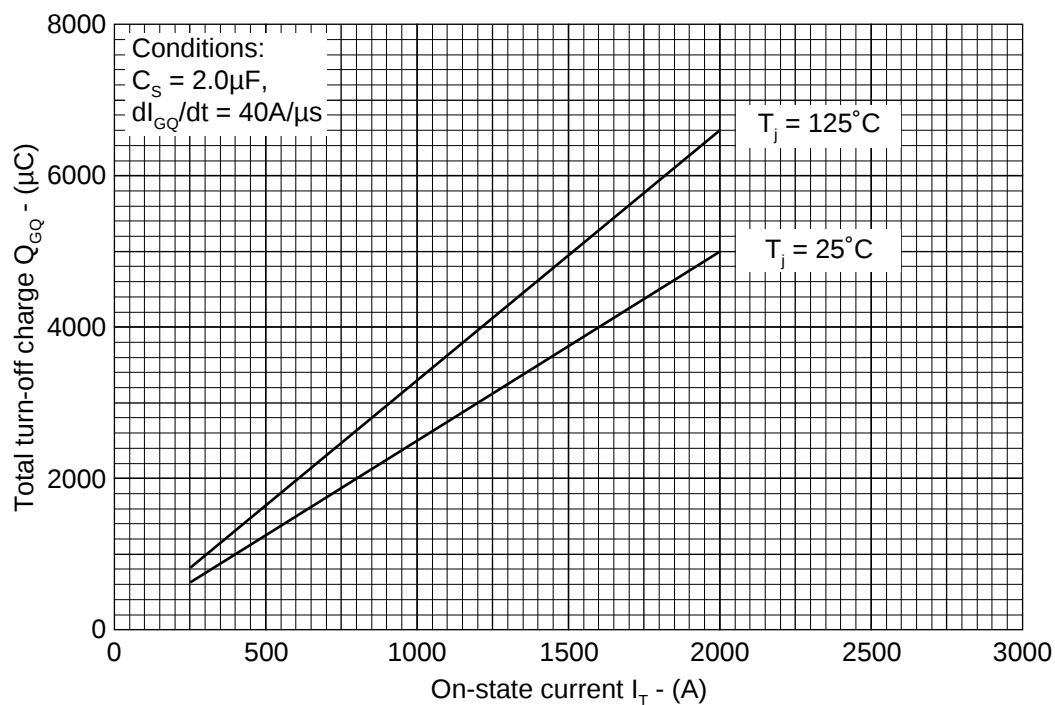


FIG. 26 TURN-OFF GATE CHARGE vs ON-STATE CURRENT

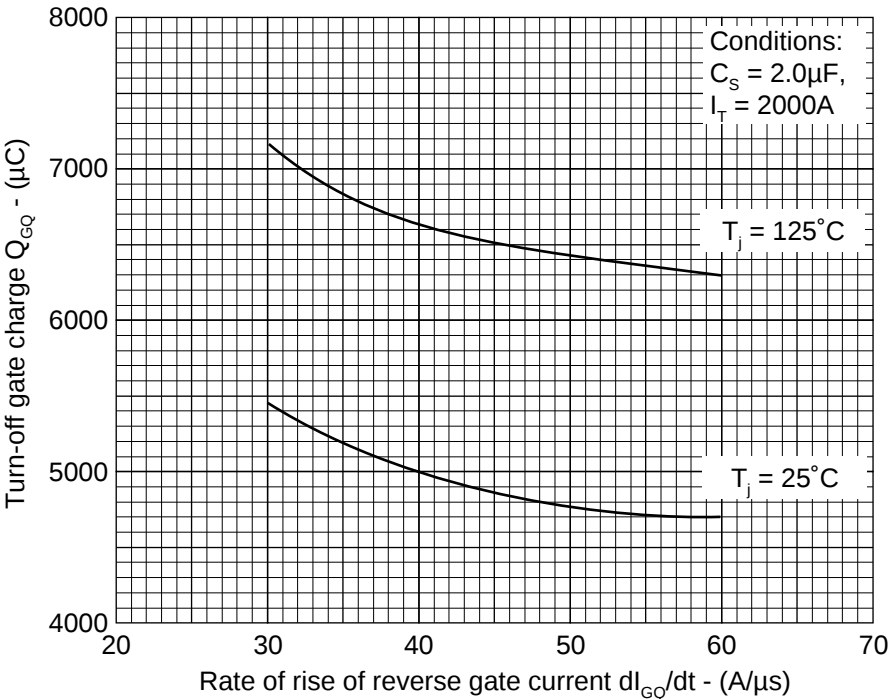


FIG. 27 TURN-OFF GATE CHARGE vs RATE OF RISE OF REVERSE GATE CURRENT

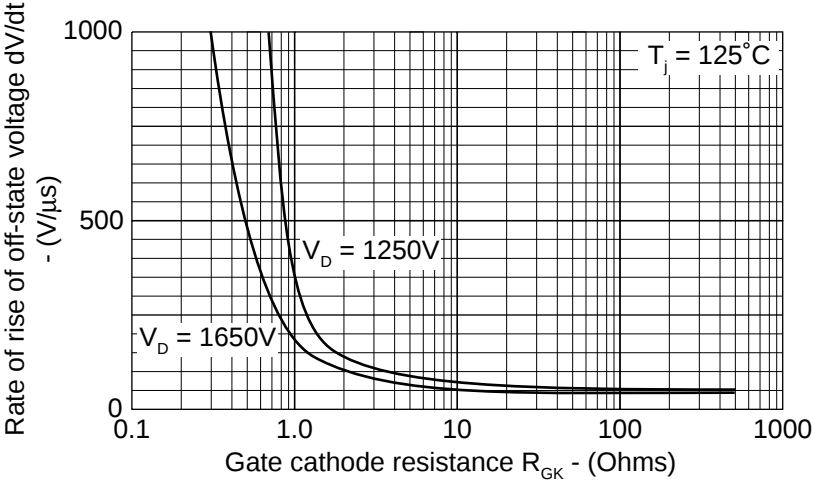
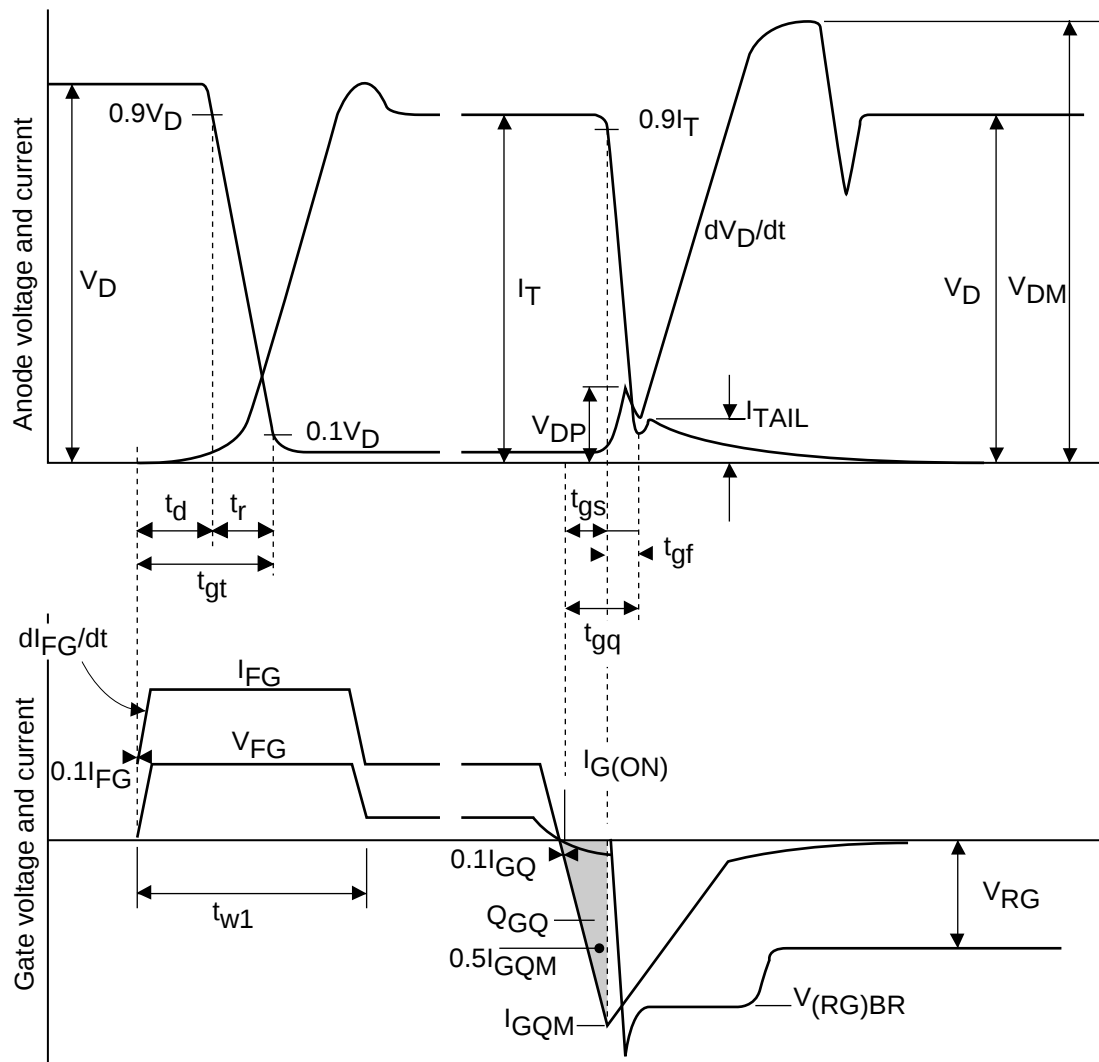


FIG. 28 RATE OF RISE OF OFF-STATE VOLTAGE vs GATE CATHODE RESISTANCE



Recommended gate conditions:

$$I_{TCM} = 2000A$$

$$I_{FG} = 30A$$

$$I_{G(ON)} = 7A \text{ d.c.}$$

$$t_{w1(min)} = 20\mu s$$

$$I_{GQM} = 650 A$$

$$dI_{GQ}/dt = 40A/\mu s$$

$$Q_{GQ} = 6600\mu C$$

$$V_{RG(min)} = 2V$$

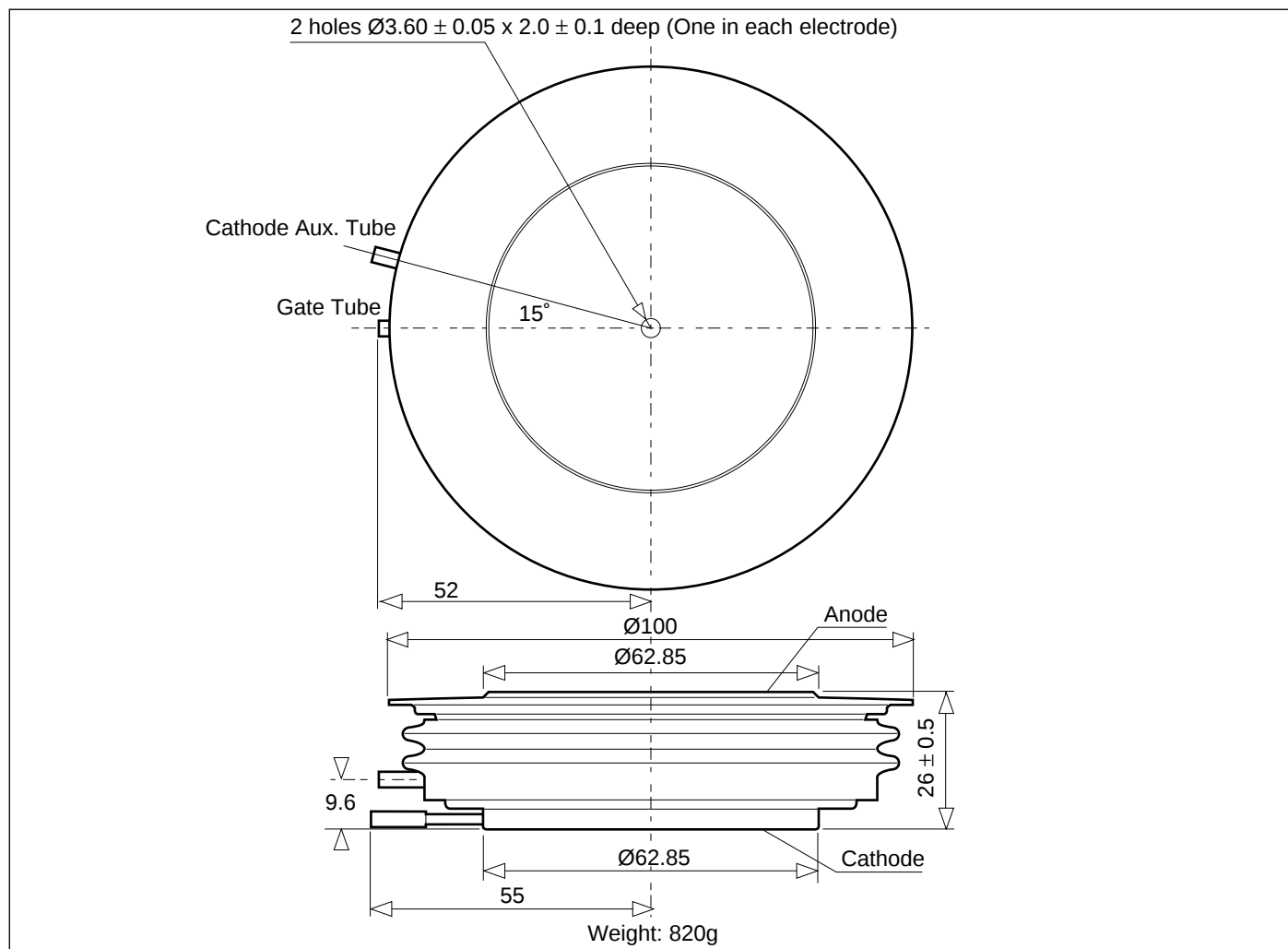
$$V_{RG(max)} = 16V$$

These are recommended GEC Plessey Semiconductor conditions. Other conditions are permitted according to users gate drive specifications.

FIG. 29 GENERAL SWITCHING WAVEFORMS

PACKAGE DETAILS - H

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



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