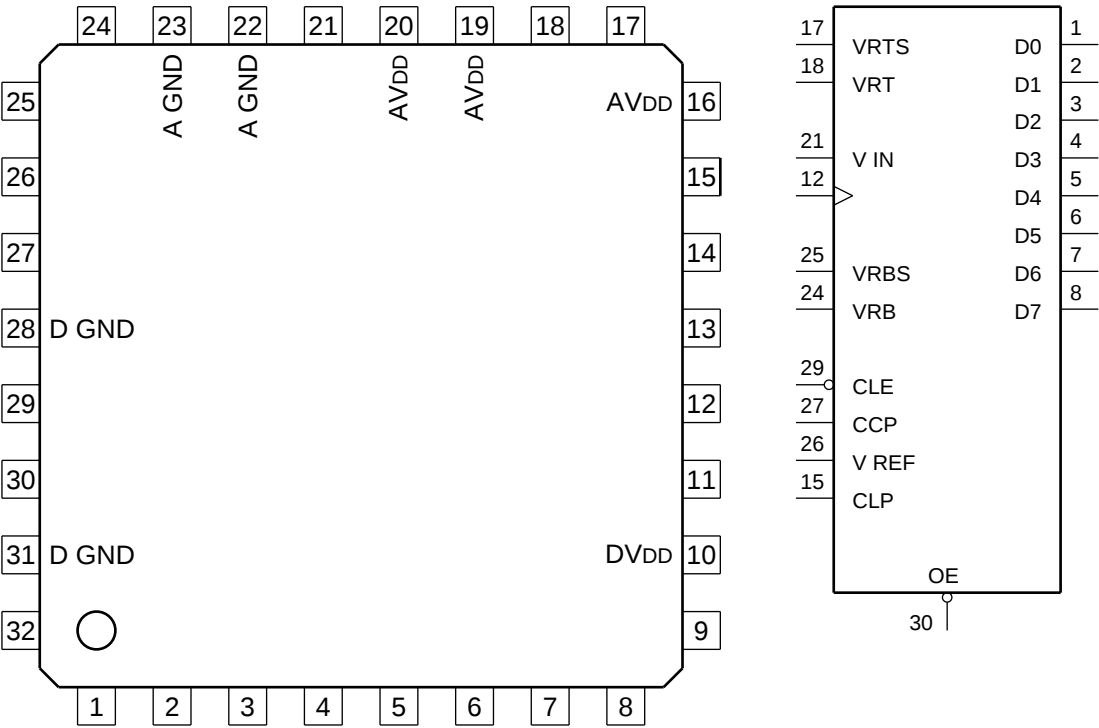


C-MOS 8-BIT 50MSPS VIDEO A/D CONVERTER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	D0 (LSB)	17	I	VRTS
2	O	D1	18	I	VRT
3	O	D2	19	—	A VDD
4	O	D3	20	—	A VDD
5	O	D4	21	I	V IN
6	O	D5	22	—	A GND
7	O	D6	23	—	A GND
8	O	D7 (MSB)	24	I	VRB
9	—	TEST	25	I	VRBS
10	—	D VDD	26	I	VREF
11	—	TEST	27	I	CCP
12	I	CLK	28	—	D GND
13	—	NC	29	I	CL
14	—	NC	30	I	OE
15	I	CLP	31	—	D GND
16	—	A VDD	32	—	NC

- INPUT
- CCP

:

CLAMP CONTROL VOLTAGE FOR INTEGRATOR
- CL

:

CLAMP ENABLE
- CLK

:

CLOCK
- CLP

:

CLAMP PULSE
- OE

:

OUTPUT ENABLE
- V IN

:

ANALOG
- VRB

:

REFERENCE VOLTAGE (BOTTOM)
- VRBS

:

SELF REFERENCE VOLTAGE SHORT TO AGND (+0.6 V/BOTTOM)
- VREF

:

VOLTAGE REFERENCE FOR CLAMP
- VRT

:

REFERENCE VOLTAGE (TOP)
- VRTS

:

SELF REFERENCE VOLTAGE SHORT TO AVDD (+2.5 V/TOP)
- OUTPUT
- D0 - D7 ; DATA

