The RF Line

Video Driver Hybrid Amplifier

The CR2428 is designed specifically for use as the video channel final stage in high resolution monitors.

- Typical 10-90% Transitions Times are 2.5 ns
- 130 MHz Minimum Bandwidth at 40 Vp-p Output
- 290 MHz Minimum Video Clock Frequency
- Up to 50 Vp-p Output Swing with 60 V Supply Voltage
- Low Power Consumption
- Excellent Grey-Scale Linearity
- Unconditional Stability
- · Gold Metallization System for the Ultimate in Reliability

CR2428

2.5 ns 130 MHz VIDEO DRIVER HYBRID AMPLIFIER



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	Vcc	70	Vdc
Operating Case Temperature Range	TC	-20 to +100	°C
Storage Temperature Range	T _{stg}	-40 to +100	°C

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$, $V_{CC} = 60$ V, $C_{LOAD} = 8.5$ pF, 40 V peak—to—peak output swing with 30 Vdc offset; $R_1 = 215 \Omega$, $C_1 = 90$ pF typ)

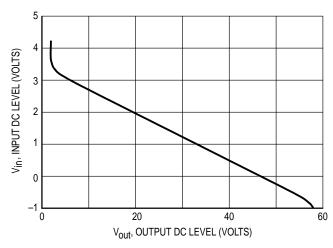
Characteristic	Symbol	Min	Тур	Max	Unit
Supply Current (With Input Open Circuited)	Icc	39.5	43.5	47.5	mA
Input DC Voltage (With Input Open Circuited)	VinDC	1.15	1.4	1.65	V
Output DC Voltage (With Input Open Circuited)	VoutDC	26	30	34	V
Voltage Gain (1) (2)	A _V	11.2	12.4	13.2	V/V
Transient Response (2) — Rise Time (10% to 90%) — Overshoot — Fall Time (90% to 10%) — Overshoot	t _r V _{os,r} t _f V _{os,f}	_ _ _ _	2.5 8.0 2.5 6.0	2.9 15 2.9 10	ns % ns %
Operating Supply Current (V _{Out} = 40 V Peak–to–Peak, 50 MHz Square Wave with 30 V offset) (3)	Icc	_	100	_	mA
Linearity Error (V _{out} = +5.0 V to +55 V)	_	_	_	5.0	%

NOTES:

- 1. $A_V = V_{out}/V_S$
- 2. Input Signal is nominally a 62.5 kHz square wave of 3.25 V peak-to-peak with 1.4 Vdc offset. Input t_r, t_f < 1.0 ns.
- 3. Output is not short circuit protected.



TYPICAL CHARACTERISTICS



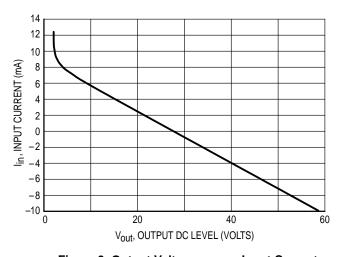
1.7 1.65 1.65 1.5 1.5 1.45 1.35 1.25 1.2 0

20

Vout, OUTPUT DC LEVEL (VOLTS)

Figure 1. Voltage Ratio at RF Input Port

Figure 2. Voltage Ratio at Port 1



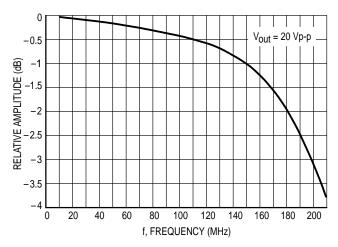


Figure 3. Output Voltage versus Input Current

Figure 4. Frequency Response

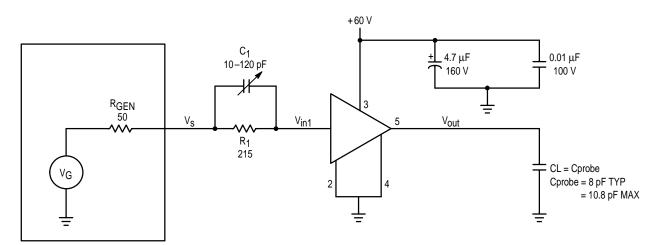
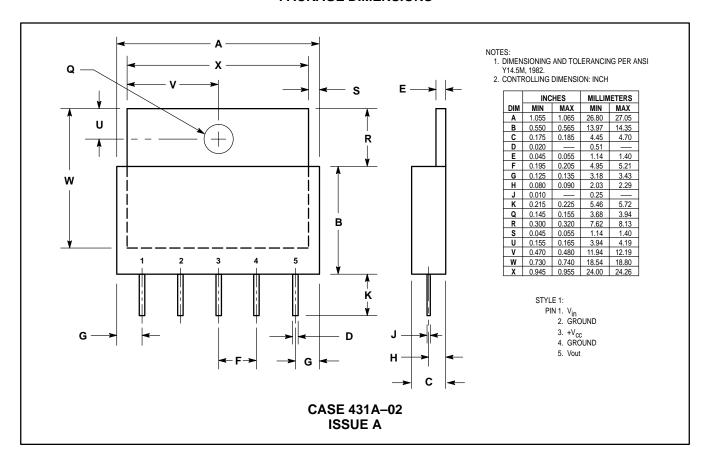


Figure 5. CRT Driver Test Circuit

PACKAGE DIMENSIONS



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How to reach us:

USA/EUROPE: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447

MFAX: RMFAX0@email.sps.mot.com – TOUCHTONE (602) 244–6609 INTERNET: http://Design_NET.com

JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, Toshikatsu Otsuki, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–3521–8315

HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



