

**CPH5803****DC / DC Converter Applications****Features**

- Composite type with an N-Channel Silicon MOSFET (MCH3405) and a Schottky Barrier Diode (SBS004M) contained in one package facilitating high-density mounting.

[MOSFET]

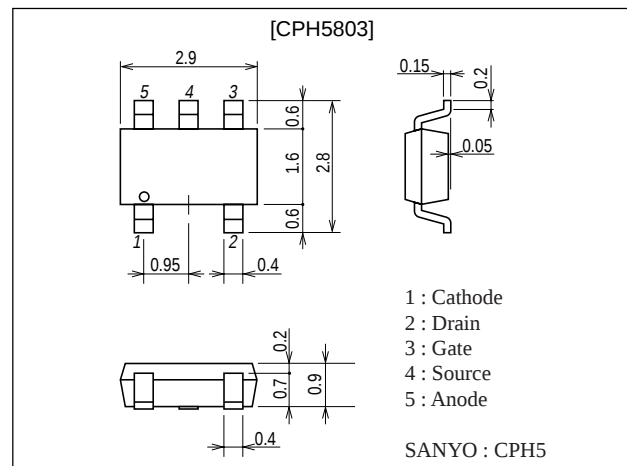
- Low ON-resistance.
- Ultrahigh-speed switching.
- 1.8V drive.

[SBD]

- Short reverse recovery time.
- Low forward voltage.

Package Dimensions

unit : mm

**Specifications****Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
[MOSFET]				
Drain-to-Source Voltage	V _{DSS}		20	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	I _D		1.5	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	6	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (900mm²×0.8mm) 1unit	0.8	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +125	°C
[SBD]				
Repetitive Peak Reverse Voltage	V _{RRM}		15	V
Non-repetitive Peak Reverse Surge Voltage	V _{RSM}		15	V
Average Output Current	I _O		1	A
Surge Forward Current	I _{FSM}	50Hz sine wave, 1 cycle	10	A
Junction Temperature	T _J		-55 to +125	°C
Storage Temperature	T _{stg}		-55 to +125	°C

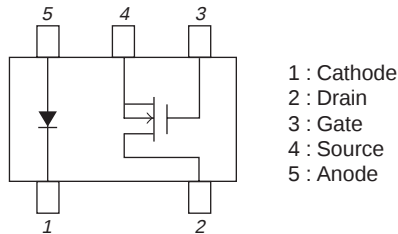
Marking : QD

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Electrical Characteristics at Ta=25°C

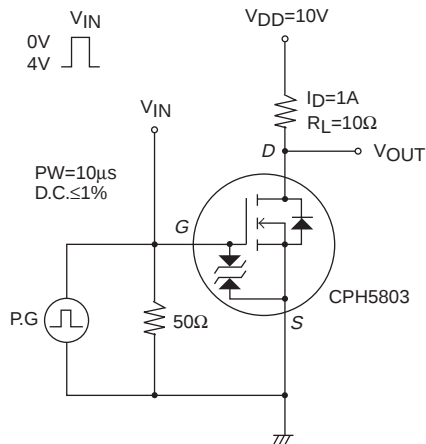
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[MOSFET]						
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	20			V
Zero-Gate Voltage Drain Current	IDSS	VDS=20V, VGS=0			1	μA
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μA
Cutoff Voltage	VGS(off)	VDS=10V, ID=1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	VDS=10V, ID=1A	1.9	2.8		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=1A, VGS=4V		160	210	mΩ
	RDS(on)2	ID=0.5A, VGS=2.5V		200	280	mΩ
	RDS(on)3	ID=0.1A, VGS=1.8V		280	390	mΩ
Input Capacitance	Ciss	VDS=10V, f=1MHz		100		pF
Output Capacitance	Coss	VDS=10V, f=1MHz		22		pF
Reverse Transfer Capacitance	Crss	VDS=10V, f=1MHz		15		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit		6.5		ns
Rise Time	tr	See specified Test Circuit		28		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		19		ns
Fall Time	tf	See specified Test Circuit		13		ns
Total Gate Charge	Qg	VDS=10V, VGS=10V, ID=1.5A		4.5		nC
Gate-to-Source Charge	Qgs	VDS=10V, VGS=10V, ID=1.5A		0.4		nC
Gate-to-Drain "Miller" Charge	Qgd	VDS=10V, VGS=10V, ID=1.5A		0.4		nC
Diode Forward Voltage	VSD	IS=1.5A, VGS=0		0.83	1.2	V
[SBD]						
Reverse Voltage	VR	IR=1mA	15			V
Forward Voltage	VF1	IF=0.5A		0.30	0.35	V
	VF2	IF=1A		0.35	0.40	V
Reverse Current	IR	VR=6V			500	μA
Interterminal Capacitance	C	VR=10V, f=1MHz, 1 cycle		42		pF
Reverse Recovery Time	trr	IF=IR=100mA, See specified Test Circuit.			15	ns
Thermal Resistance	Rth(j-a)	Mounted on a ceramic board (900mm²×0.8mm)		110		°C / W

Electrical Connection (Top view)



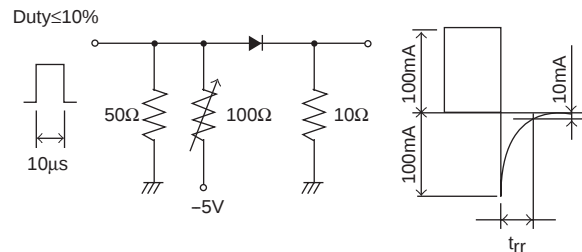
Switching Time Test Circuit

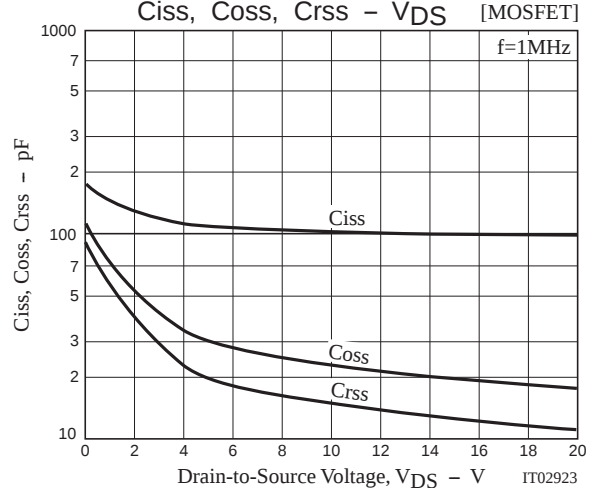
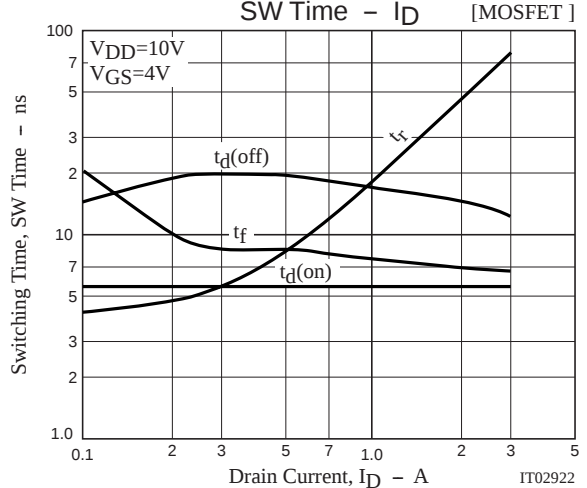
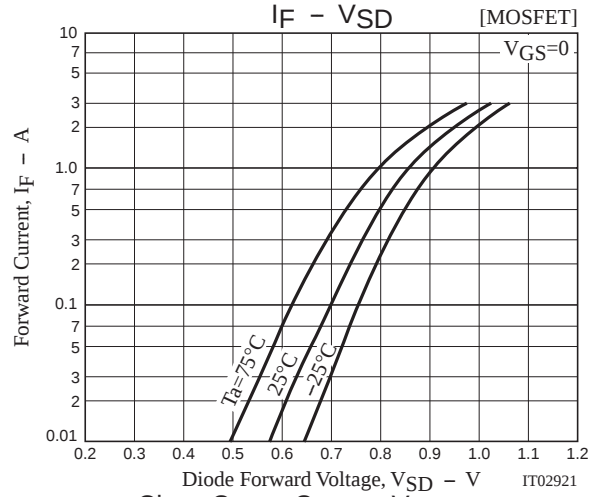
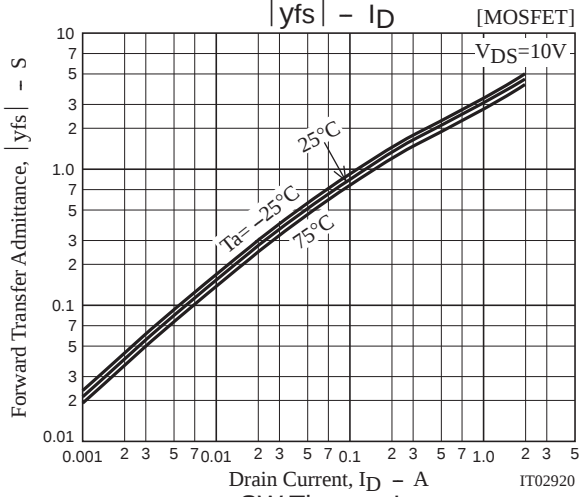
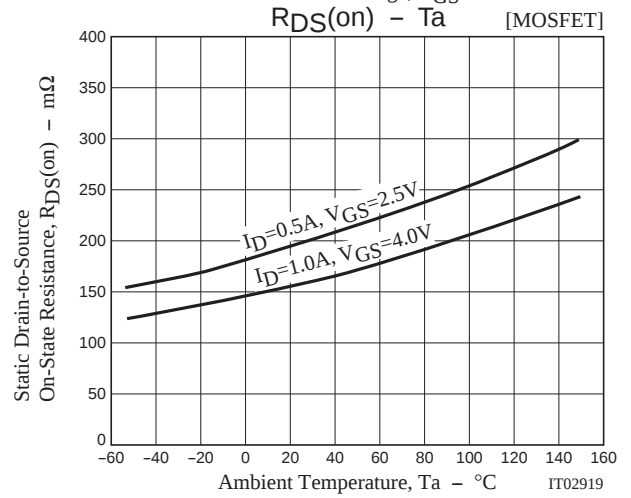
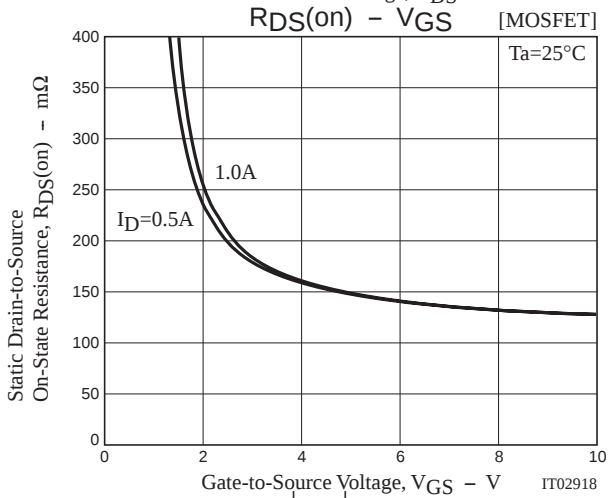
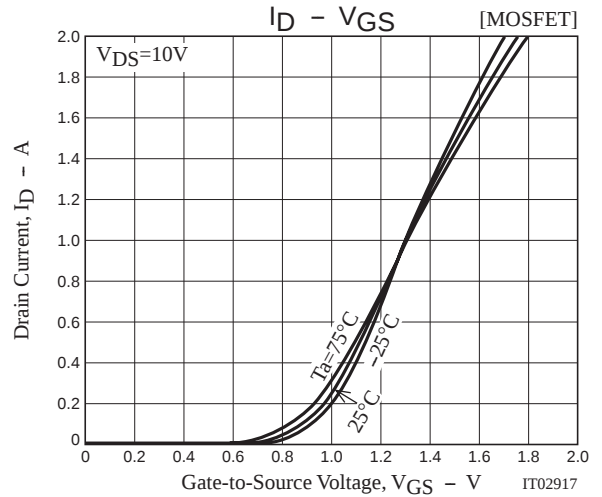
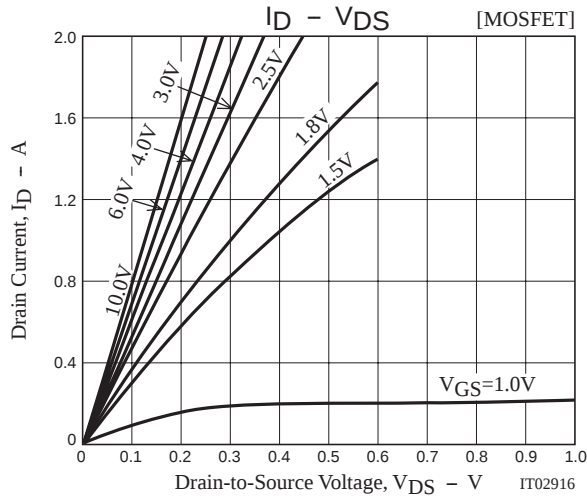
[MOSFET]

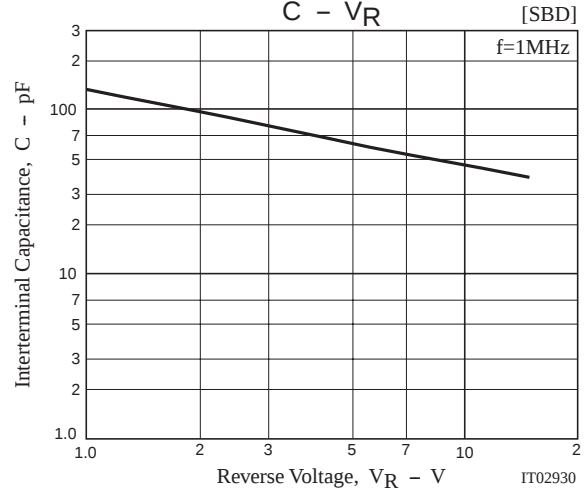
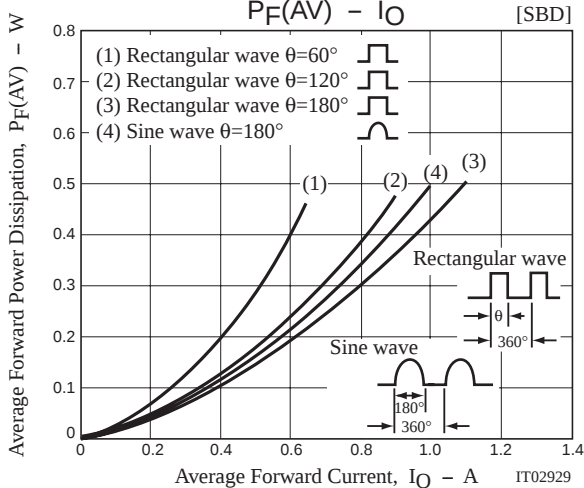
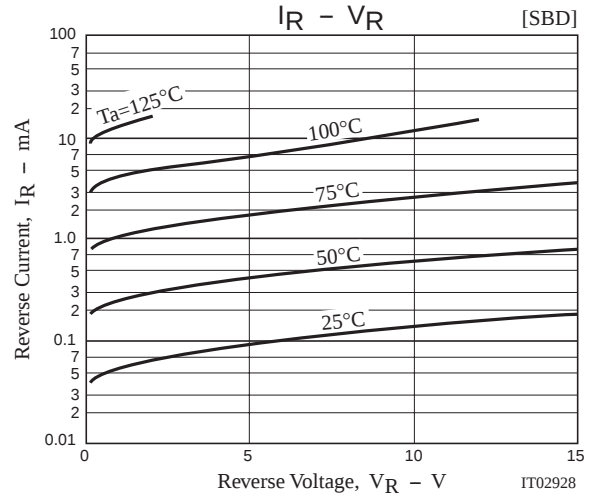
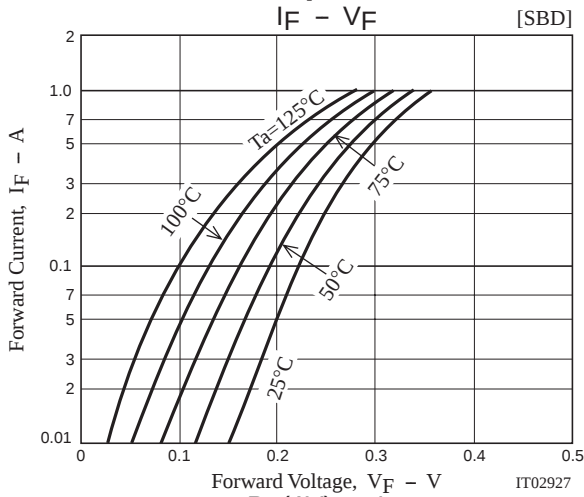
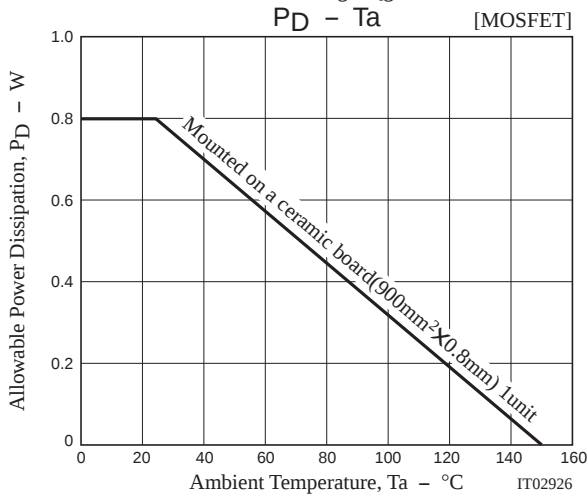
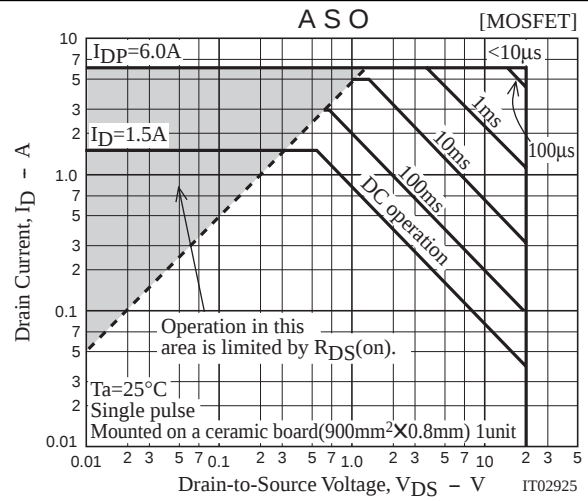
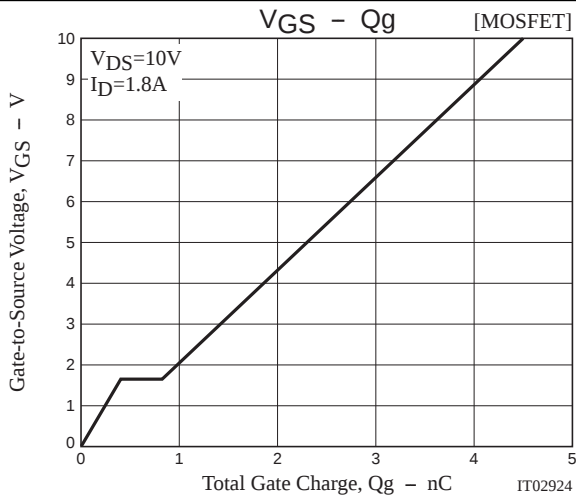


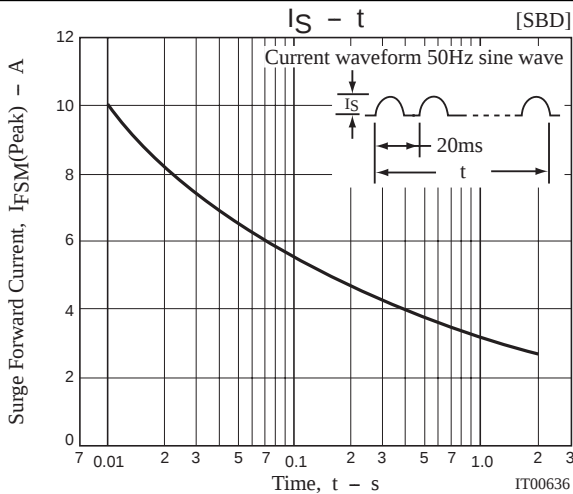
trr Test Circuit

[SBD]









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