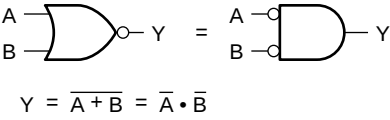
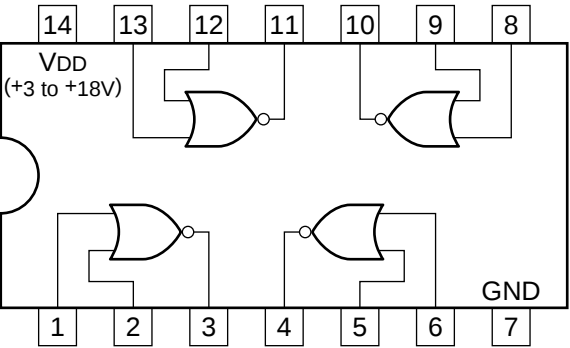

C-MOS 2-INPUT NOR GATE
-TOP VIEW-



A	B	Y
0	0	1
0	1	0
1	0	0
1	1	0

0 ; LOW LEVEL
1 ; HIGH LEVEL