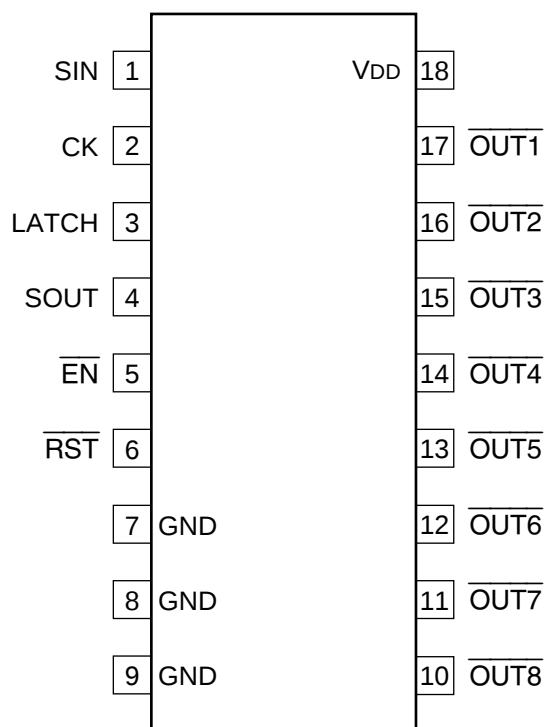


CMOS SHIFT RESISTOR WITH LATCH

—TOP VIEW—



SIN : SERIAL DATA INPUT
CK : SHIFT CLOCK INPUT
LATCH : LATCH INPUT
SOUT : SHIFT RESISTOR (FINAL STAGE) OUTPUT
 $\overline{EN}$: ENABLE INPUT
 $\overline{RST}$: RESET INPUT
 $\overline{OUT1} - \overline{OUT8}$: SHIFT RESISTOR LATCH OUTPUT1-8

