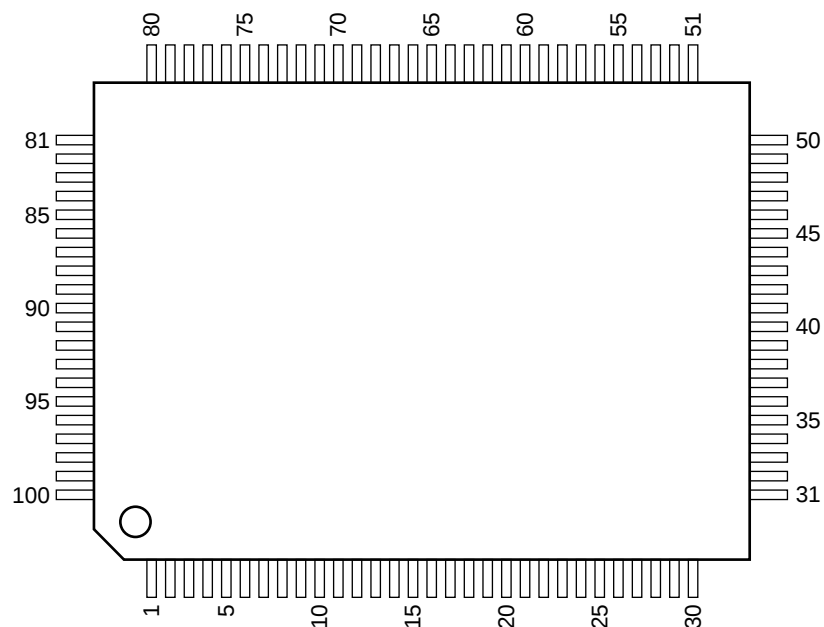


VIDEO CAPTURE PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	Vcc	26	I/O	VD3	51	I	YABIAS	76	—	Vcc
2	O	VD15	27	I/O	VD2	52	I	YIN	77	—	GND
3	O	VD14	28	I/O	VD1	53	O	MUXOUT	78	O	FIELD
4	O	VD13	29	I/O	VD0	54	—	AGND	79	O	VRESET
5	O	VD12	30	—	Vcc	55	I	MUX0	80	I	NUMXTAL
6	O	VD11	31	—	GND	56	—	AGND	81	—	GND
7	O	VD10	32	O	TDO	57	I	MUX1	82	O	HRESET
8	O	VD9	33	—	GND	58	—	AGND	83	O	ACTIVE
9	O	VD8	34	I	TCK	59	I	SYNCDDET	84	O	DVALID
10	—	Vcc	35	I	TRST	60	—	Vcc	85	—	GND
11	—	GND	36	I	TMS	61	—	AGND	86	O	VACTIVE
12	I	XT0I	37	I	TDI	62	I	YREF—	87	O	CCVALID
13	O	XT0O	38	—	Vcc	63	—	NC	88	—	Vcc
14	I	12CCS	39	—	GND	64	I	CREF+	89	O	CBFLAG
15	I	RST	40	—	VPOS	65	—	Vcc	90	—	GND
16	I	XT1I	41	I	AGCCAP	66	—	AGND	91	I	PWRDN
17	O	XT1O	42	—	VNEG	67	I	CIN	92	—	Vcc
18	I/O	SDA	43	O	REFOUT	68	—	NC	93	—	GND
19	I	SCL	44	—	Vcc	69	I	CCBIAS	94	O	QCLK
20	—	Vcc	45	I	MUX2	70	I	CABIAS	95	—	GND
21	—	GND	46	I	YCBIAS	71	—	AGND	96	—	Vcc
22	I/O	VD7	47	—	AGND	72	—	Vcc	97	O	CLKx1
23	I/O	VD6	48	—	Vcc	73	I	CREF—	98	I	OE
24	I/O	VD5	49	I	YREF+	74	I	CLEVEL	99	O	CLKx2
25	I/O	VD4	50	—	NC	75	—	AGND	100	—	GND

INPUTS

I ² CCS	: I ² C CHIP SELECT
AGCCAP	: AGC TIME CONSTANT CONTROL CAPACITOR
CABIAS	: C-ADC BIAS
CCBIAS	: C-ADC BIAS
CIN	: ANALOG CHROMA
CLEVEL	: C-ADC DC REFERENCE
CREF–	: C-ADC BOTTOM REFERENCE
CREF+	: C-ADC TOP REFERENCE
MUX0 - MUX2	: ANALOG COMPOSITE VIDEO
NUMXTAL	: CRYSTAL FORMAL
$\overline{OE}$	: OUTPUT ENABLE CONTROL
PWRDN	: POWER DOWN
$\overline{RST}$	: RESET CONTROL
SCL	: SERIAL CLOCK
SYNCDDET	: SYNC DETECT
TCK	: TEST CLOCK
TDI	: TEST DATA
TMS	: TEST MODE SELECT
$\overline{TRST}$	: TEST RESET
XT0I, XT1I	: CLOCK
YABIAS	: Y-ADC BIAS
YCBIAS	: Y-ADC BIAS
YIN	: ANALOG COMPOSITE OR LUMINANCE SIGNAL
YREF–	: Y-ADC BOTTOM REFERENCE
YREF+	: Y-ADC TOP REFERENCE

OUTPUTS

ACTIVE	: ACTIVE VIDEO
CBFLAG	: CB DATA IDENTIFIER
CCVALID	: CC FIFO VALID
CLKx1	: 1x CLOCK
CLKx2	: 2x CLOCK
DVALID	: DATA VALID
FIELD	: ODD/EVEN FIELD
$\overline{HRESET}$	: HORIZONTAL RESET
MUXOUT	: ANALOG VIDEO
QCLK	: QUALIFIED CLOCK
REFOUT	: AGC REFERENCE
TDO	: TEST DATA
VACTIVE	: VERTICAL BLANKING
VD8 - VD15	: DIGITIZED VIDEO DATA
$\overline{VRESET}$	: VERTICAL RESET
XT0O, XT1O	: CLOCK

INPUTS/OUTPUTS

SDA	: SERIAL DATA
VD0 - VD7	: DIGITIZED VIDEO DATA

