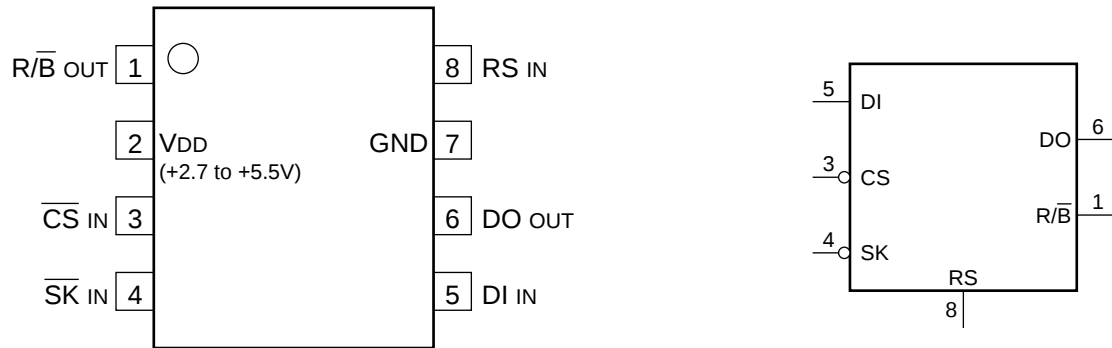

C-MOS 2k (128 X 16) BITS SERIAL EEPROM -TOP VIEW-



INPUT

CS ; CHIP SELECT
DI ; OP CODE, ADDRESS, AND SERIAL DATA
RS ; RESET
SK ; SERIAL DATA CLOCK

OUTPUT

DO ; SERIAL DATA
R/B ; READY / BUSY STATUS SIGNAL

