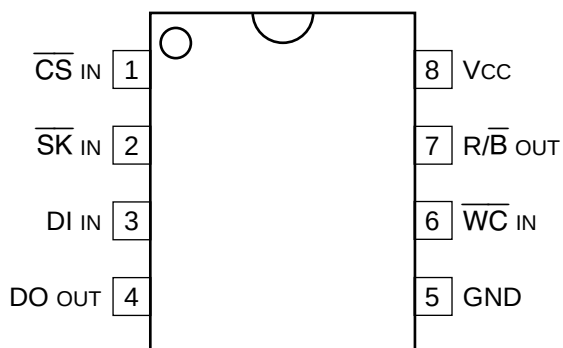


2K (128×16)-BIT SERIAL EEPROM

—TOP VIEW—



INPUTS

$\overline{CS}$: CHIP SELECT

DI : OPERAND CODE, ADDRESS, AND SERIAL DATA

$\overline{SK}$: SERIAL DATA CLOCK

$\overline{WC}$: WRITE CONTROL

OUTPUTS

DO : SERIAL DATA

$R/\overline{B}$: READY/BUSY STATUS SIGNAL

