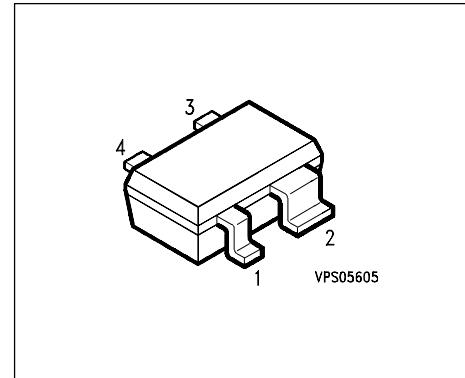


**NPN Silicon RF Transistor**

- For low noise, high-gain broadband amplifiers at collector currents from 2 mA to 30 mA
- $f_T = 8$  GHz
- $F = 1.2$  dB at 900 MHz

**ESD: Electrostatic discharge sensitive device, observe handling precaution!**

Type	Marking	Ordering Code	Pin Configuration				Package
BFP 183W	RHs	Q62702-F1503	1 = E	2 = C	3 = E	4 = B	SOT-343

**Maximum Ratings**

Parameter	Symbol	Values	Unit
Collector-emitter voltage	$V_{CEO}$	12	V
Collector-emitter voltage	$V_{CES}$	20	
Collector-base voltage	$V_{CBO}$	20	
Emitter-base voltage	$V_{EBO}$	2	
Collector current	$I_C$	65	mA
Base current	$I_B$	5	
Total power dissipation $T_S \leq 58$ °C	$P_{tot}$	450	mW
Junction temperature	$T_j$	150	°C
Ambient temperature	$T_A$	- 65 ... + 150	
Storage temperature	$T_{stg}$	- 65 ... + 150	

**Thermal Resistance**

Junction - soldering point <sup>1)</sup>	$R_{thJS}$	$\leq 205$	K/W
--	------------	------------	-----

1)  $T_S$  is measured on the collector lead at the soldering point to the pcb.

**Electrical Characteristics** at  $T_A = 25^\circ\text{C}$ , unless otherwise specified.

<b>Parameter</b>	<b>Symbol</b>	<b>Values</b>			<b>Unit</b>
		min.	typ.	max.	
<b>DC Characteristics</b>					
Collector-emitter breakdown voltage $I_C = 1 \text{ mA}, I_B = 0$	$V_{(\text{BR})\text{CEO}}$	12	-	-	V
Collector-emitter cutoff current $V_{\text{CE}} = 20 \text{ V}, V_{\text{BE}} = 0$	$I_{\text{CES}}$	-	-	100	$\mu\text{A}$
Collector-base cutoff current $V_{\text{CB}} = 10 \text{ V}, I_E = 0$	$I_{\text{CBO}}$	-	-	100	nA
Emitter-base cutoff current $V_{\text{EB}} = 1 \text{ V}, I_C = 0$	$I_{\text{EBO}}$	-	-	1	$\mu\text{A}$
DC current gain $I_C = 15 \text{ mA}, V_{\text{CE}} = 8 \text{ V}$	$h_{\text{FE}}$	50	100	200	-

**Electrical Characteristics** at  $T_A = 25^\circ\text{C}$ , unless otherwise specified.

<b>Parameter</b>	<b>Symbol</b>	<b>Values</b>			<b>Unit</b>
		min.	typ.	max.	
<b>AC Characteristics</b>					
Transition frequency $I_C = 25 \text{ mA}, V_{CE} = 8 \text{ V}, f = 500 \text{ MHz}$	$f_T$	6	8	-	GHz
Collector-base capacitance $V_{CB} = 10 \text{ V}, f = 1 \text{ MHz}$	$C_{cb}$	-	0.4	0.6	pF
Collector-emitter capacitance $V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}$	$C_{ce}$	-	0.27	-	
Emitter-base capacitance $V_{EB} = 0.5 \text{ V}, f = 1 \text{ MHz}$	$C_{eb}$	-	1	-	
Noise figure $I_C = 5 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_{\text{Sopt}}$ $f = 900 \text{ MHz}$ $f = 1.8 \text{ GHz}$	$F$	-	1.2	-	dB
-	-	-	2	-	
Power gain 1) $I_C = 15 \text{ mA}, V_{CE} = 8 \text{ V}, f = 900 \text{ MHz}$ $Z_S = Z_{\text{Sopt}}, Z_L = Z_{\text{Lopt}}$	$G_{ms}$	-	21.5	-	
Power gain 2) $I_C = 15 \text{ mA}, V_{CE} = 8 \text{ V}, f = 1.8 \text{ GHz}$ $Z_S = Z_{\text{Sopt}}, Z_L = Z_{\text{Lopt}}$	$G_{ma}$	-	14.5	-	
Transducer gain $I_C = 15 \text{ mA}, V_{CE} = 8 \text{ V}, Z_S = Z_L = 50 \Omega$ $f = 900 \text{ MHz}$ $f = 1.8 \text{ GHz}$	$ S_{21e} ^2$	-	17	-	
-	-	-	11	-	

$$1) G_{ms} = |S_{21}/S_{12}|$$

$$2) G_{ma} = |S_{21}/S_{12}| (k - (k^2 - 1)^{1/2})$$

**SPICE Parameters (Gummel-Poon Model, Berkeley-SPICE 2G.6 Syntax) :****Transistor Chip Data**

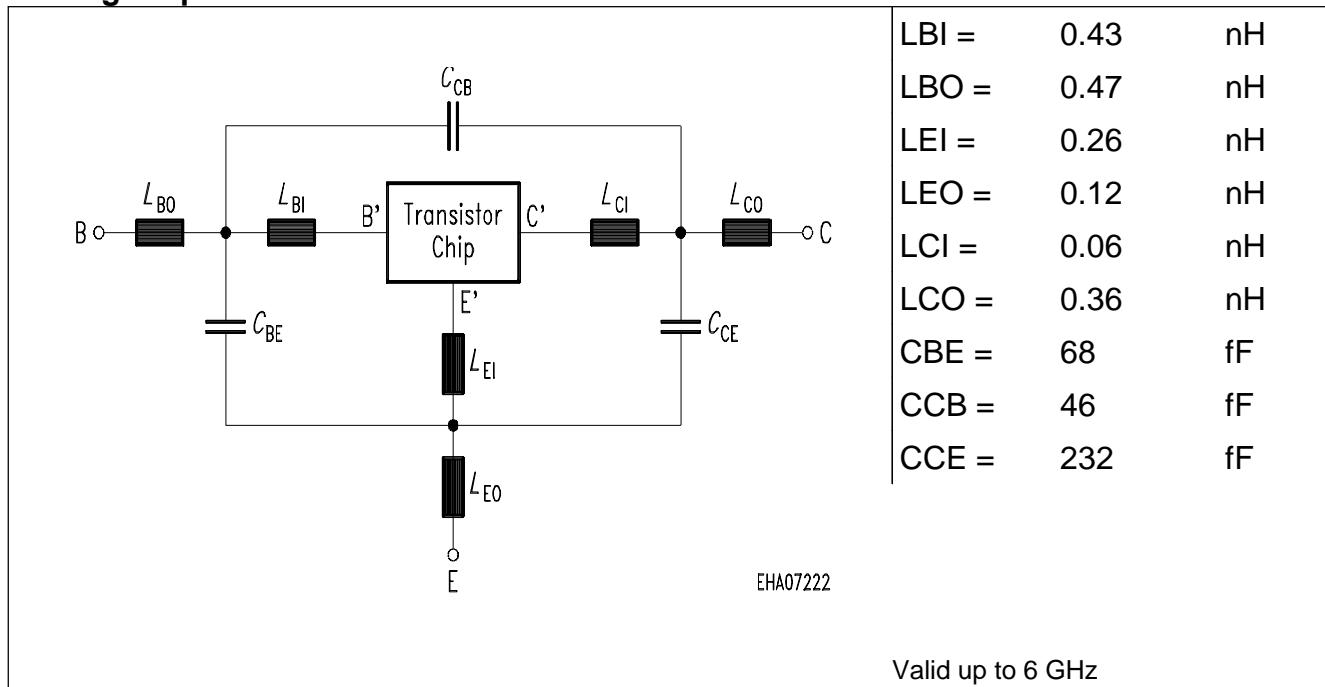
IS =	1.0345	fA	BF =	115.98	-	NF =	0.80799	-
VAF =	14.772	V	IKF =	0.14562	A	ISE =	16.818	fA
NE =	1.2149	-	BR =	10.016	-	NR =	0.99543	-
VAR =	3.4276	V	IKR =	0.013483	A	ISC =	1.3559	fA
NC =	0.85331	-	RB =	1.0112	$\Omega$	IRB =	0.43801	mA
RBM =	2.5426	$\Omega$	RE =	1.3435	$\Omega$	RC =	0.20486	$\Omega$
CJE =	23.077	fF	VJE =	1.0792	V	MJE =	0.45354	-
TF =	22.746	ps	XTF =	0.36823	-	VTF =	0.50905	V
ITF =	1.8773	mA	PTF =	0	deg	CJC =	460.11	fF
VJC =	1.1967	V	MJC =	0.3	-	XCJC =	0.053823	-
TR =	1.0553	ns	CJS =	0	fF	VJS =	0.75	V
MJS =	0	-	XTB =	0	-	EG =	1.11	eV
XTI =	3	-	FC =	0.54852	-	TNOM	300	K

All parameters are ready to use, no scaling is necessary.

Extracted on behalf of SIEMENS Small Signal Semiconductors by:

Institut für Mobil- und Satellitenfunktechnik (IMST)

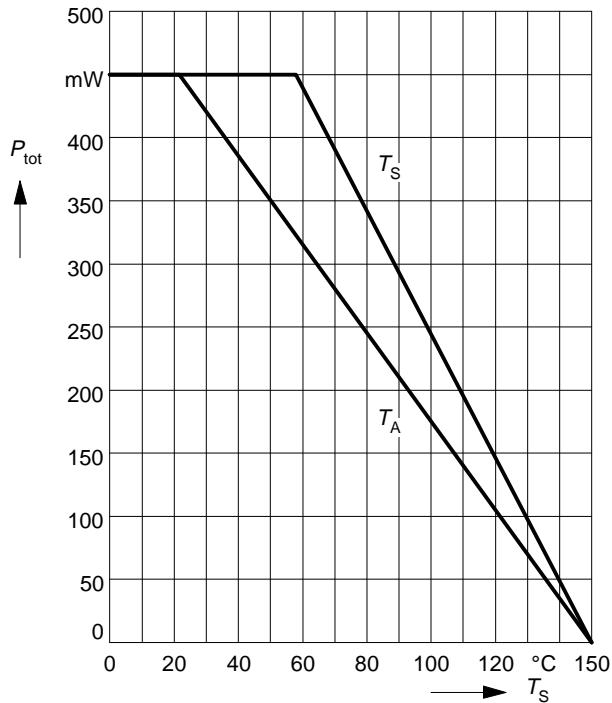
© 1996 SIEMENS AG

**Package Equivalent Circuit:**

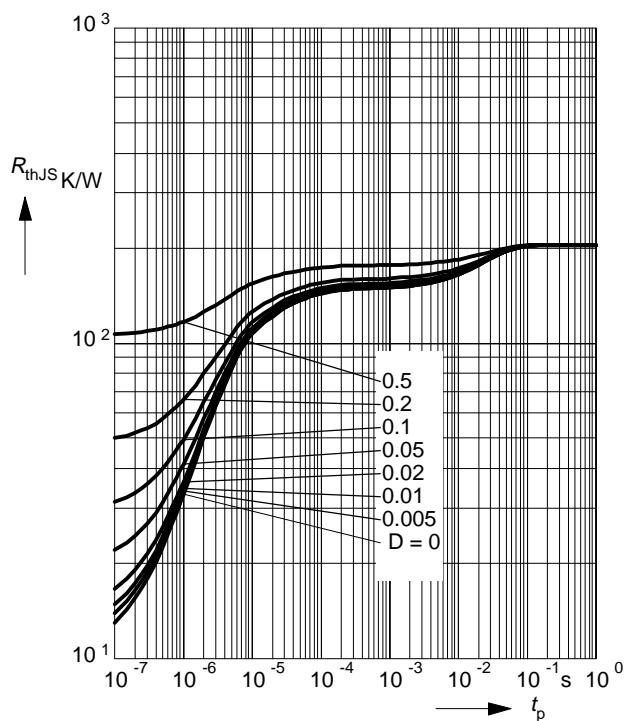
For examples and ready to use parameters please contact your local Siemens distributor or sales office to obtain a Siemens CD-ROM or see Internet: <http://www.siemens.de/Semiconductor/products/35/35.htm>

**Total power dissipation**  $P_{\text{tot}} = f(T_A^*, T_S)$

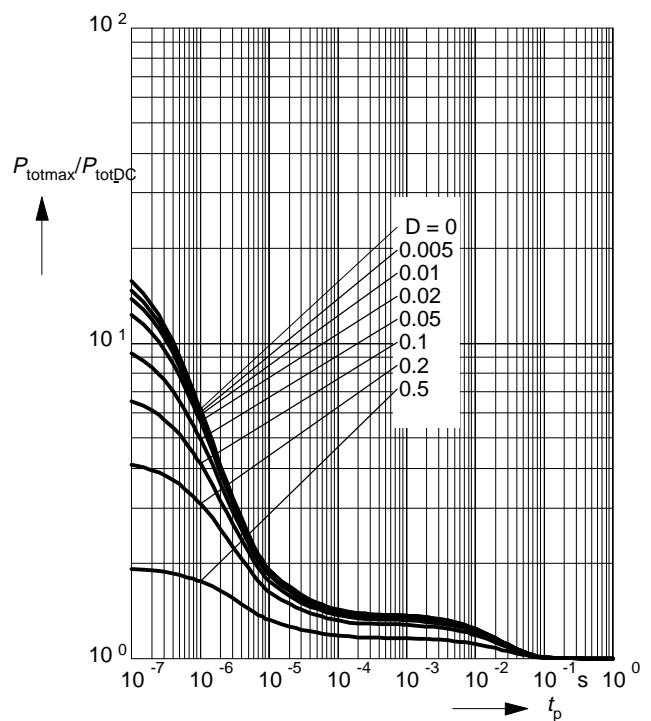
\* Package mounted on epoxy



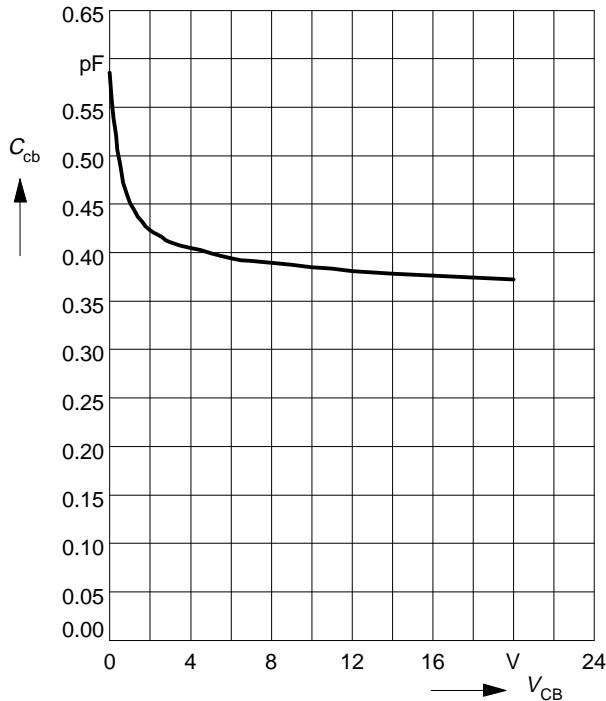
**Permissible Pulse Load**  $R_{\text{thJS}} = f(t_p)$



**Permissible Pulse Load**  $P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$



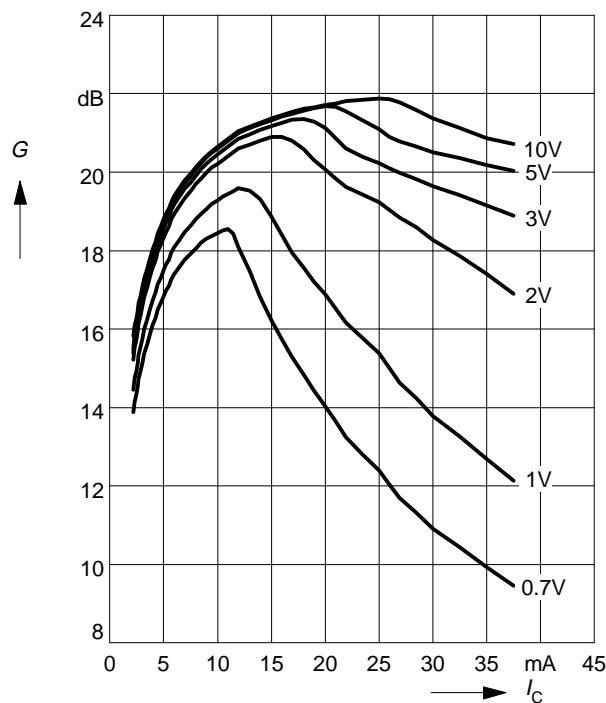
**Collector-base capacitance**  $C_{cb} = f(V_{CB})$   
 $V_{BE} = v_{be} = 0$ ,  $f = 1\text{MHz}$



**Power Gain**  $G_{ma}, G_{ms} = f(I_C)$

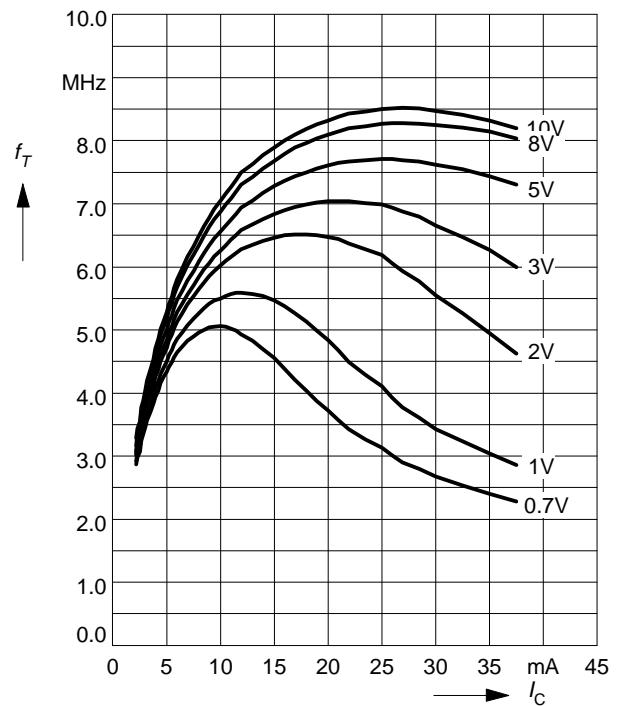
$f = 0.9\text{GHz}$

$V_{CE}$  = Parameter



**Transition frequency**  $f_T = f(I_C)$

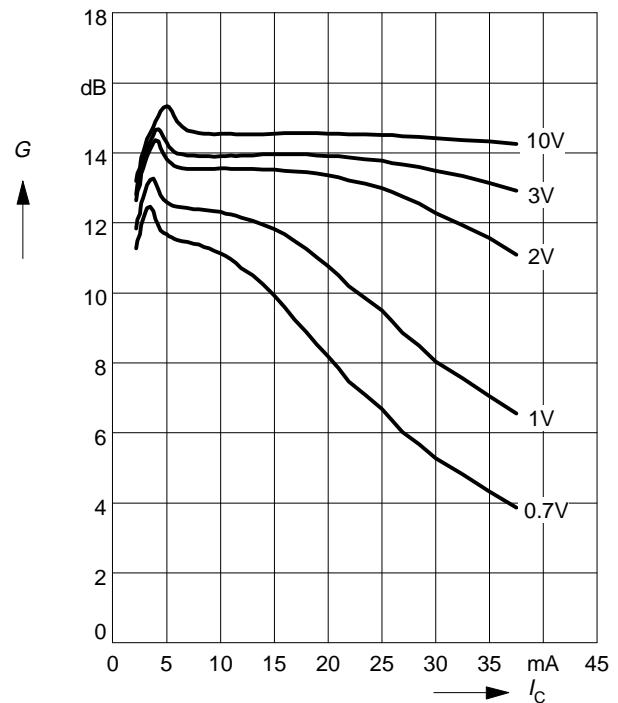
$V_{CE}$  = Parameter



**Power Gain**  $G_{ma}, G_{ms} = f(I_C)$

$f = 1.8\text{GHz}$

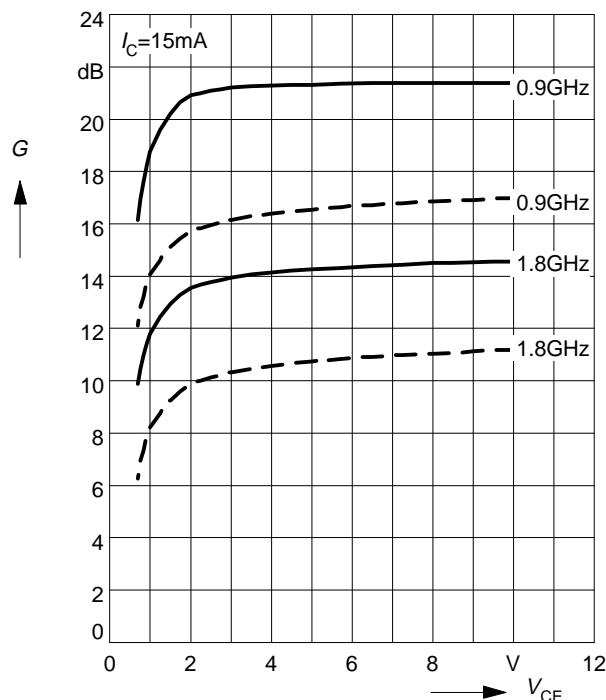
$V_{CE}$  = Parameter



**Power Gain**  $G_{ma}$ ,  $G_{ms} = f(V_{CE})$ : \_\_\_\_\_

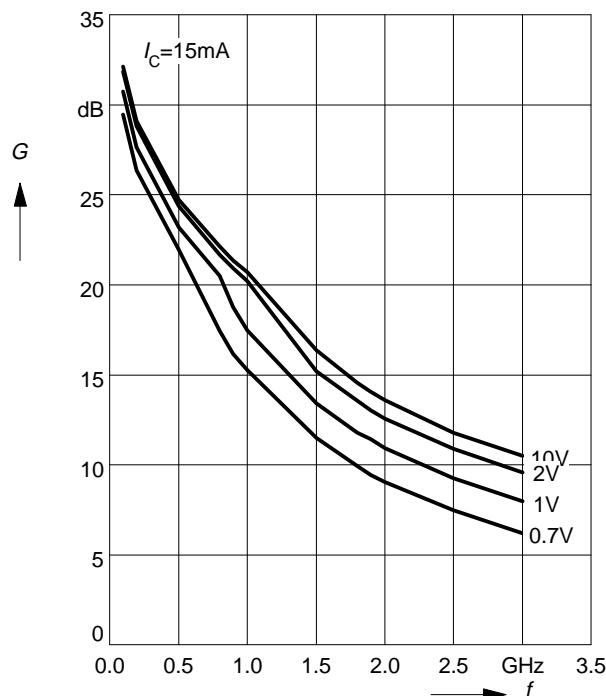
$|S_{21}|^2 = f(V_{CE})$ : -----

f = Parameter



**Power Gain**  $G_{ma}$ ,  $G_{ms} = f(f)$

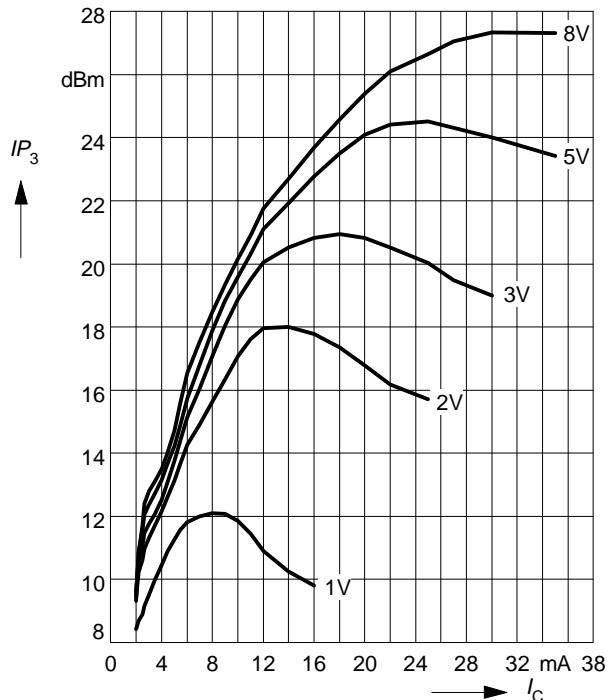
$V_{CE}$  = Parameter



**Intermodulation Intercept Point**  $IP_3 = f(I_C)$

(3rd order, Output,  $Z_S = Z_L = 50\Omega$ )

$V_{CE}$  = Parameter,  $f = 900\text{MHz}$



**Power Gain**  $|S_{21}|^2 = f(f)$

$V_{CE}$  = Parameter

