Plastic Medium-Power Silicon PNP Darlingtons

 \ldots for use as output devices in complementary general–purpose amplifier applications.

- High DC Current Gain —
 hFE = 750 (Min) @ IC = 1.5 and 2.0 Adc
- Monolithic Construction
- BD676, 676A, 678, 678A, 680, 680A, 682 are complementary with BD675, 675A, 677, 677A, 679, 679A, 681
- BD 678, 678A, 680, 680A are equivalent to MJE 700, 701, 702, 703

MAXIMUM RATING

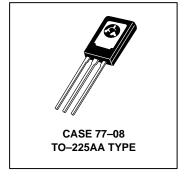
Rating	Symbol	BD676 BD676A	BD678 BD678A	BD680 BD680A	BD682	Unit
Collector–Emitter Voltage	VCEO	45	60	80	100	Vdc
Collector-Base Voltage	VCB	45	60	80	100	Vdc
Emitter-Base Voltage	V _{EB}	5.0			Vdc	
Collector Current	IC	4.0			Adc	
Base Current	ΙΒ	0.1			Adc	
Total Device Dissipation @ T _C = 25 °C Derate above 25 °C	P _D	40 0.32			Watts W/°C	
Operating and Storage Junction Temperating Range	TJ, T _{Stg}	-55 to +150			°C	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θJC	3.13	°C/W



4.0 AMPERE DARLINGTON POWER TRANSISTORS PNP SILICON 45, 60, 80, 100 VOLTS 40 WATTS



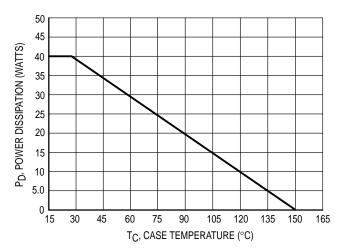


Figure 1. Power Temperature Derating

BD676 BD676A BD678 BD678A BD680 BD680A BD682

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS					•
Collector–Emitter Breakdown Voltage ⁽¹⁾ (IC = 50 mAdc, IB = 0)	BD676, 676A BD678, 678A BD680, 680A BD682	BVCEO	45 60 80 100		Vdc
Collector Cutoff Current (V_{CE} = Half Rated V_{CEO} , I_B = 0)		ICEO	_	500	μAdc
Collector Cutoff Current ($V_{CB} = Rated BV_{CEO}, I_{E} = 0$) ($V_{CB} = Rated BV_{CEO}. I_{E} = 0, T_{C} = 100^{\circ}C$)		I _{CBO}	_ _	0.2 2.0	mAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)		I _{EBO}	_	2.0	mAdc
ON CHARACTERISTICS		_			
DC Current $Gain(1)$ (I _C = 1.5 Adc, V _{CE} = 3.0 Vdc) (I _C = 2.0 Adc, V _{CE} = 3.0 Vdc)	BD676, 678, 680, 682 BD676A, 678A, 680A	hFE	750 750		
Collector–Emitter Saturation Voltage ⁽¹⁾ (I _C = 1.5 Adc, I _B = 30 mAdc) (I _C = 2.0 Adc, I _B = 40 mAdc)	BD678, 680, 682 BD676A, 678A, 680A	VCE(sat)	_ _	2.5 2.8	Vdc
Base–Emitter On Voltage(1) (IC = 1.5 Adc, VCE = 3.0 Vdc) (IC = 2.0 Adc, VCE = 3.0 Vdc)	BD678, 680, 682 BD676A, 678A, 680A	V _{BE(on)}	_ _	2.5 2.5	Vdc
DYNAMIC CHARACTERISTICS					
Small-Signal Current Gain (I _C = 1.5 Adc, V _{CE} = 3.0 Vdc, f =	= 1.0 MHz)	h _{fe}	1.0		_

⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

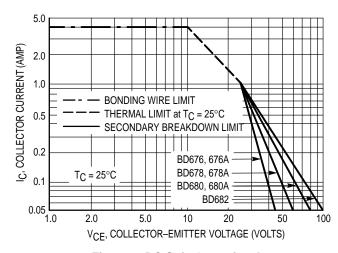


Figure 2. DC Safe Operating Area

There are two limitations on the power handling ability of a transistor average junction temperature and secondary breakdown. Safe operating area curves indicate $I_{\text{C}} - V_{\text{CE}}$ limits of the transistor that must be observed for reliable operation; e.g., the transistor must not be subjected to greater dissipation than the curves indicate.

At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by secondary breakdown.

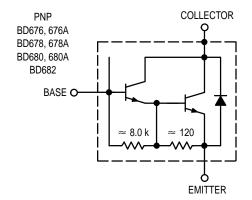
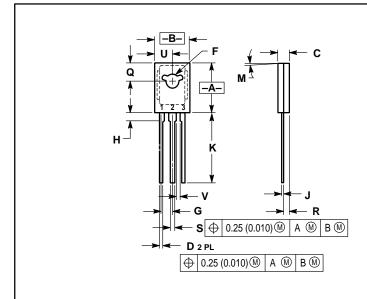


Figure 3. Darlington Circuit Schematic

BD676 BD676A BD678 BD678A BD680 BD680A BD682

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIMETERS			
DIM	MIN	MAX	MIN	MAX		
Α	0.425	0.435	10.80	11.04		
В	0.295	0.305	7.50	7.74		
С	0.095	0.105	2.42	2.66		
D	0.020	0.026	0.51	0.66		
F	0.115	0.130	2.93	3.30		
G	0.094 BSC		2.39 BSC			
Н	0.050	0.095	1.27	2.41		
J	0.015	0.025	0.39	0.63		
K	0.575	0.655	14.61	16.63		
M	5°	5° TYP		5° TYP		
Q	0.148	0.158	3.76	4.01		
R	0.045	0.055	1.15	1.39		
S	0.025	0.035	0.64	0.88		
U	0.145	0.155	3.69	3.93		
٧	0.040		1.02			

STYLE 1:
PIN 1. EMITTER
2. COLLECTOR
3. BASE

CASE 77-08 TO-225AA TYPE **ISSUE V**

BD676 BD676A BD678 BD678A BD680 BD680A BD682

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