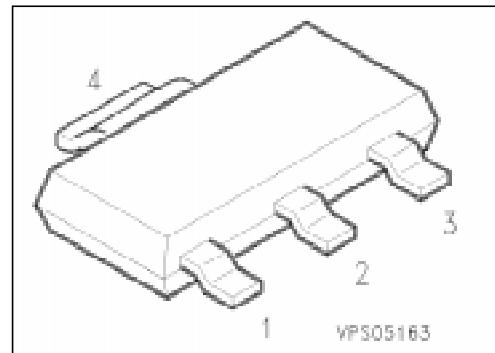


PNP Silicon AF Transistors

BCP 51
... **BCP 53**

- For AF driver and output stages
- High collector current
- Low collector-emitter saturation voltage
- Complementary types: BCP 54 ... BCP 56 (NPN)



Type	Marking	Ordering Code (tape and reel)	Pin Configuration				Package ¹⁾
			1	2	3	4	
BCP 51	BCP 51	Q62702-C2107	B	C	E	C	SOT-223
BCP 51-10	BCP 51-10	Q62702-C2109					
BCP 51-16	BCP 51-16	Q62702-C2110					
BCP 52	BCP 52	Q62702-C2146					
BCP 52-10	BCP 52-10	Q62702-C2112					
BCP 52-16	BCP 52-16	Q62702-C2113					
BCP 53	BCP 53	Q62702-C2147					
BCP 53-10	BCP 53-10	Q62702-C2115					
BCP 53-16	BCP 53-16	Q62702-C2116					

¹⁾ For detailed information see chapter Package Outlines.

Maximum Ratings

Parameter	Symbol	Values			Unit	
		BCP 51	BCP 52	BCP 53		
Collector-emitter voltage $R_{BE} \leq 1 \text{ k}\Omega$	V_{CEO} V_{CER}	45 45	60 60	80 100	V	
Collector-base voltage	V_{CB0}	45	60	100		
Emitter-base voltage	V_{EB0}	5			A	
Collector current	I_C	1				
Peak collector current	I_{CM}	1.5			mA	
Base current	I_B	100				
Peak base current	I_{BM}	200			°C	
Total power dissipation, $T_s = 124 \text{ }^{\circ}\text{C}^1)$	P_{tot}	1.5				
Junction temperature	T_j	150				
Storage temperature range	T_{stg}	– 65 ... + 150				

Thermal Resistance

Junction - ambient ¹⁾	$R_{th JA}$	≤ 72	K/W
Junction - soldering point	$R_{th JS}$	≤ 17	

¹⁾ Package mounted on epoxy pcb 40 mm × 40 mm × 1.5 mm/6 cm² Cu.

Electrical Characteristicsat $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC characteristics

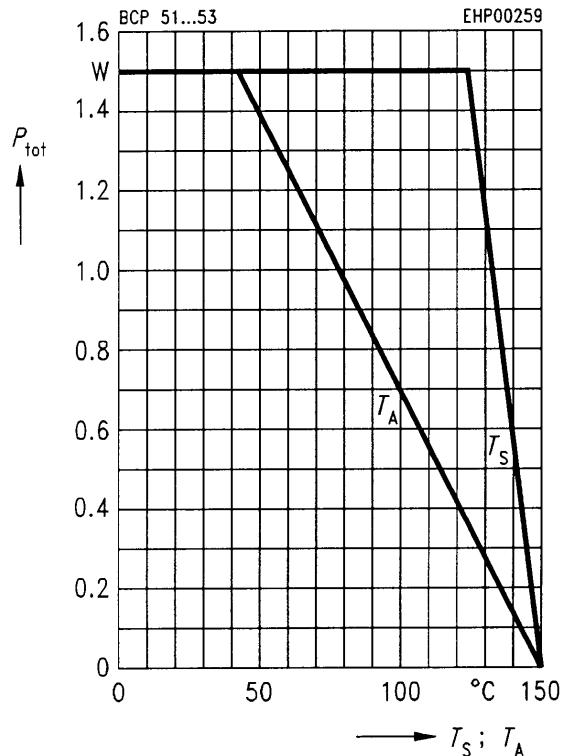
Collector-emitter breakdown voltage $I_C = 10 \text{ mA}, I_B = 0$	$V_{(\text{BR})\text{CE}0}$	45	—	—	V
BCP 51		60	—	—	
BCP 52		80	—	—	
BCP 53					
Collector-base breakdown voltage $I_C = 100 \mu\text{A}, I_B = 0$	$V_{(\text{BR})\text{CB}0}$	45	—	—	
BCP 51		60	—	—	
BCP 52		100	—	—	
BCP 53					
Emitter-base breakdown voltage $I_E = 10 \mu\text{A}, I_C = 0$	$V_{(\text{BR})\text{EB}0}$	5	—	—	
Collector-base cutoff current $V_{CB} = 30 \text{ V}, I_E = 0$	I_{CBO}	—	—	100	nA
$V_{CB} = 30 \text{ V}, I_E = 0, T_A = 150^\circ\text{C}$		—	—	20	μA
Emitter-base cutoff current $V_{EB} = 5 \text{ V}, I_C = 0$	I_{EBO}	—	—	10	μA
DC current gain ¹⁾ $I_C = 5 \text{ mA}, V_{CE} = 2 \text{ V}$	h_{FE}	25	—	—	—
$I_C = 150 \text{ mA}, V_{CE} = 2 \text{ V}$		40	—	250	
BCP 51/BCP 52/BCP 53		63	100	160	
BCP 51/BCP 52/BCP 53-10		100	160	250	
BCP 51/BCP 52/BCP 53-16		25	—	—	
$I_C = 500 \text{ mA}, V_{CE} = 2 \text{ V}$					
Collector-emitter saturation voltage ¹⁾ $I_C = 500 \text{ mA}, I_B = 50 \text{ mA}$	V_{CEsat}	—	—	0.5	V
Base-emitter voltage ¹⁾ $I_C = 500 \text{ mA}, V_{CE} = 2 \text{ V}$	V_{BE}	—	—	1	

AC characteristics

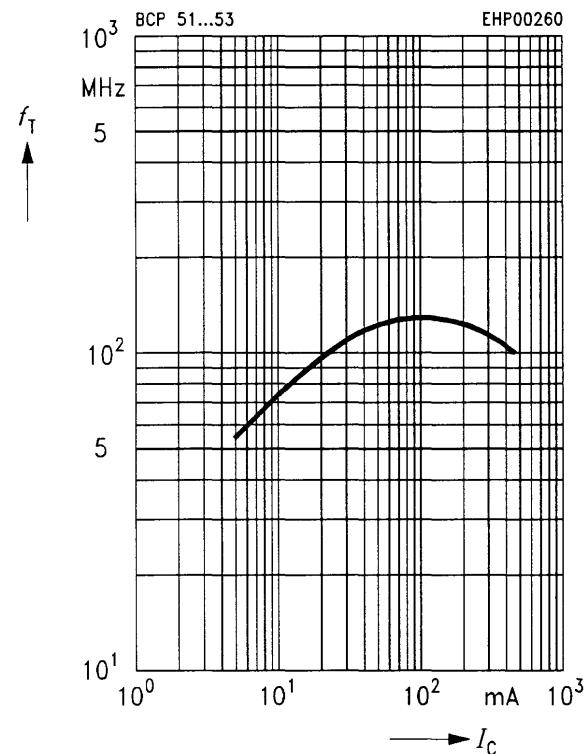
Transition frequency $I_C = 50 \text{ mA}, V_{CE} = 10 \text{ V}, f = 100 \text{ MHz}$	f	—	125	—	MHz
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¹⁾ Pulse test conditions: $t \leq 300 \mu\text{s}$, $D = 2\%$.

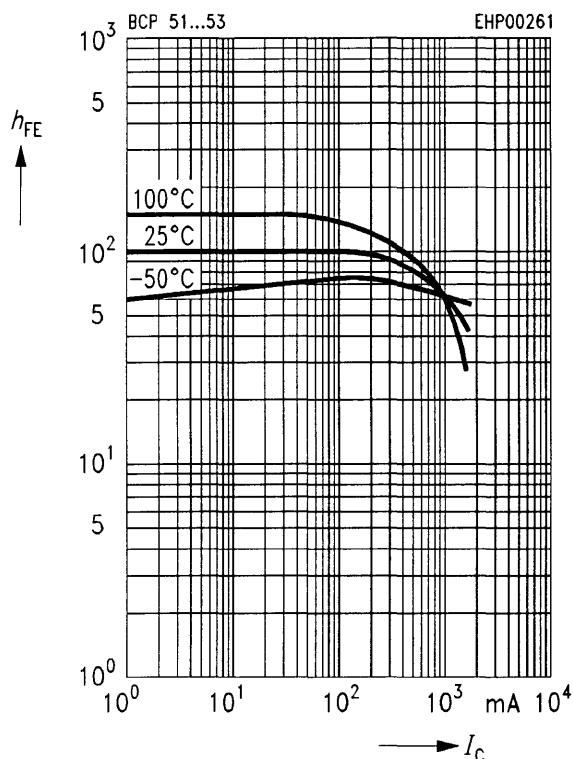
Total power dissipation $P_{\text{tot}} = f(T_A^*; T_S)$
 * Package mounted on epoxy



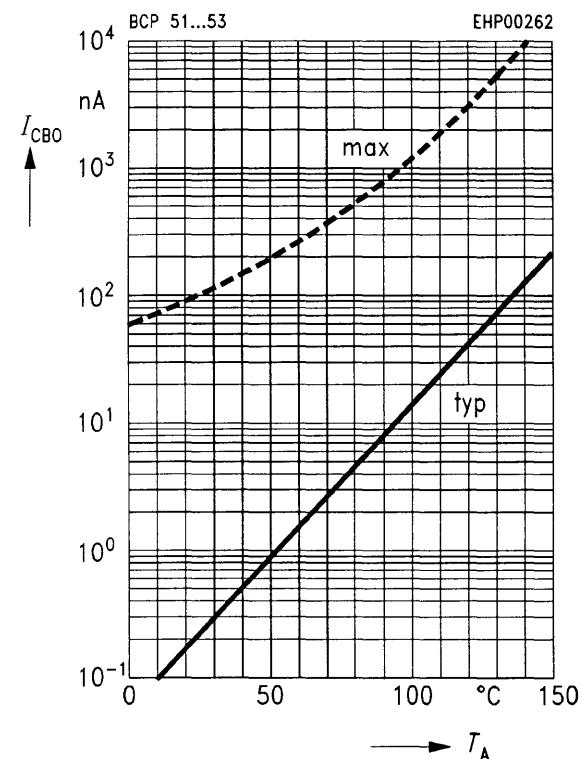
Transition frequency $f_T = f(I_C)$
 $V_{\text{CE}} = 10 \text{ V}$



DC current gain $h_{\text{FE}} = f(I_C)$
 $V_{\text{CE}} = 2 \text{ V}$



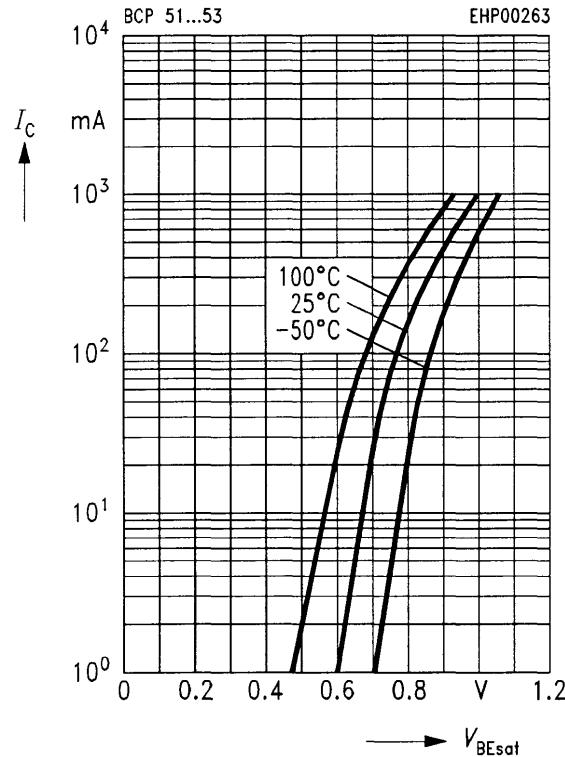
Collector cutoff current $I_{\text{CBO}} = f(T_A)$
 $V_{\text{CB}} = 30 \text{ V}$



Base-emitter saturation voltage

$$I_C = f(V_{BEsat})$$

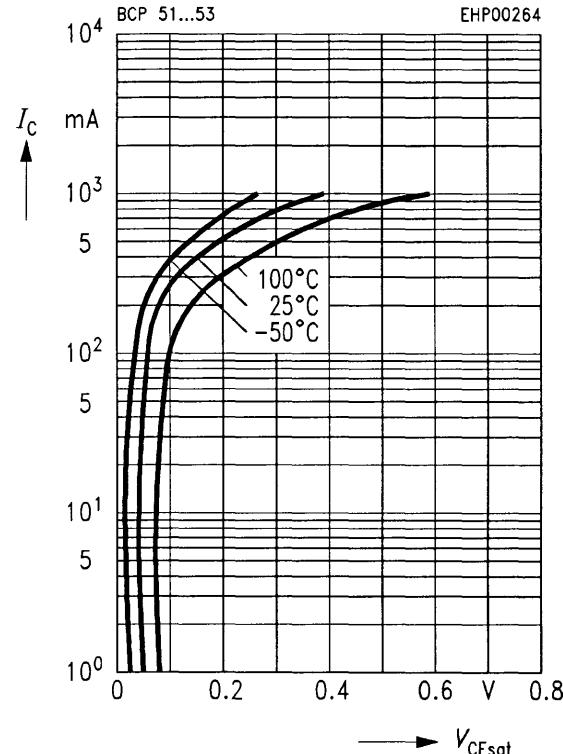
$$h_{FE} = 10$$



Collector-emitter saturation voltage

$$I_C = f(V_{CEsat})$$

$$h_{FE} = 10$$



Permissible pulse load $P_{tot\ max}/P_{tot\ DC} = f(t_p)$

