

Features

- 2 channels of DC motor BTL driver
- 2 channels of BTL driver for coils with current feedback
- 1 channels forward/reverse control DC motor driver
- Built-in suspension function.
- Built-in thermal shutdown circuit.
- Operating voltage: 4.5V~13.2V

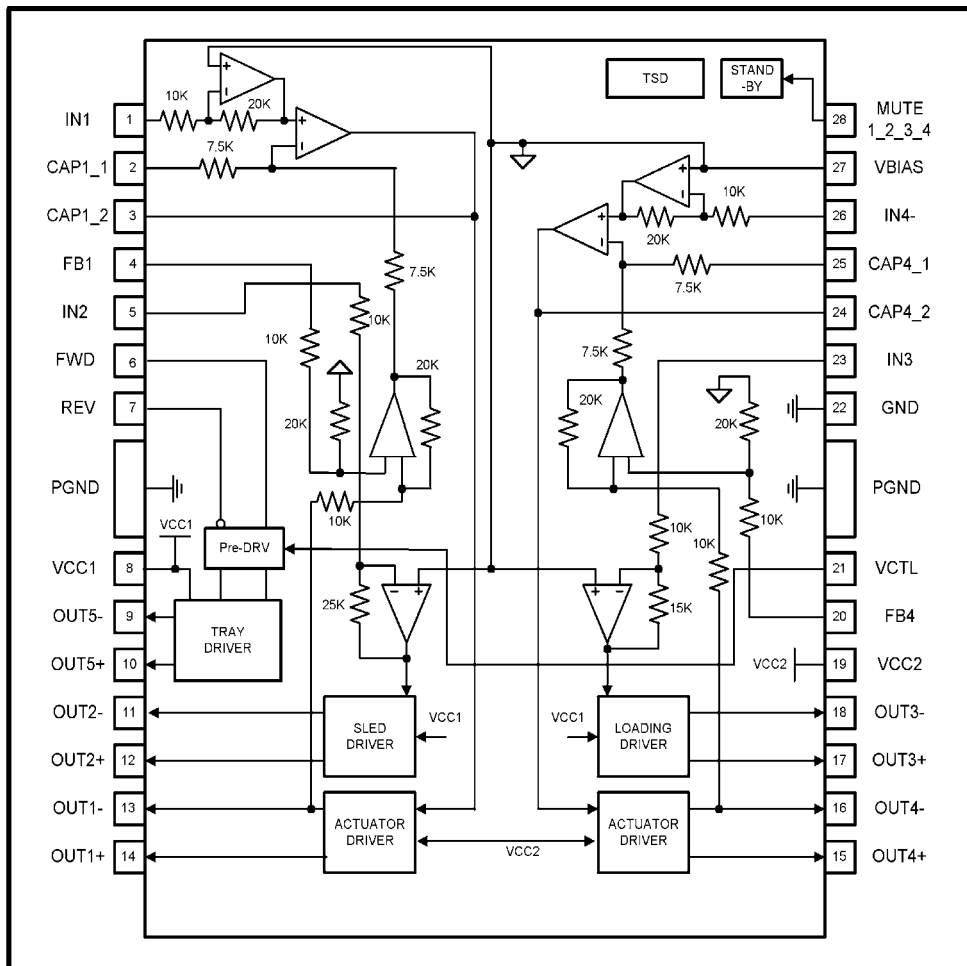
Description

The AT5665 is a 5-channel BTL driver IC. Two of them can drive DC motors and two can drive coils, such as the focus and tracking actuators of a CD-ROM/DVD-ROM/DVD-Player system, with current feedback. It also built-in one channel bi-direction DC motor driver for Tray.

Applications

CD-ROM/DVD-ROM/CD-RW drives
DVD player.

Block Diagram



Aimtron reserves the right without notice to change this circuitry and specifications.

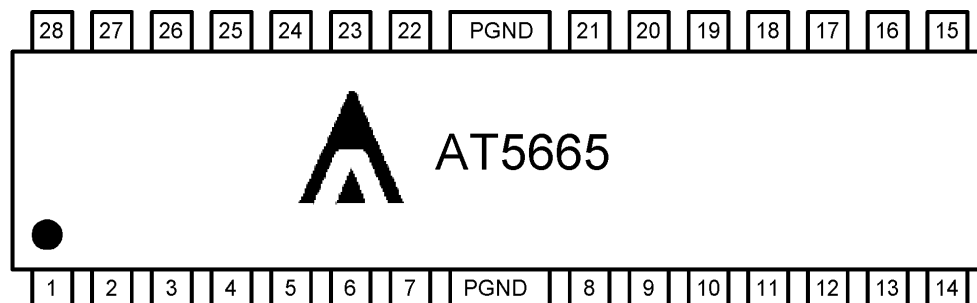
Pin Descriptions

Pin No.	Pin name	Function
1	IN1	CH1(Focus coil) input
2	CAP1_1	CH1 error amplifier filter capacitor connection 1
3	CAP1_2	CH1 error amplifier filter capacitor connection 2
4	FB1	CH1(Focus coil) feedback pin
5	IN2	CH2(Sled driver) input
6	FWD	CH5 forward input
7	REV	CH5 reverse input
8	VCC1	Power Supply 1
9	OUT5-	CH5(Tray driver) output(-)
10	OUT5+	CH5(Tray driver) output(+)
11	OUT2-	CH2(Sled driver) output (-)
12	OUT2+	CH2(Sled driver) output (+)
13	OUT1-	CH1(Focus coil) output (-)
14	OUT1+	CH1(Focus coil) output (+)
15	OUT4+	CH4(Tracking coil) output (+)
16	OUT4-	CH4(Tracking coil) output (-)
17	OUT3+	CH3(Loader) output (+)
18	OUT3-	CH3(Loader) output (-)
19	VCC2	Power Supply 2
20	FB4	CH4(Tracking coil) feedback pin
21	VCTL	CH5 Speed control input
22	GND	Ground
23	IN3	CH3(Loader) input
24	CAP4_2	CH4 error amplifier filter capacitor connection 2
25	CAP4_1	CH4 error amplifier filter capacitor connection 1
26	IN4-	CH4 (Tracking driver) input
27	VBIAS	VREF input pin
28	MUTE 1 2 3 4	Mute control for CH1,Ch2,CH3,CH4

Notes: The indicated polarities for the output pins are under the condition that all inputs are (+).

The power supplies for the driver output are PVcc1 for the loader, PVcc2 for focus and tracking drivers, and Vcc for pre-block and sled driver. Therefore, make sure $V_{cc} \geq PV_{cc1,2}$.

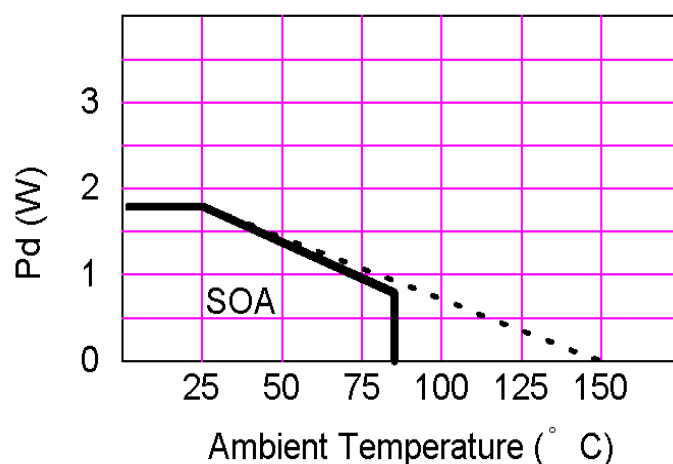
Pin Out



Ordering Information

Part number	Package	Remarks
AT5665	28-pin HSOP	AT5665H

Power Dissipation (Ta = 25°C)



Condition: mount on 50x50 mm² t=1.0mm paper phenol PCB

Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	unit
Power supply voltage	VCC1,2	13.5	V
Power dissipation	P _d	1.7	W
Operating temperature	T _{opr}	-30~+80	°C
Storage temperature	T _{stg}	-55~+150	°C

NOTE:*

1. Power dissipation reduces 13.6mW / °C for using above Ta=25°C
2. Do not exceed P_d and SOA.

Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Limits	unit
Power supply voltage	VCC1	4.0~13.2	V
	VCC2	4.0~Vcc1	V

Electrical characteristics

(unless otherwise noted, $T_a = 25^\circ\text{C}$, $V_{CC1} = 12\text{V}$, $V_{CC2} = 5\text{V}$, $V_{BIAS} = 2.5\text{V}$, $R_{L1} = R_{L2} = 8\Omega$, $R_{L3} = R_{L4} = 12\Omega$)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Quiescent current	I_{OC}	-	18	27	mA	No Load
Mute quiescent current	I_{OSB}	-	-	5	mA	CH5 active
Mute ON voltage	V_{SBON}	0	-	0.5	V	
Mute OFF voltage	V_{SBOFF}	2.0	-	-	V	
<Actuator driver>						
Output current offset	$I_{O1,2}$	-6	-	6	mA	
Maximum output voltage	$V_{O1,2}$	3.6	4.0	-	V	$V_{IN} = V_{BIAS} \pm 1.5\text{V}$
Gain	Gm	1.3	1.5	1.7	A/V	$V_{IN} = V_{BIAS} \pm 0.2\text{V}$
<Sled motor driver>						
Output voltage offset	V_{OFFSL}	-100	0	100	mV	
Maximum output voltage	V_{O3MAX}	7.5	9.0	-	V	$V_{IN} = V_{BIAS} \pm 1.5\text{V}$
Closed loop gain	G_{VSL}	18.0	20.0	22.0	dB	$V_{IN} = V_{BIAS} \pm 0.2\text{V}$
<Loader motor driver>						
Output voltage offset	V_{OFFLD}	-50	0	50	mV	
Maximum output voltage	V_{O4max1}	7.5	9.0	-	V	$V_{IN} = V_{BIAS} \pm 2.0\text{V}$
Maximum output voltage	V_{O4max2}	3.6	4.0	-	V	$V_{IN} = V_{BIAS} \pm 1.5\text{V}$ $V_{CC1} = 5\text{V}$
Closed loop gain	G_{VLD}	13.5	15.5	17.5	dB	$V_{IN} = V_{BIAS} \pm 0.2\text{V}$
Gain error	ΔG_{VLD}	0	1	2	dB	$V_{IN} = V_{BIAS} \pm 0.2\text{V}$
<Tray motor driver>						
Maximum output voltage	V_{O5max1}	-	6.0	-	V	$R_{L5} = 45\Omega$, $R_{VCTL} = 400\Omega$,
		3.8	-	4.2	V	$R_{L5} = 45\Omega$, $R_{VCTL} = 0\Omega$, $V_{CC1} = V_{CC2} = 5\text{V}$
Output voltage offset	V_{OFFTR}	-50	0	50	mV	
Input high level voltage	V_{IH}	2.0	-	-	V	
Input low level voltage	V_{IL}	-	-	0.8	V	

Tray Motor Driver

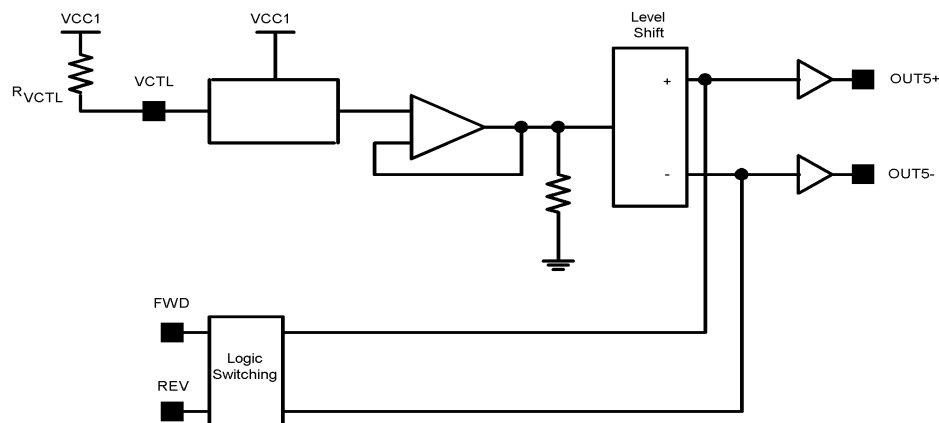


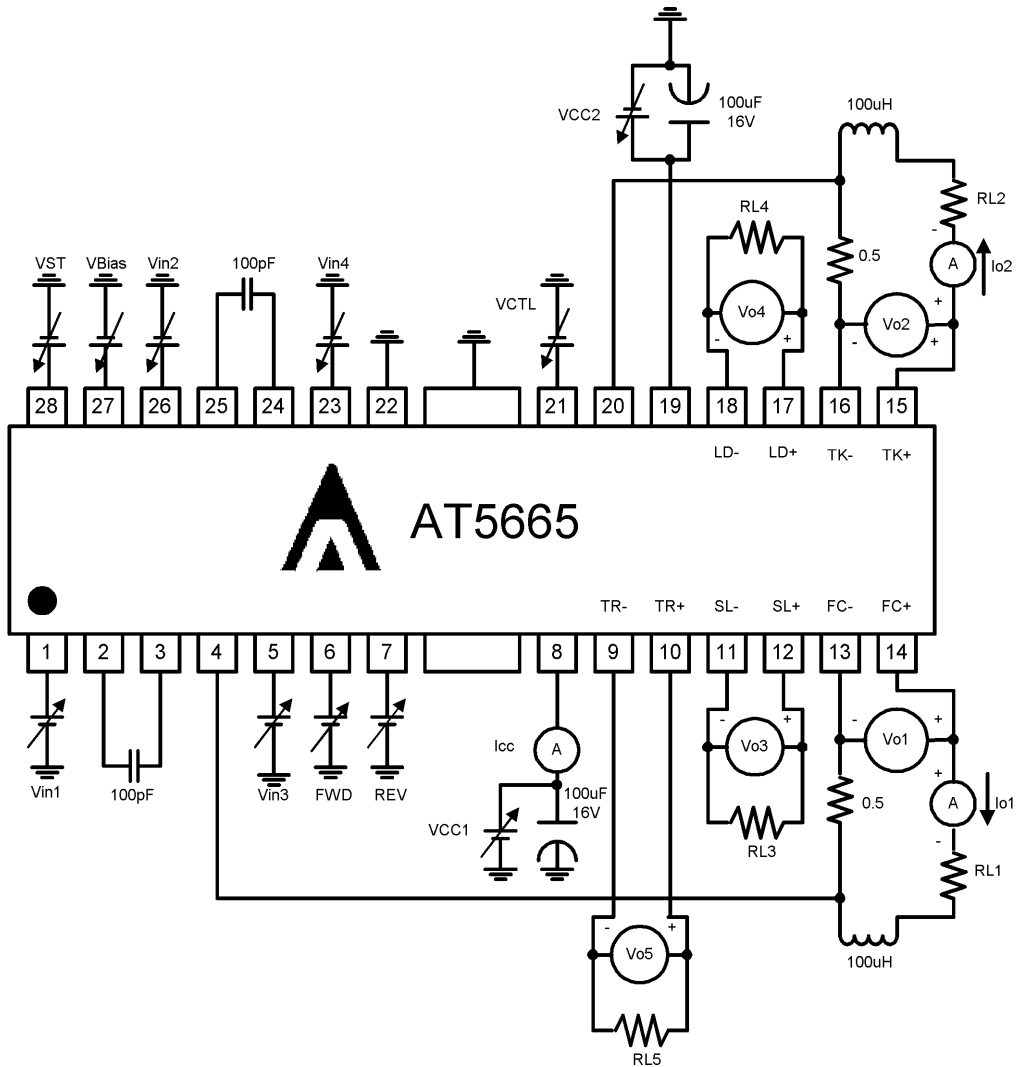
Fig1.

Input		Output		
FWD	REV	OUT5+	OUT5-	Status
High	High	Low	Low	Break
High	Low	High	Low	Forward
Low	High	Low	High	Reverse
Low	Low	Open	Open	Standby

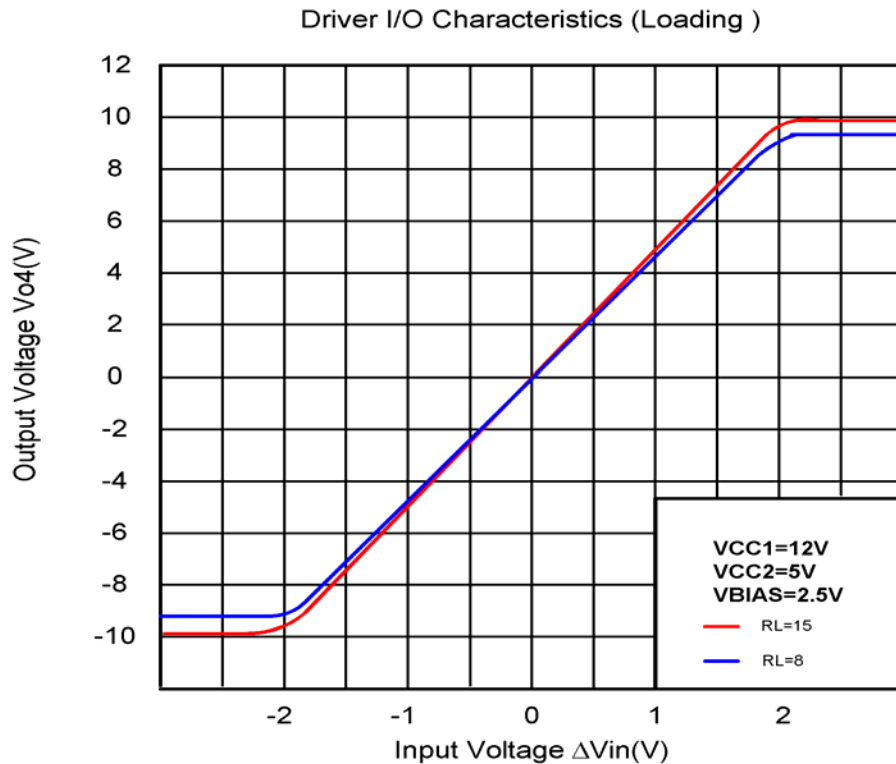
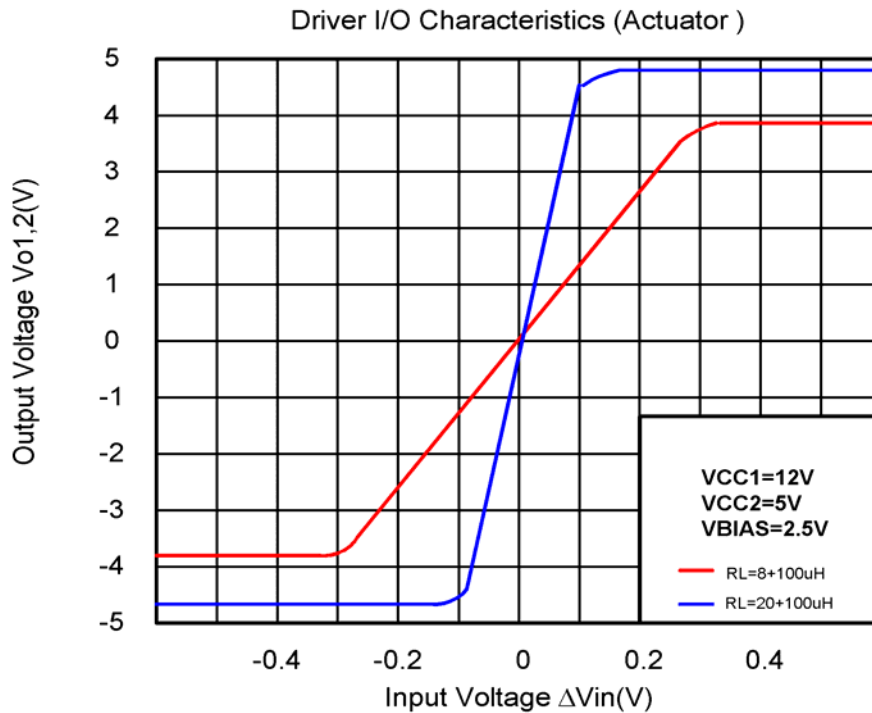
Tray Motor Speed Control

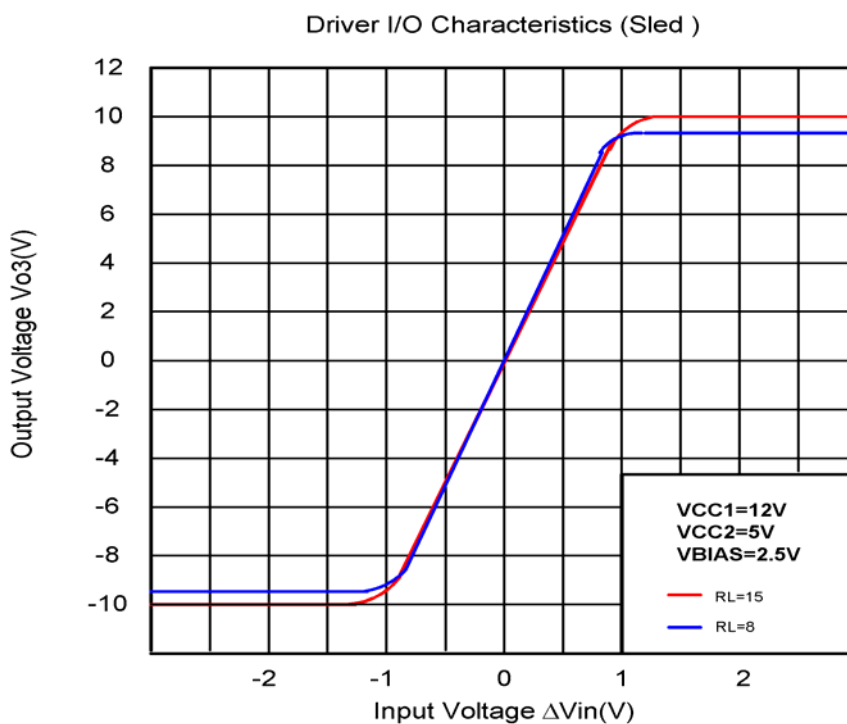
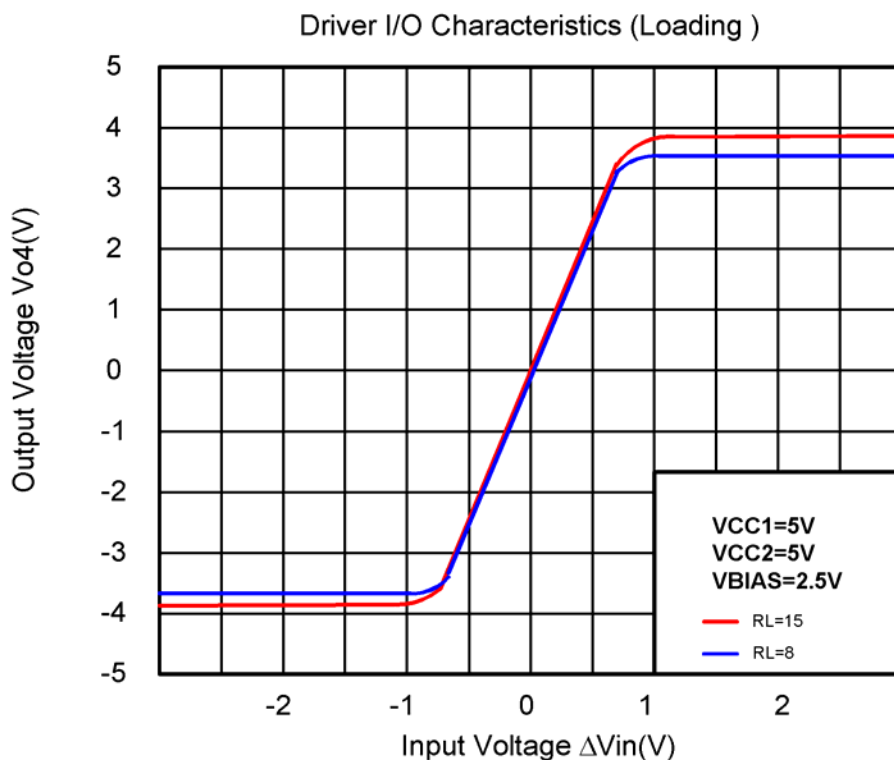
The amplitude of the output voltage is controlled by VCTL(pin21). If you want to control differential output voltage of the tray, insert external resistor R_{VCTL} as shown in figure 1.

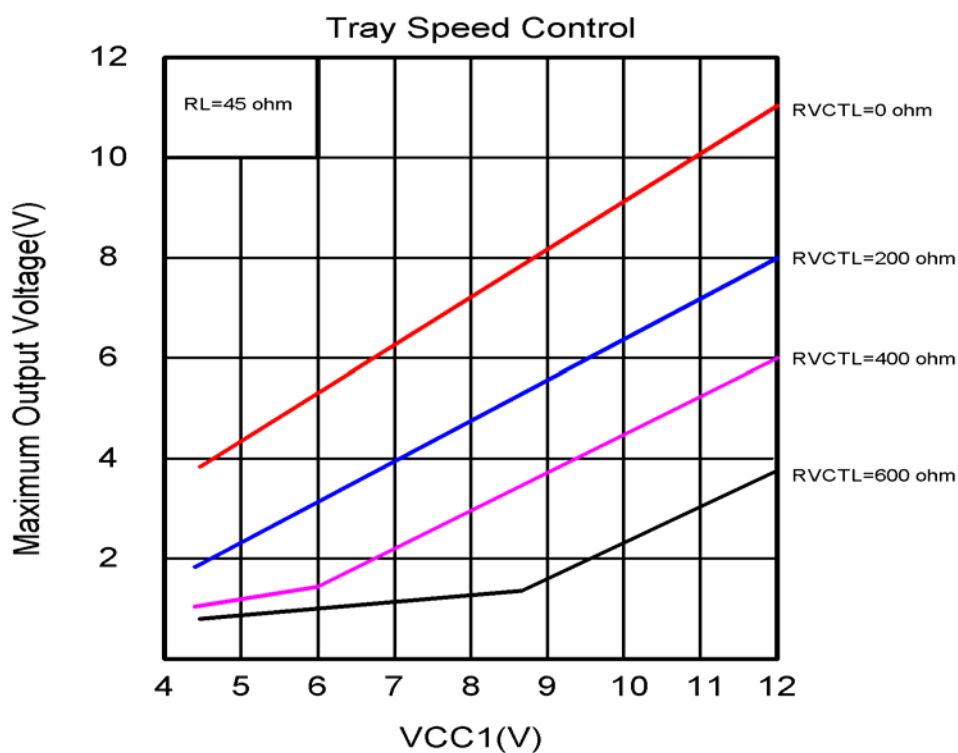
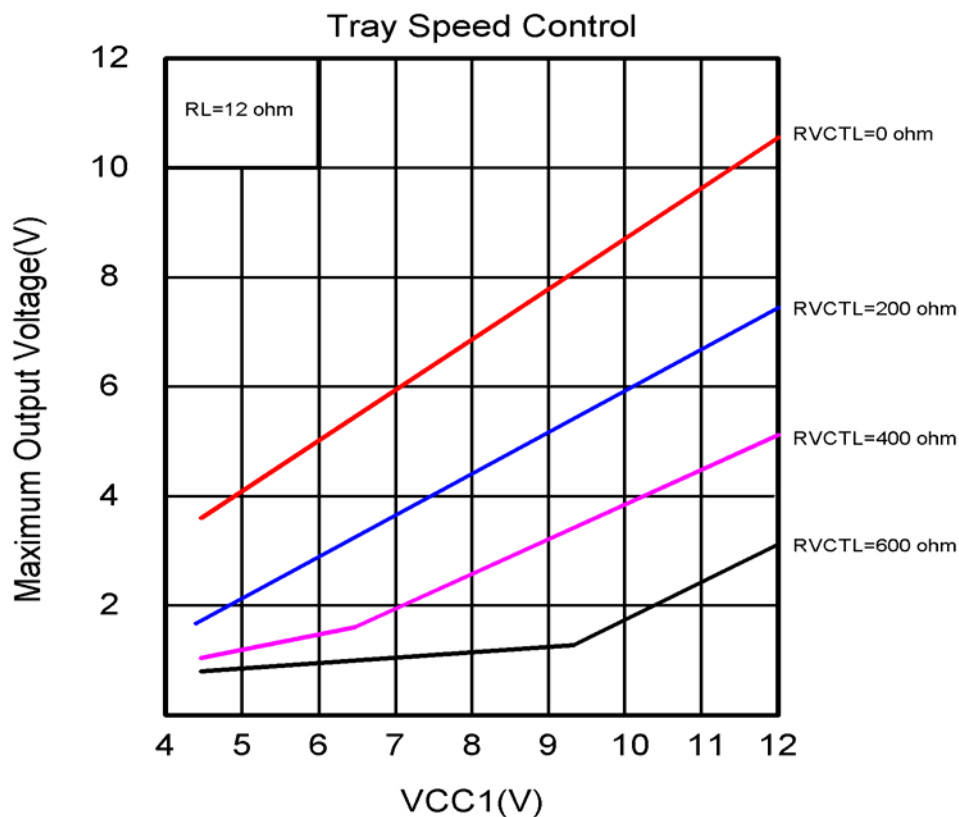
Test Circuit



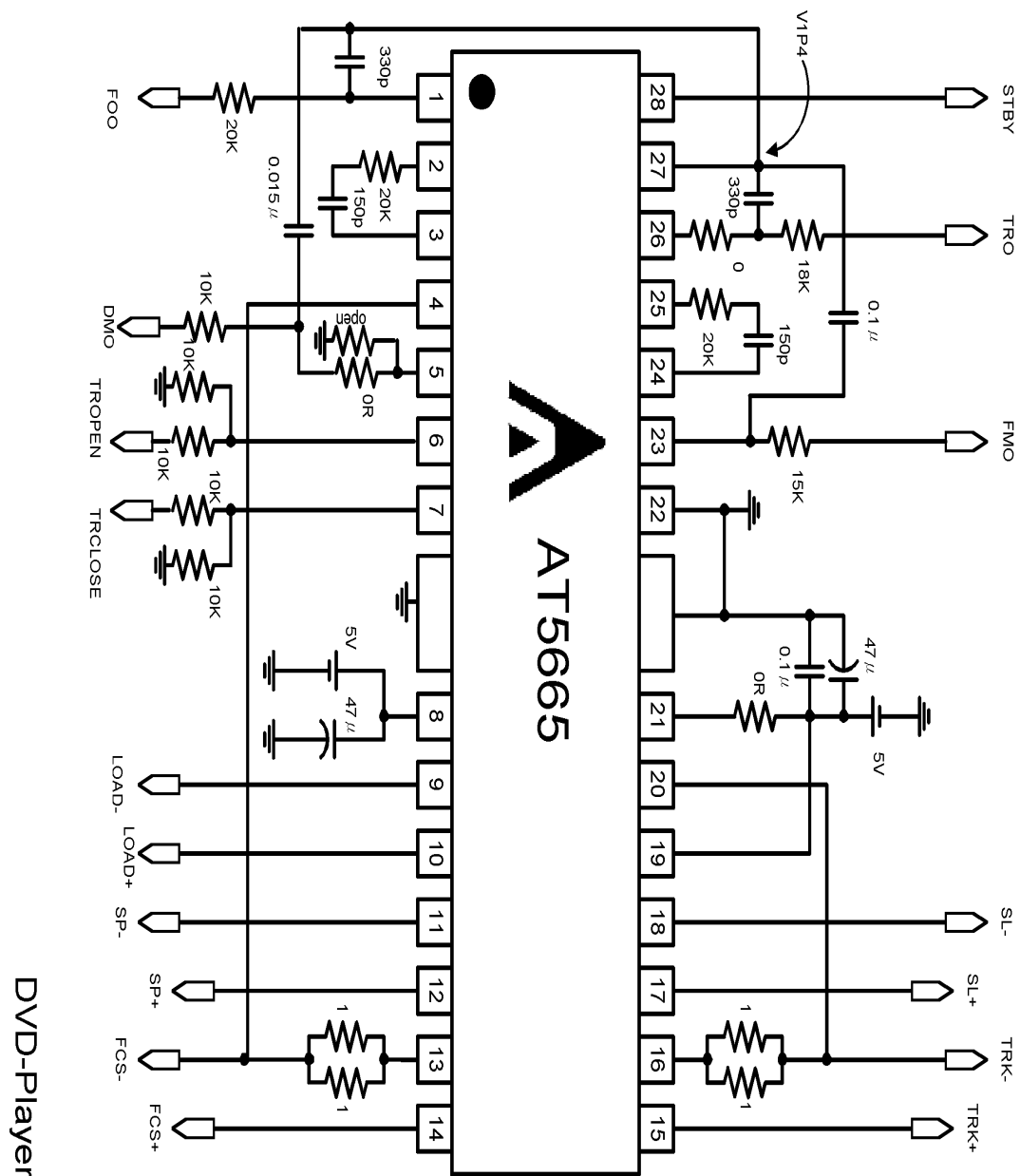
Typical Curve



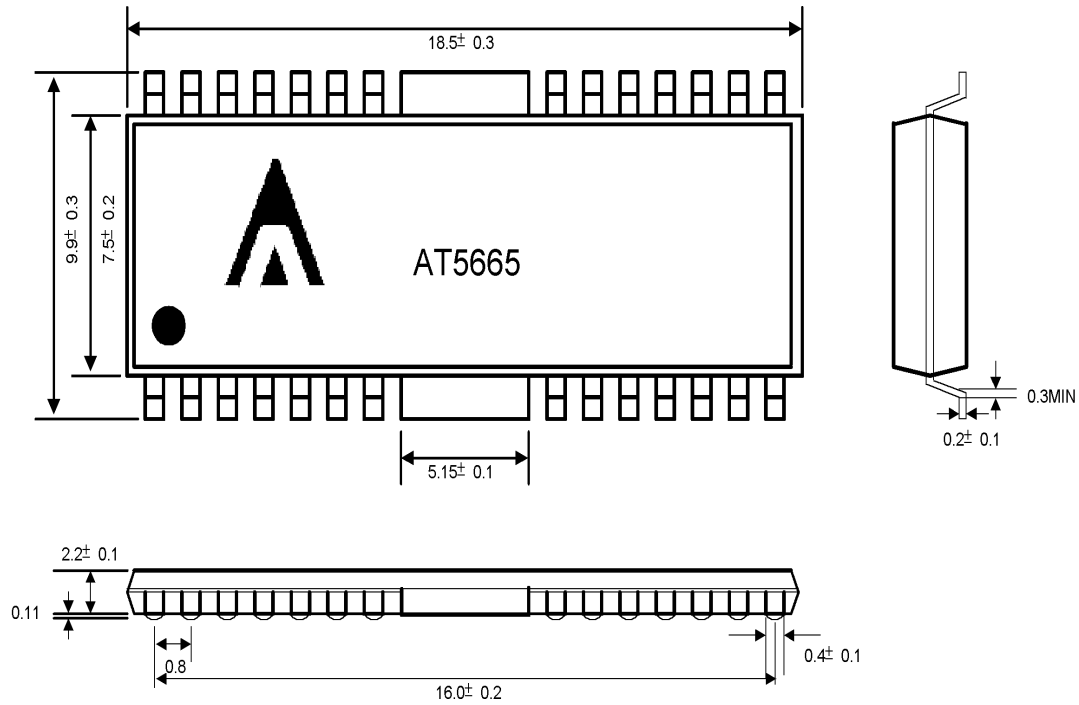




Application circuit

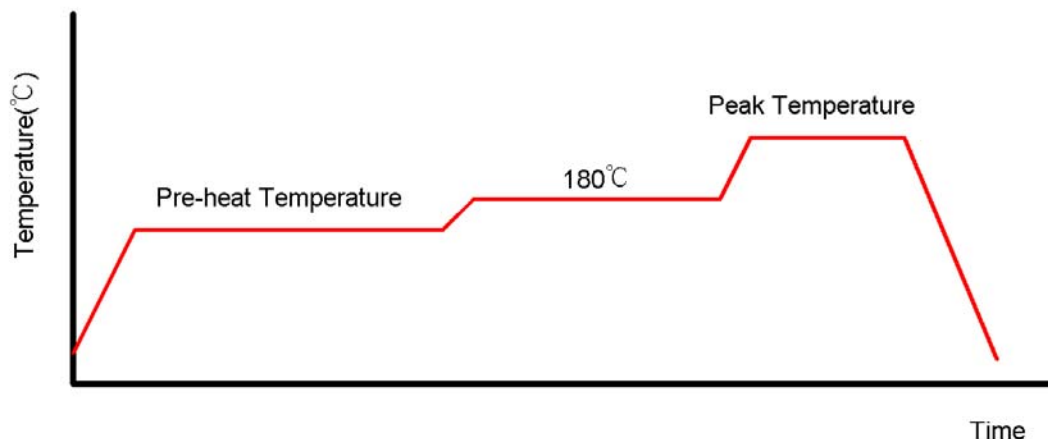


Package Outlines (units:mm): HSOP-28



Reflow Condition (IR/Convection or VPR Reflow)

Reference JEDEC Standard J-STD-020A



Classification Reflow Profiles

	Convection or IR/Convction	VPR
Average Heating Rate(180°C to peak)	5°C/second max.	10°C/second max.
Preheat Temperature(125±20°C)	120 seconds max.	
Temperature maintained above 180°C	10~150 seconds	
Time within 5°C of actual Peak Temperature	10~20 seconds	60 seconds
Peak Temperature Range(Note 1)	219~225°C or 235~240°C	219~225°C or 235~240°C
Cooling Rate	6°C /second max.	10°C/second max.
Time 25°C to Peak Temperature	6 minutes max.	

*1 The maximum peak temperatures for IR and VP reflow are depending on package dimensions.

Package Reflow Conditions

Pkg. Thickness ≥2.5mm and all bags	Pkg. Thickness <2.5mm and Pkg. Volume ≥350 mm ³	Pkg. Thickness <2.5mm and Pkg. Volume <350 mm ³
Convection 219~225°C		Convection 235~240°C
VPR 219~225°C		VPR 235~240°C
IR/Convection 219~225°C		IR/Convection 235~240°C