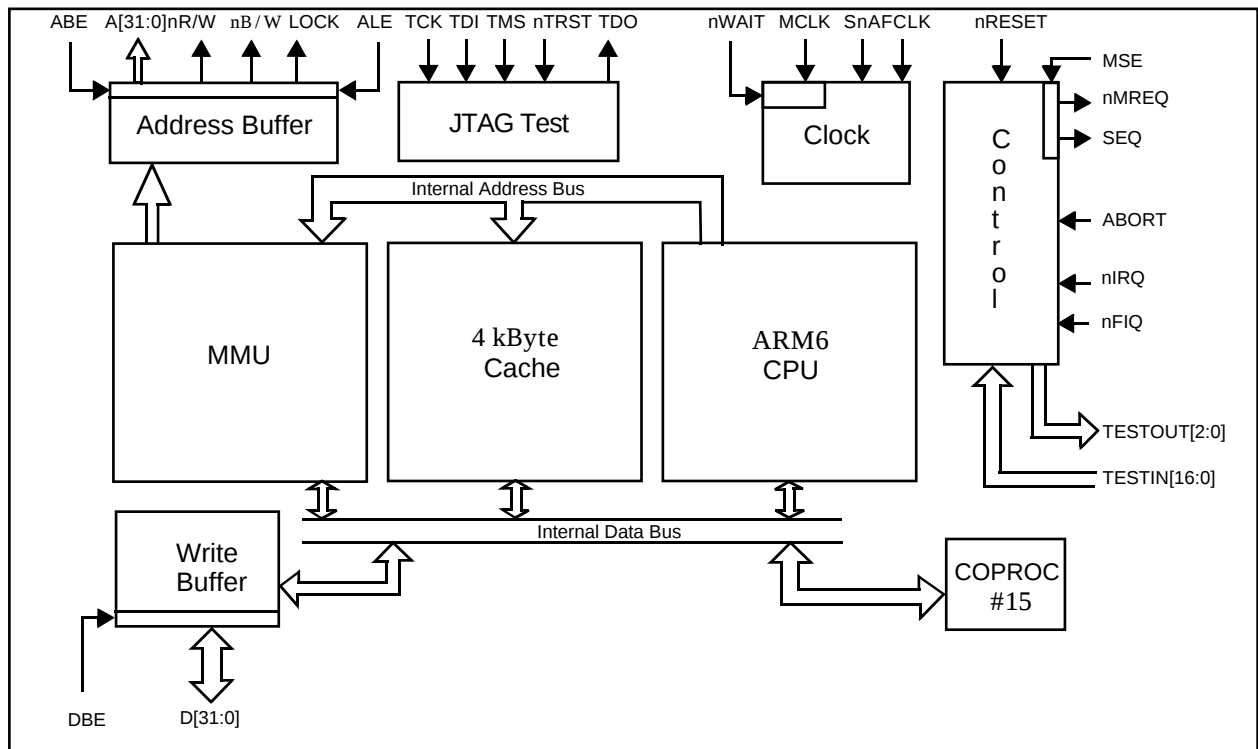


ARM610-B Overview

ARM610-B RISC Processor

ARM610-B is a general purpose 32-bit microprocessor with 4kByte cache, write buffer and Memory Management Unit (MMU) combined in a single chip. The ARM610-B offers high level RISC performance yet its fully static design ensures minimal power consumption - making it ideal for portable, low cost systems.

The innovative MMU supports a conventional two-level page-table structure and a number of extensions which make it ideal for embedded control and Object Oriented systems. This results in a high instruction throughput and impressive real-time interrupt response from a small and cost-effective chip.



- **High performance RISC**
30 Dhrystone MIPS at 5V/33MHz
- **Memory Management Unit (MMU)**
support for virtual memory systems
- **4 kByte instruction & data cache**
- **Write Buffer - enhancing performance**
- **Fully static operation - low power consumption**
ideal for power sensitive applications
- **Fast sub-microsecond interrupt response**
for real-time applications
- **Excellent high-level language support**
- **Big and Little Endian operating modes**
- **IEEE 1149.1 Boundary Scan**
- **144 pin Thin Quad Flat Pack (TQFP) package**

Applications:

- Personal computer devices e.g. PDAs
- High performance real time control systems
- Portable telecommunications
- Data communications equipment
- Consumer products
- Automotive

Ordering Code

P610ARM-B/KG/FPNR

For full details please contact your local GPS
Customer Service Centres

ARM610-B Overview

