



# **Ultrahigh-Speed Switching Applications**

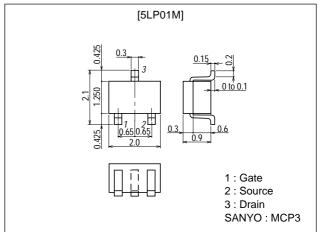
#### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

## **Package Dimensions**

unit:mm

2158



## **Specifications**

### **Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-50	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±10	V
Drain Current (DC)	I <sub>D</sub>		-0.07	Α
Drain Current (pulse)	I <sub>DP</sub>	PW≤10µs, duty cycle≤1%	-0.28	А
Allowable Power Dissipation	PD		0.15	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

#### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Drain-to-Source Breakdown Voltage	V(BR)DSS	$I_D=-1$ mA, $V_{GS}=0$	-50			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-50V, V <sub>GS</sub> =0			-10	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-100μA	-0.4		-1.4	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-40mA	70	100		mS
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	$I_D=-40$ mA, $V_{GS}=-4$ V		18	23	Ω
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-20mA, V <sub>GS</sub> =-2.5V		20	28	Ω
	R <sub>DS</sub> (on)3	I <sub>D</sub> =-5mA, V <sub>GS</sub> =-1.5V		30	60	Ω

Marking: XB Continued on next page.

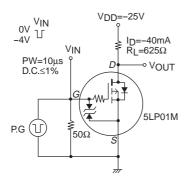
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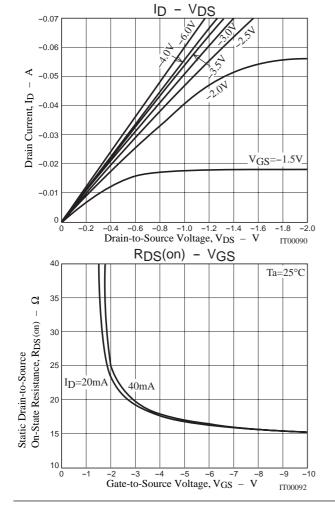
## **5LP01M**

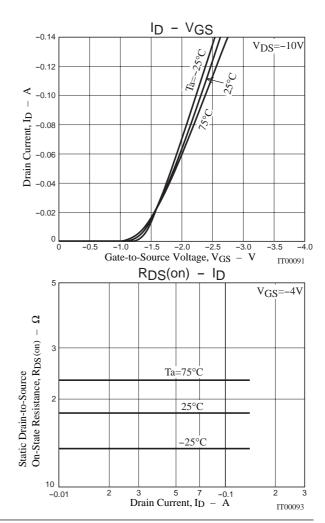
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		7.4		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		4.2		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		1.3		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		20		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		35		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		160		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		150		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-70mA		1.40		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-70mA		0.16		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-70mA		0.23		nC
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =-70mA, V <sub>GS</sub> =0		-0.85	-1.2	V

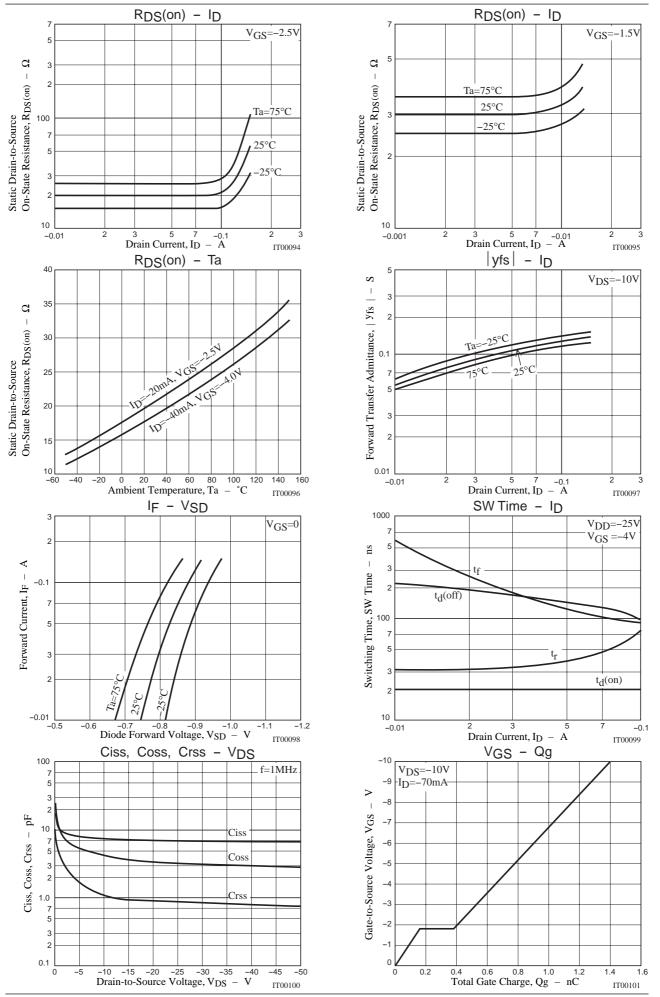
### **Switching Time Test Circuit**



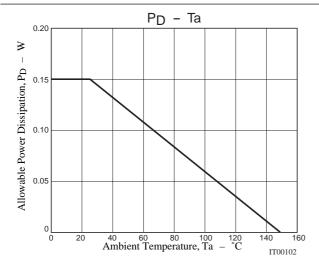




### **5LP01M**



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