

2SK2495

Silicon N-Channel Power F-MOS

■ Features

- Avalanche energy capability guaranteed
- High-speed switching
- No secondary breakdown

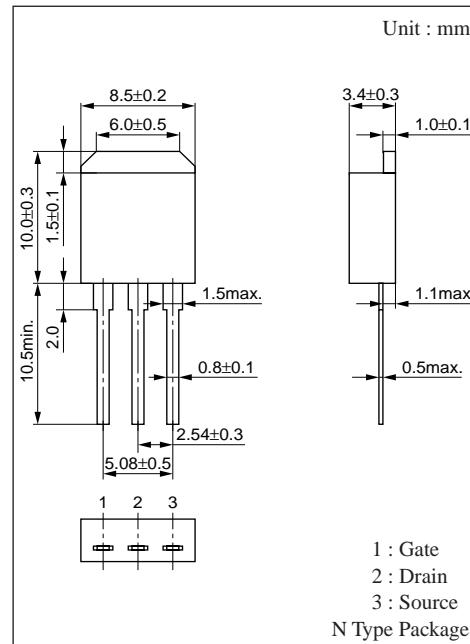
■ Applications

- High-speed switching (switching mode regulator)
- For high-frequency power amplification

■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

Parameter	Symbol	Rating	Unit
Drain-Source breakdown voltage	V_{DSS}	250	V
Gate-Source voltage	V_{GSS}	± 30	V
Drain current	DC	I_D	A
	Pulse	I_{DP}	A
Avalanche energy capability	EAS *	10	mJ
Allowable power dissipation	$T_C = 25^\circ\text{C}$	P_D	30
		$T_a = 25^\circ\text{C}$	1.3
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

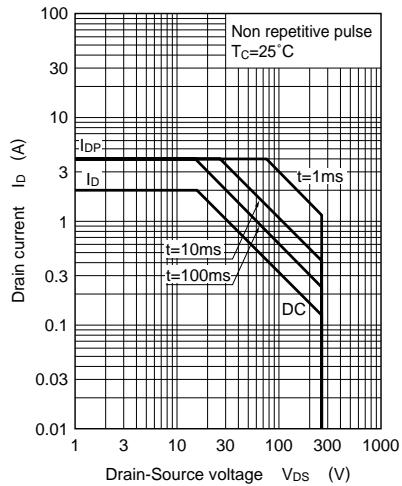
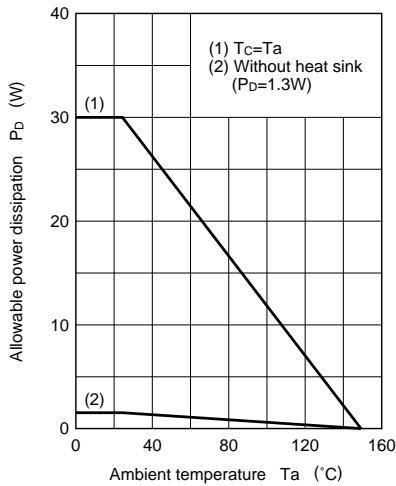
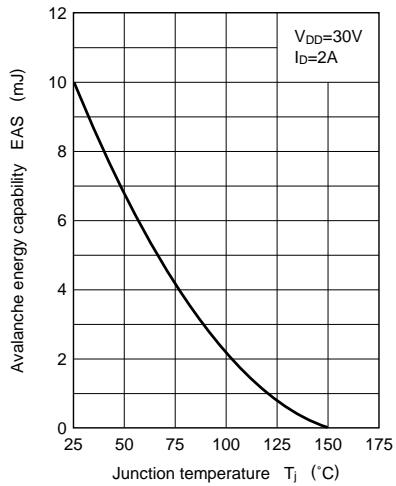
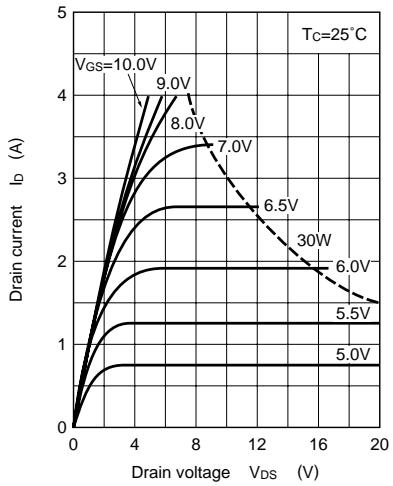
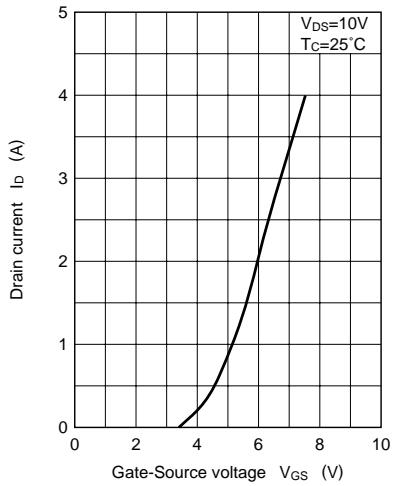
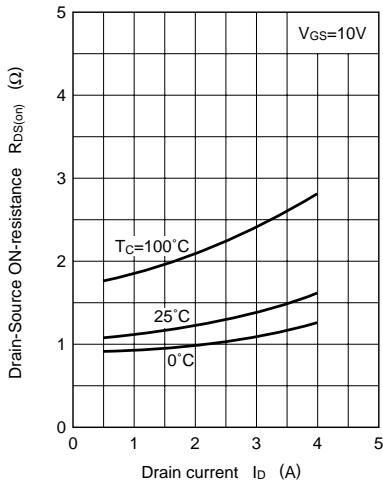
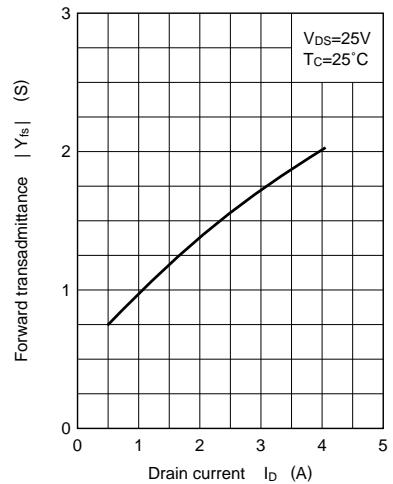
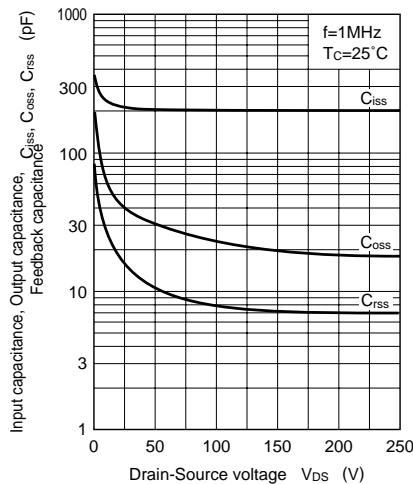
* $L = 5\text{mH}$, $I_L = 2\text{A}$, $V_{DD} = 30\text{V}$, 1 pulse



■ Electrical Characteristics ($T_c = 25^\circ\text{C}$)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source cut-off current	I_{DSS}	$V_{DS} = 200\text{V}$, $V_{GS} = 0$			100	μA
Gate-Source leakage current	I_{GSS}	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0$			± 1	μA
Drain-Source breakdown voltage	V_{DSS}	$I_D = 1\text{mA}$, $V_{GS} = 0$	250			V
Gate threshold voltage	V_{th}	$V_{DS} = 10\text{V}$, $I_D = 1\text{mA}$	1		5	V
Drain-Source ON-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 1\text{A}$		1.2	2	Ω
Forward transadmittance	$ Y_{fs} $	$V_{DS} = 25\text{V}$, $I_D = 1\text{A}$	0.5	1		S
Diode forward voltage	V_{DSF}	$I_{DR} = 2\text{A}$, $V_{GS} = 0$			-1.6	V
Input capacitance	C_{iss}	$V_{DS} = 10\text{V}$, $V_{GS} = 0$, $f = 1\text{MHz}$		220		pF
Output capacitance	C_{oss}			60		pF
Feedback capacitance	C_{rss}			20		pF
Turn-on time (delay time)	$t_{d(on)}$	$V_{DD} = 200\text{V}$, $I_D = 2\text{A}$ $V_{GS} = 10\text{V}$, $R_L = 100\Omega$		10		ns
Rise time	t_r			20		ns
Fall time	t_f			45		ns
Turn-off time (delay time)	$t_{d(off)}$			90		ns
Channel-Case heat resistance	$R_{th(ch-c)}$				4.17	$^\circ\text{C/W}$
Channel-Atmosphere heat resistance	$R_{th(ch-a)}$				96.2	$^\circ\text{C/W}$

Area of safe operation (ASO)

P_D – TaEAS – T_jI_D – V_{DS}I_D – V_{GS}R_{DS(on)} – I_D|Y_{fs}| – I_DC_{iss}, C_{oss}, C_{rss} – V_{DS}t_{d(on)}, t_r, t_f, t_{d(off)} – I_D