

2SK1611

Silicon N-Channel Power F-MOS

■ Features

- High avalanche energy capability
 - V_{GSS} : 30V guaranteed
 - Low $R_{DS(on)}$, high-speed switching characteristic

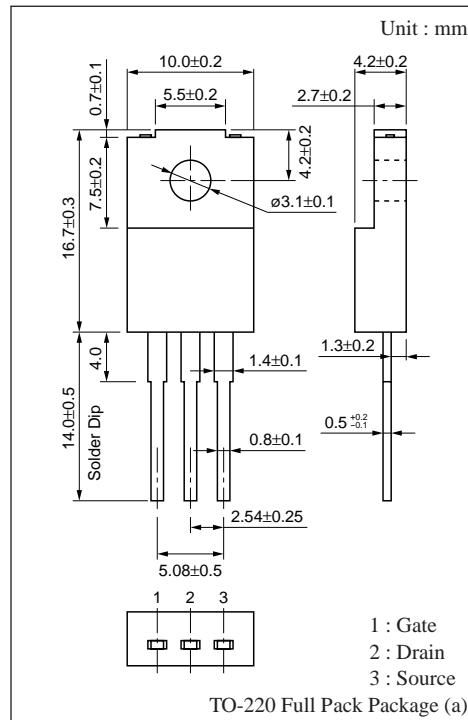
■ Applications

- High-speed switching (switching mode regulator, AC adaptor)
 - For high-frequency power amplification

■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

| Parameter | | Symbol | Rating | Unit |
|--------------------------------|-----------------------|------------------|-------------|------|
| Drain-Source breakdown voltage | | V _{DSS} | 800 | V |
| Gate-Source voltage | | V _{GSS} | ±30 | V |
| Drain current | DC | I _D | ±3 | A |
| | Pulse | I _{DP} | ±6 | A |
| Avalanche energy capability | | EAS* | 20 | mJ |
| Allowable power dissipation | T _C = 25°C | P _D | 50 | W |
| | T _a = 25°C | | 2 | |
| Channel temperature | | T _{ch} | 150 | °C |
| Storage temperature | | T _{stg} | -55 to +150 | °C |

* Single pulse



■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|--------------------------------|-----------------------|---|-----|-----|-----|------|
| Drain-Source cut-off current | I _{DSS} | V _{DS} = 640V, V _{GS} = 0 | | | 0.1 | mA |
| Gate-Source leakage current | I _{GSS} | V _{GS} =±30V, V _{DS} = 0 | | | ±1 | µ A |
| Drain-Source breakdown voltage | V _{DSS} | I _D =1mA, V _{GS} = 0 | 800 | | | V |
| Avalanche energy capability | EAS * | L= 4.5mH, I _D = 3A, V _{DD} = 50V | 20 | | | mJ |
| Gate threshold voltage | V _{th} | V _{DS} = 25V, I _D =1mA | 1 | | 5 | V |
| Drain-Source ON-resistance | R _{D(S(on))} | V _{GS} =10V, I _D = 2A | | 3.2 | 4 | Ω |
| Forward transadmittance | Y _{fs} | V _{DS} = 25V, I _D = 2A | 1.5 | 2.4 | | S |
| Input capacitance | C _{iss} | V _{DS} = 20V, V _{GS} = 0, f=1MHz | | 730 | | pF |
| Output capacitance | C _{oss} | | | 90 | | pF |
| Feedback capacitance | C _{rss} | | | 40 | | pF |
| Turn-on time | t _{on} | V _{GS} =10V, I _D = 2A V _{DD} = 200V, R _L =100Ω | | 40 | | ns |
| Fall time | t _f | | | 35 | | ns |
| Turn-off time (delay time) | t _{d(off)} | | | 105 | | ns |

* Avalanche energy capability

Test circuit

