

2SK3413LS

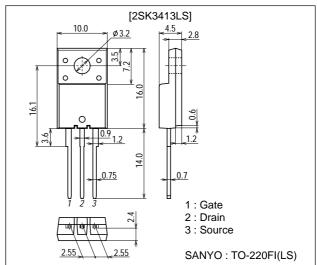
DC / DC Converter Applications

Features

- · Low ON-resistance.
- 4V drive.

Package Dimensions

unit : mm 2078C



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		25	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	100	Α
Allowable Power Dissipation	Do		2.0	W
	PD	Tc=25°C	25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			l lait
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	60			V
Zero-Gate Voltage Drain Current	IDSS	VDS=60V, VGS=0			10	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =12A	16	23		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =12A, V _G S=10V		25	33	mΩ
	RDS(on)2	I _D =12A, V _{GS} =4V		35	49	mΩ

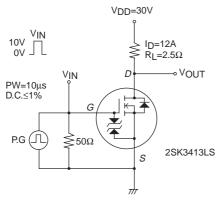
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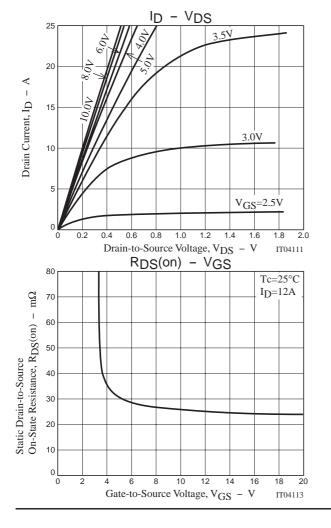
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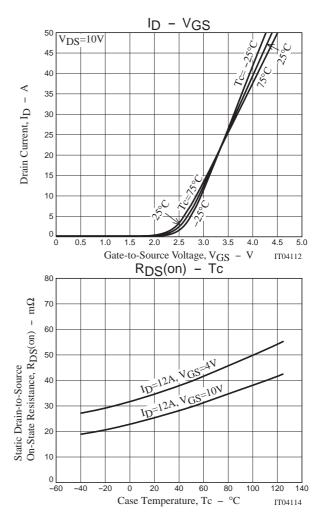
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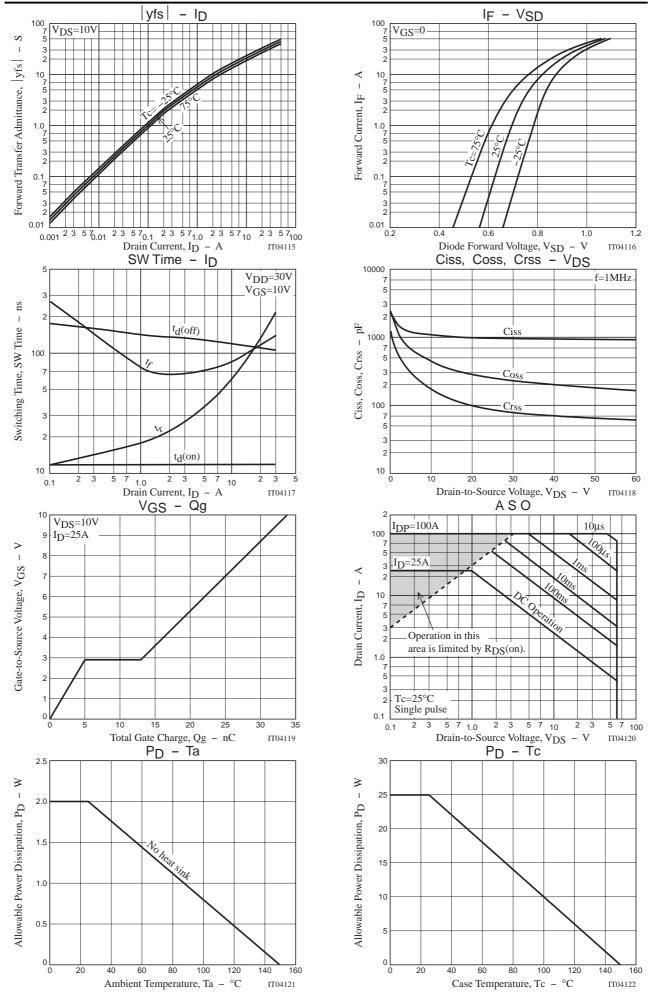
Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		1000		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		280		pF
Reverse Transfer Capacitance	Crss	VDS=20V, f=1MHz		100		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		12		ns
Rise Time	t _r	See specified Test Circuit.		72		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		120		ns
Fall Time	tf	See specified Test Circuit.		90		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =25A		34		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =25A		5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =25A		8		nC
Diode Forward Voltage	V _{SD}	I _S =25A, V _{GS} =0		0.93	1.2	V

Switching Time Test Circuit









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