



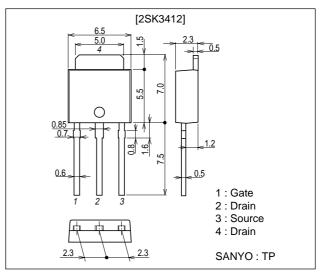
DC / DC Converter Applications

Features

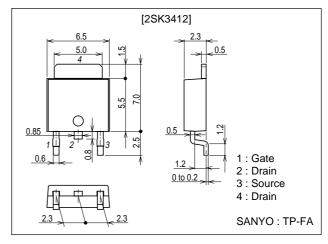
- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

Package Dimensions

unit : mm 2083B



unit : mm 2092B



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Specifications

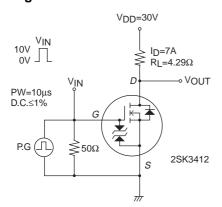
Absolute Maximum Ratings at Ta=25°C

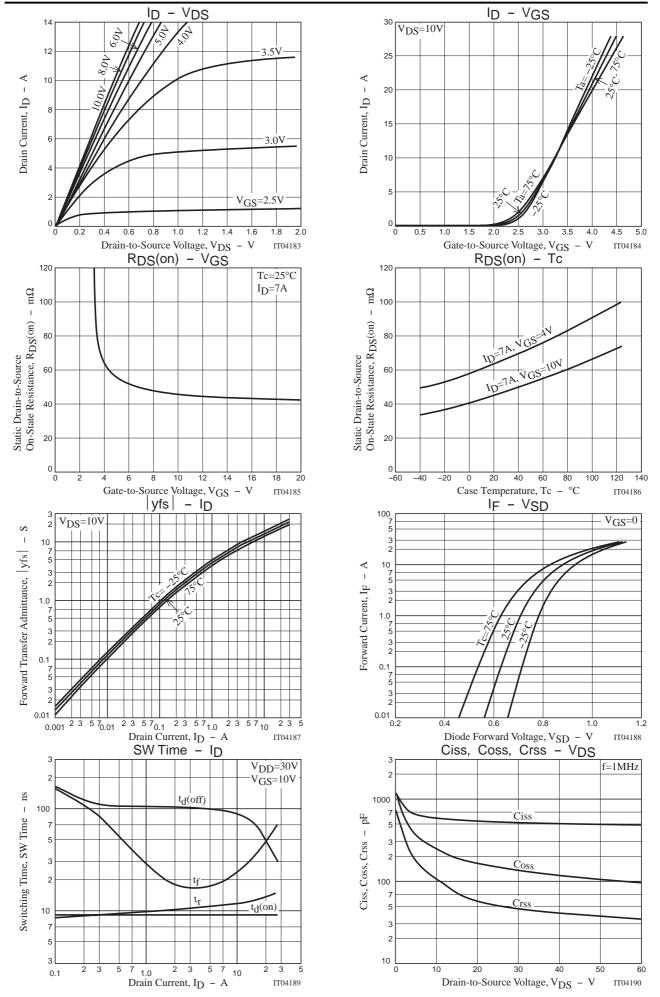
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		14	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	52	А
Allowable Power Dissipation	D-		1	W
	PD	Tc=25°C	20	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

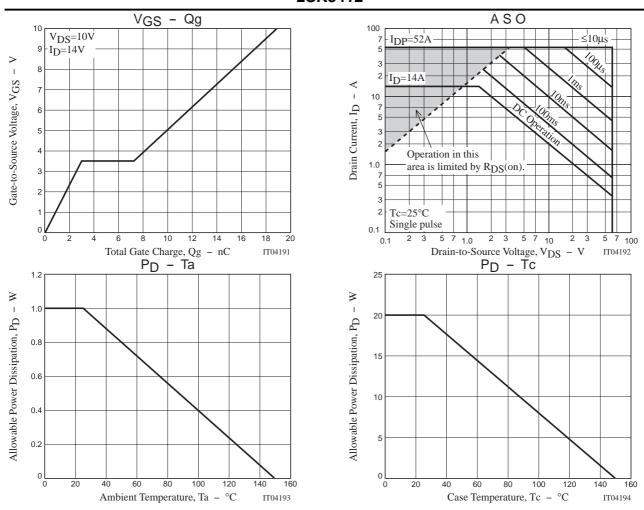
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0			10	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =7A	8	11		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =7A, V _G S=10V		48	62	mΩ
	RDS(on)2	ID=7A, VGS=4V		64	90	mΩ
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		550		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		170		pF
Reverse Transfer Capacitance	Crss	VDS=20V, f=1MHz		60		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		9		ns
Rise Time	t _r	See specified Test Circuit.		12		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		95		ns
Fall Time	tf	See specified Test Circuit.		20		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =14A		19		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =14A		3		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =14A		4.2		nC
Diode Forward Voltage	V _{SD}	I _S =14A, V _{GS} =0		0.94	1.2	V

Switching Time Test Circuit





2SK3412



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