



Ultrahigh-Speed Switching Applications

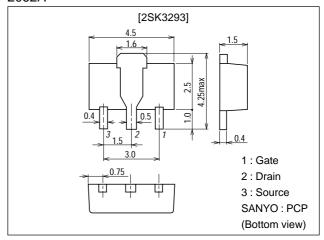
Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

Package Dimensions

unit:mm

2062A



Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ΙD		3	Α
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	12	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (250mm ² ×0.8mm)	1.5	W
		Tc=25°C	3.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	60			V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0			10	μA
Gate-to-Source Leakage Current	IGSS	V_{GS} =±16V, V_{DS} =0			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V_{DS} =10V, I_D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =1.5A	2.6	3.6		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =1.5A, V _{GS} =10V		115	150	mΩ
	R _{DS} (on)2	I _D =1A, V _{GS} =4V		150	210	mΩ

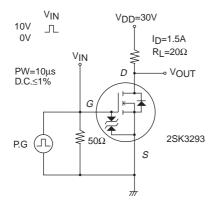
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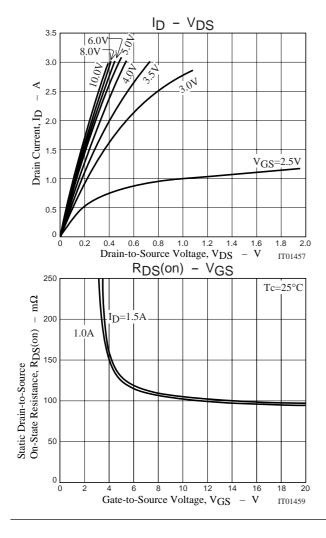
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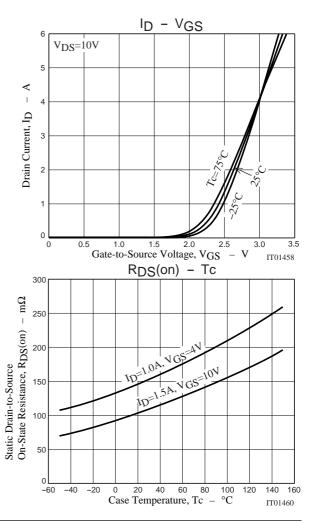
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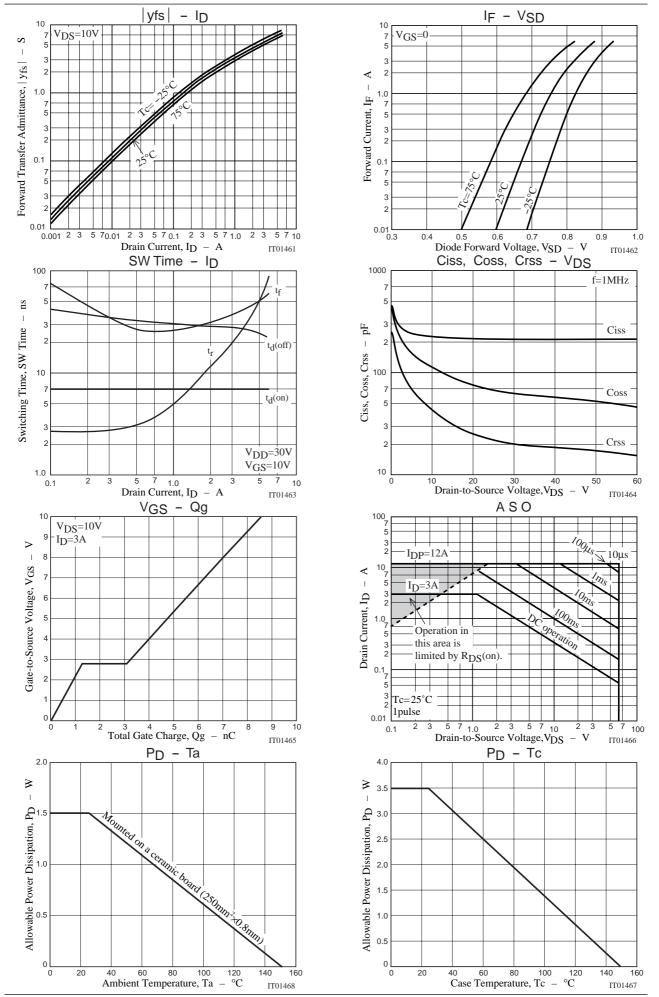
Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Offic
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		220		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		75		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		25		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		7		ns
Rise Time	t _r	See specified Test Circuit		8		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		30		ns
Fall Time	t _f	See specified Test Circuit		29		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =3A		8.6		nC
Gate-to-Source Charge	Qgs			1.3		nC
Gate-to-Drain "Miller" Charge	Qgd			1.8		nC
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0		0.83	1.2	V

Switching Time Test Circuit









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