TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π−MOSVI)

2SK2989

Chopper Regulator, DC−DC Converter and Motor Drive Applications

- Low drain−source ON resistance : $R_{DS\ (ON)}$ = 120 mΩ (typ.)
- High forward transfer admittance : $|Y_{fs}|$ = 2.6 S (typ.)
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 50 V)
- Enhancement−mode : V_{th} = 0.8~2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain−source voltage		V_{DSS}	50	V
Drain−gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	50	V
Gate−source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	I_D	5	A
	Pulse (Note 1)	I_{DP}	15	
Drain power dissipation		P_D	0.9	W
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55~150	°C

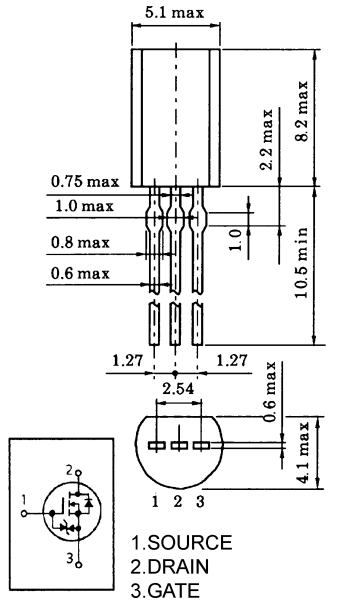
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	$R_{th\ (ch-a)}$	138	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

This transistor is an electrostatic sensitive device.
Please handle with caution.

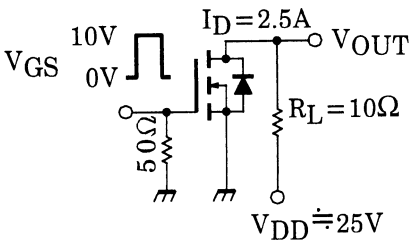
Unit: mm



JEDEC	TO-92MOD
JEITA	—
TOSHIBA	2-5J1C

Weight: 0.36 g (typ.)

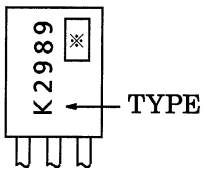
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cut−off current		I_{DSS}	V_{DS} = 50 V, V_{GS} = 0 V	—	—	100	μA
Drain−source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	50	—	—	V
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	0.8	—	2.0	V
Drain−source ON resistance		$R_{DS\ (ON)}$	V_{GS} = 4 V, I_D = 1.3 A	—	240	330	mΩ
			V_{GS} = 10 V, I_D = 2.5 A	—	120	150	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 2.5 A	1.3	2.6	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	145	—	pF
Reverse transfer capacitance		C_{rss}		—	25	—	
Output capacitance		C_{oss}		—	75	—	
Switching time	Rise time	t_r	 Duty ≦ 1%, t_w = 10μs	—	16	—	ns
	Turn−on time	t_{on}		—	23	—	
	Fall time	t_f		—	27	—	
	Turn−off time	t_{off}		—	110	—	
Total gate charge (gate−source plus gate−drain)		Q_g	V_{DD} ≈ 40 V, V_{GS} = 10 V, I_D = 5 A	—	6.5	—	nC
Gate−source charge		Q_{gs}		—	5	—	
Gate−drain (“miller”) Charge		Q_{gd}		—	1.5	—	

Source−Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	5	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	15	A
Forward voltage (diode)	V_{DSF}	I_{DR} = 5 A, V_{GS} = 0 V	—	—	−1.5	V

Marking



※ Lot Number

□□ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)

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