TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK2825

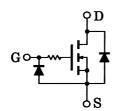
For Portable Equipment
High Speed Switch Applications
Analog Switch Applications

- High input impedance
- 1.5 V gate drive
- Low gate threshold voltage: $V_{th} = 0.5 \sim 1.0 \text{ V}$
- Small package

Marking

Equivalent Circuit



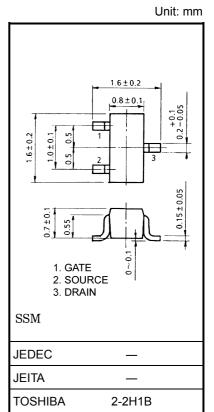


Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage	V _{DS}	20	V	
Gate-source voltage	V_{GSS}	10	V	
DC drain current	I _D	100	mA	
Drain power dissipation	P_{D}	100	mW	
Channel temperature	T _{ch}	150	°C	
Storage temperature range	T _{stg}	-55~150	°C	

Note: This transistor is electrostatic sensitive device.

Please handle with caution.

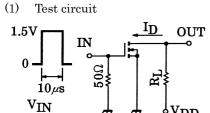


Weight: 2.4 mg (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = 10 V, V _{DS} = 0	_	_	1	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0$	20	_	_	V
Drain cut-off current		I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0$	_	_	1	μΑ
Gate threshold voltage		V _{th}	V _{DS} = 1.5 V, I _D = 0.1 mA	0.5	_	1.0	V
Forward transfer	admittance	Y _{fs}	V _{DS} = 1.5 V, I _D = 10 mA	35	70	_	mS
Drain-source ON resistance 1		R _{DS} (ON) 1	I _D = 1 mA, V _{GS} = 1.2 V	_	15	50	Ω
Drain-source ON resistance 2		R _{DS} (ON) 2	$I_D = 10 \text{ mA}, V_{GS} = 1.5 \text{ V}$	_	10	40	Ω
Drain-source ON resistance 3		R _{DS} (ON) 3	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	_	7	28	Ω
Input capacitance		C _{iss}	V _{DS} = 1.5 V, V _{GS} = 0, f = 1 MHz	_	12	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 1.5 V, V _{GS} = 0, f = 1 MHz	_	3.4	_	pF
Output capacitance		C _{oss}	$V_{DS} = 1.5 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	12	_	pF
Switching time	Turn-on time	t _{on}	V _{DD} = 1.5 V, I _D = 10 mA, V _{GS} = 0~1.5 V	_	0.35	_	0
	Turn-off time	t _{off}		_	0.2	_	μS

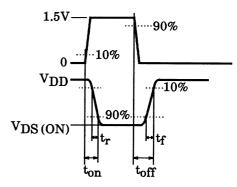
Switching Time Test Circuit

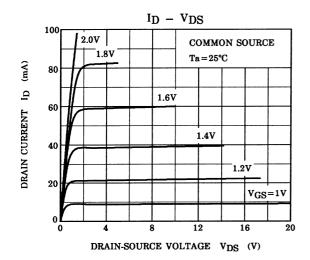


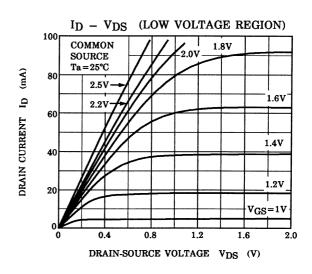
 $\begin{array}{c} \text{OUT} & \text{V}_{\text{DD}} = 1.5\text{V} \\ \text{OUT} & \text{D.U.} \leq 1\% \\ \text{V}_{\text{IN}} : \text{t}_{\text{r}}, \text{tf} < 5\text{ns} \\ \text{(Z}_{\text{out}} = 50\Omega) \\ \text{COMMON SOURCE} \end{array}$

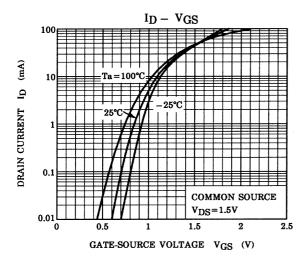
VGS
(3) VOUT
VDS

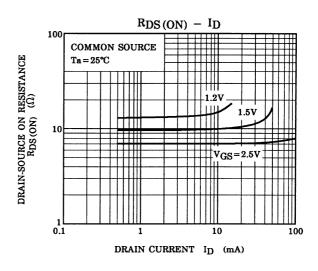
VIN

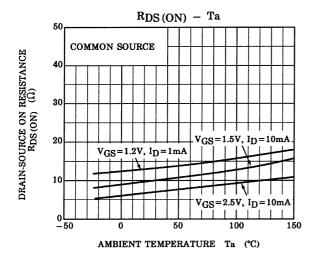


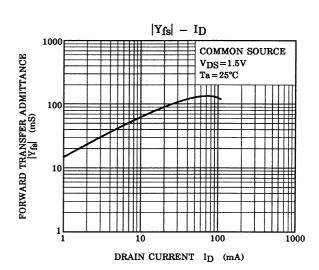


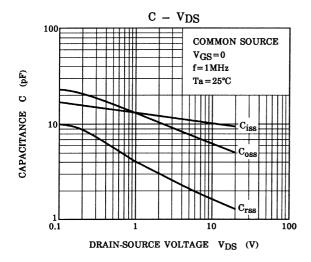


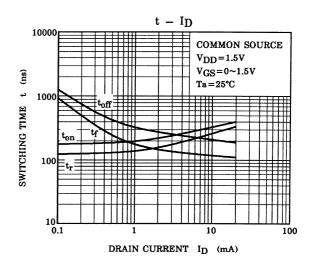


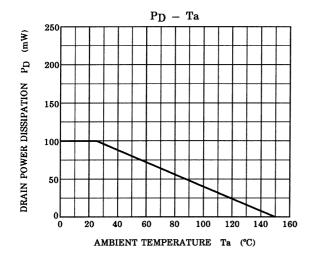












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