TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSIII)

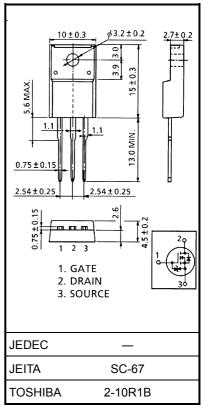
# 2SK2717

# DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance  $RDS(ON) = 2.3 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 4.4 \text{ S (typ.)}$
- Low leakage current  $: IDSS = 100 \ \mu A \ (max) \ (VDS = 720 \ V)$
- Enhancement-mode :  $V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA})$

#### Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	900	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	900	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	۱ <sub>D</sub>	5	А	
	Pulse (Note 1)	I <sub>DP</sub>	15	A	
Drain power dissipatio	n (Tc = 25°C)	PD	45	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	595	mJ	
Avalanche current		I <sub>AR</sub>	5	А	
Repetitive avalanche e	energy (Note 3)	E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C	



Weight: 1.9 g (typ.)

## **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	2.78	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	62.5	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 43.6 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 5 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution. Unit: mm

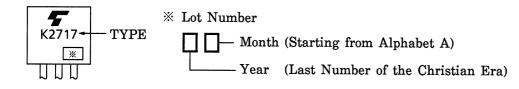
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V			±10	μA
Gate-source bro	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V	_		100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.0 A	-	2.3	2.5	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 3.0 A	1.1	4.4	_	S
Input capacitance	ce	C <sub>iss</sub>		-	1200	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	20	_	pF
Output capacitance		C <sub>oss</sub>		_	120	—	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \prod_{ID = 3A \\ \downarrow $	_	40	_	
	Turn-on time	t <sub>on</sub>		_	90	_	20
	Fall time	t <sub>f</sub>		_	60	_	- ns
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> =10µs	_	200	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	45	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		25		nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>			20		

# Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	15	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 5 A, V <sub>GS</sub> = 0 V dI <sub>DR</sub> / dt = 100 A / μs		1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / μs	_	11	_	μC

# Marking

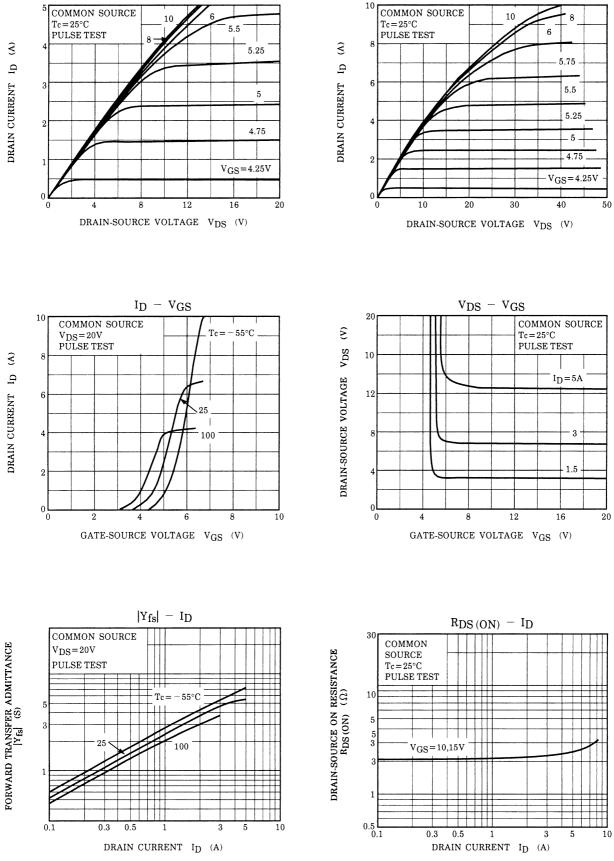


# TOSHIBA

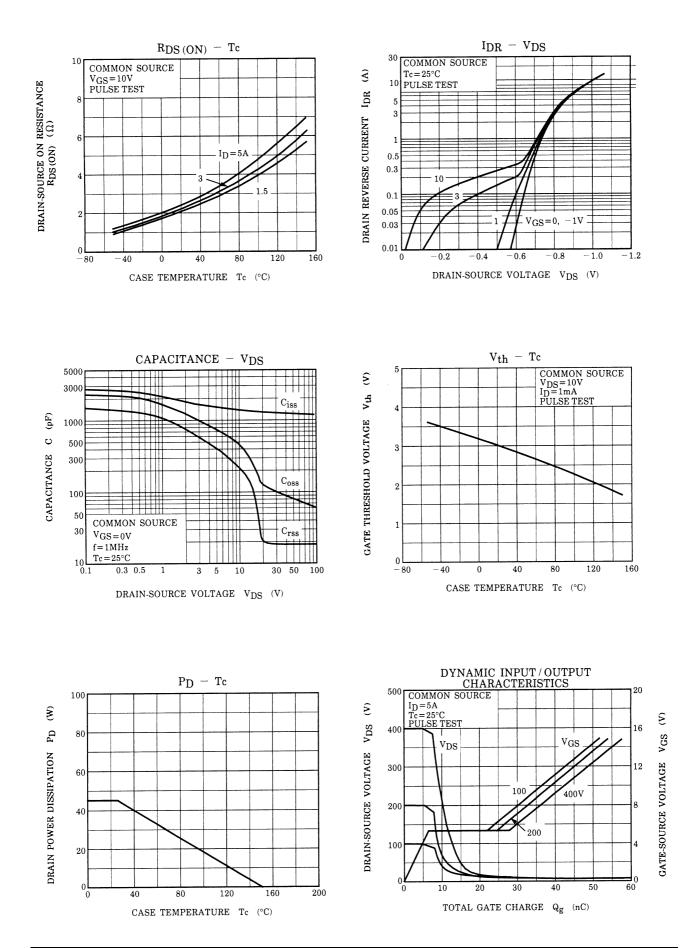
 $I_D - V_{DS}$ 

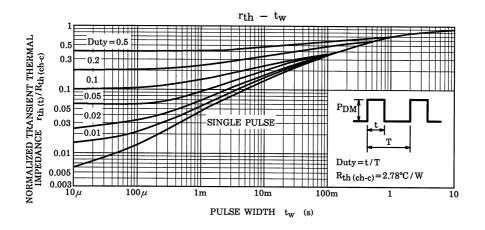
 $I_D\ -\ V_{DS}$ 

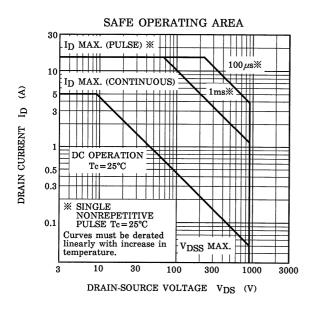
Ś ΙD

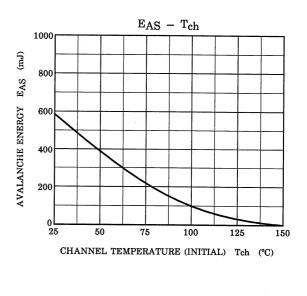


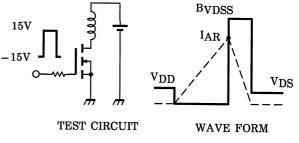
# TOSHIBA













## **RESTRICTIONS ON PRODUCT USE**

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.