TOSHIBA 2SK2614

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE ($L^2-\pi$ -MOS V)

2 S K 2 6 1 4

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

• 4 V Gate Drive

• Low Drain-Source ON Resistance : $R_{DS(ON)} = 0.032 \Omega$ (Typ.)

• High Forward Transfer Admittance : $|Y_{fs}| = 13 S$ (Typ.)

• Low Leakage Current : $I_{DSS} = 100 \mu A$ (Max.)

 $(V_{DS} = 50 V)$

• Enhancement-Mode : $V_{th} = 0.8 \sim 2.0 \text{ V}$

 $(V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA})$

MAXIMUM RATINGS (Ta = 25°C)

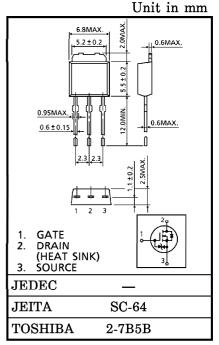
CHARACTE	SYMBOL	RATING	UNIT		
Drain-Source Voltag	$v_{ m DSS}$	50	V		
Drain-Gate Voltage	${ m v_{DGR}}$	50	V		
Gate-Source Voltage	v_{GSS}	±20	V		
Drain Current	DC (Note 1)	${ m I}_{ m D}$	20	A	
	Pulse (Note 1)	$I_{ m DP}$	50		
Drain Power Dissipa	$P_{\mathbf{D}}$	40	W		
Channel Temperatur	$\mathrm{T_{ch}}$	150	$^{\circ}\mathrm{C}$		
Storage Temperature	$\mathrm{T_{stg}}$	-55~150	°C		

THERMAL CHARACTERISTICS

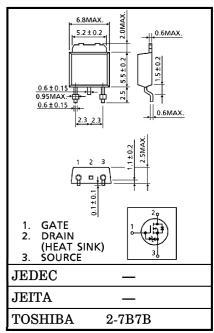
	SYMBOL		
Thermal Resistance, Channel to Case	R _{th (ch-c)}	3.125	°C/W
Thermal Resistance, Channel to Ambient	R _{th (ch-a)}		°C/W

(Note 1): Please use devices on condition that the channel temperature is below 150°C.

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 0.36 g (Typ.)



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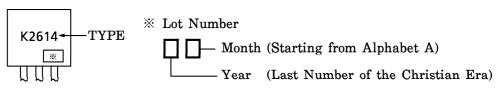
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARA	ACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakag	ge Current	$I_{ m GSS}$	$V_{GS} = \pm 16 V, V_{DS} = 0 V$		_	±10	μ A
Drain Cut-off Current		$I_{ m DSS}$	$V_{DS} = 50 \text{ V}, \ V_{GS} = 0 \text{ V}$	_	_	100	μ A
Drain-Source Breakdown Voltage		V (BR) DSS	$I_{D} = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	50	_	_	v
Gate Threshold Voltage		$V_{ m th}$	$V_{\mathrm{DS}} = 10 \mathrm{V}, \mathrm{I}_{\mathrm{D}} = 1 \mathrm{mA}$	0.8	—	2.0	V
Drain-Source ON Resistance		R _{DS} (ON)	$V_{GS} = 4 \text{ V}, I_D = 5 \text{ A}$ $V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	0.055	0.08	Ω
Forward Tra Admittance	nsfer	Y _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 10 \text{ A}$	7	13	_	s
Input Capacitance		C_{iss}		_	900	_	
Reverse Transfer Capacitance		$\mathrm{C}_{\mathrm{rss}}$	$egin{aligned} { m V}_{ m DS} &= 10 { m V}, \; { m V}_{ m GS} &= 0 { m V}, \ { m f} &= 1 { m MHz} \end{aligned}$	_	130	_	pF
Output Capacitance		Coss		_	370	_	
Switching Time	Rise Time	t _r	$V_{GS} \stackrel{10 \text{ V}}{\text{O} \text{ V}} \stackrel{\text{I}_{D}}{\text{I}_{D}} = \stackrel{10 \text{ A}}{\text{O} \text{ V}} \text{out}$ $R_{L} = 3 \Omega$ $V_{DD} = 30 \text{ V}$	_	15	_	
	Turn-on Time	t _{on}		ı	25	_	na
	Fall Time	t _f			30	_	ns
	Turn-off Time	t _{off}	Duty \leq 1%, $t_{\rm W} = 10~\mu {\rm s}$	_	100	_	
Total Gate Charge (Gate- Source Plus Gate-Drain)		$\mathbf{Q}_{\mathbf{g}}$	$V_{\mathrm{DD}} = 40 \mathrm{V}, V_{\mathrm{GS}} = 10 \mathrm{V},$	_	25	_	
Gate-Source Charge		$\mathbf{Q}_{\mathbf{g}\mathbf{s}}$	$I_D = 20 \text{ A}$	_	19	_	nC
Gate-Drain ("Miller") Charge		$\mathbf{Q}_{\mathbf{gd}}$		_	6	—	

SOURCE-DRAIN RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current (Note 1)	$I_{ m DR}$	_	_	_	20	A
Pulse Drain Reverse Current (Note 1)	$I_{ m DRP}$	_	_	_	50	A
Forward Voltage (Diode)	${ m v_{DSF}}$	$I_{DR} = 20 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse Recovery Time	t_{rr}	$I_{DR} = 20 \text{ A}, V_{GS} = 0 \text{ V}$	_	60	_	ns
Reverse Recovery Charge	$\mathrm{Q}_{\mathbf{r}\mathbf{r}}$	$dI_{DR}/dt = 50 A/\mu s$	_	45	_	μ C

MARKING



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