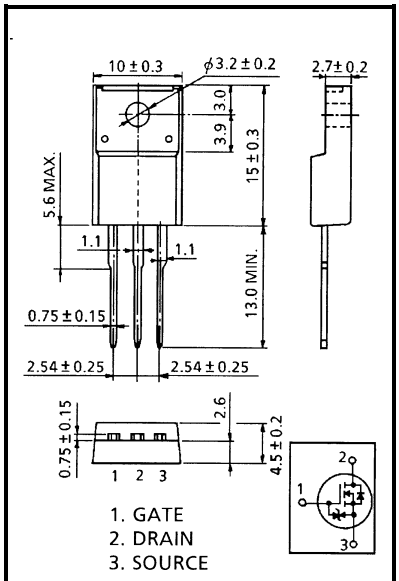
TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π−MOSV)

2SK2508

Switching Regulator and DC−DC Converter and Motor Applications

- Low drain−source ON resistance : $R_{DS\ (ON)}$ = 0.18 Ω (typ.)
- High forward transfer admittance : $|Y_{fs}|$ = 13 S (typ.)
- Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 250 V)
- Enhancement−mode : V_{th} = 1.5~3.5 V (V_{DS} = 10 V, I_D = 1 mA)

Unit: mm



JEDEC	—
JEITA	SC-67
TOSHIBA	2-10R1B

Weight: 1.9 g (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain−source voltage		V_{DSS}	250	V
Drain−gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	250	V
Gate−source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	I_D	13	A
	Pulse (Note 1)	I_{DP}	52	A
Drain power dissipation (Tc = 25°C)		P_D	45	W
Single pulse avalanche energy (Note 2)		E_{AS}	148	mJ
Avalanche current		I_{AR}	13	A
Repetitive avalanche energy (Note 3)		E_{AR}	4.5	mJ
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55~150	°C

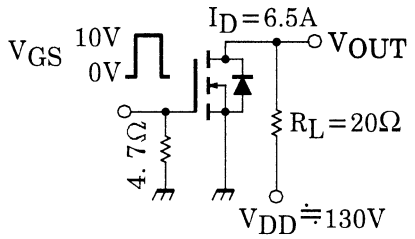
Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th\ (ch-c)}$	2.78	°C / W
Thermal resistance, channel to ambient	$R_{th\ (ch-a)}$	62.5	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.
Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 1.48 mH, R_G = 25 Ω, I_{AR} = 13 A
Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device.
Please handle with caution.

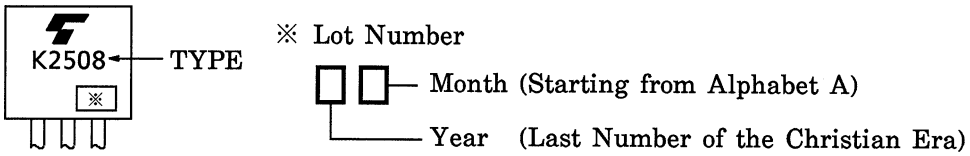
Electrical Characteristics (Ta = 25°C)

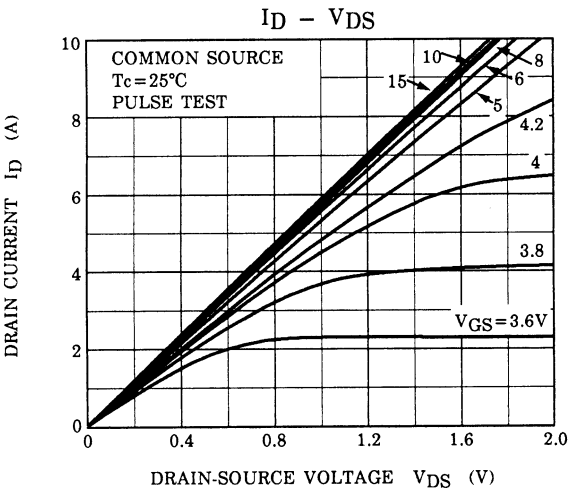
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cut−off current		I_{DSS}	V_{DS} = 250 V, V_{GS} = 0 V	—	—	100	μA
Drain−source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	250	—	—	V
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	1.5	—	3.5	V
Drain−source ON resistance		$R_{DS\ (ON)}$	V_{GS} = 10 V, I_D = 6.5 A	—	0.18	0.25	Ω
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 6.5 A	6	13	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	1800	—	pF
Reverse transfer capacitance		C_{rss}		—	130	—	
Output capacitance		C_{oss}		—	500	—	
Switching time	Rise time	t_r	 Duty ≦1%, $t_w = 10\mu s$	—	15	—	ns
	Turn−on time	t_{on}		—	25	—	
	Fall time	t_f		—	10	—	
	Turn−off time	t_{off}		—	70	—	
Total gate charge (Gate−source plus gate−drain)		Q_g	V_{DD} ≈ 200 V, V_{GS} = 10 V, I_D = 13 A	—	40	—	nC
Gate−source charge		Q_{gs}		—	25	—	
Gate−drain (“miller”) charge		Q_{gd}		—	15	—	

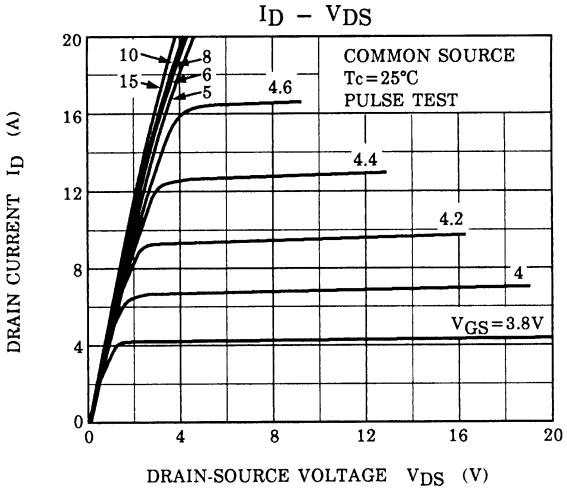
Source−Drain Ratings and Characteristics (Ta = 25°C)

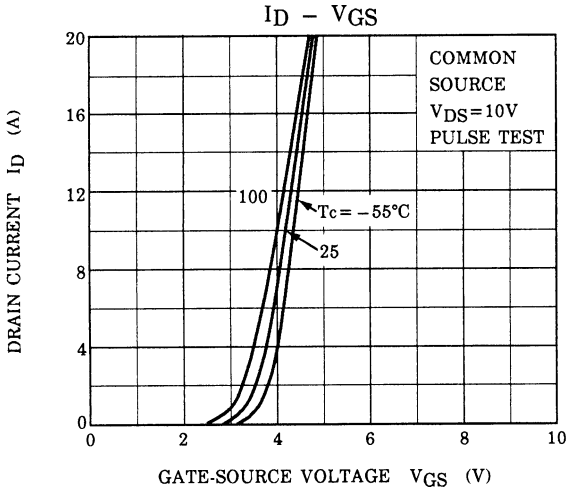
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	13	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	52	A
Forward voltage (diode)	V_{DSF}	I_{DR} = 13 A, V_{GS} = 0 V	—	—	−2.0	V
Reverse recovery time	t_{rr}	I_{DR} = 13 A, V_{GS} = 0 V dI_{DR} / dt = 100 A / μs	—	260	—	ns
Reverse recovery charge	Q_{rr}		—	0.3	—	μC

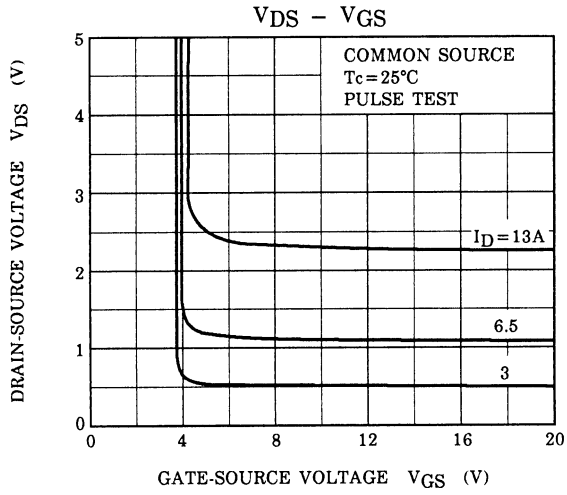
Marking

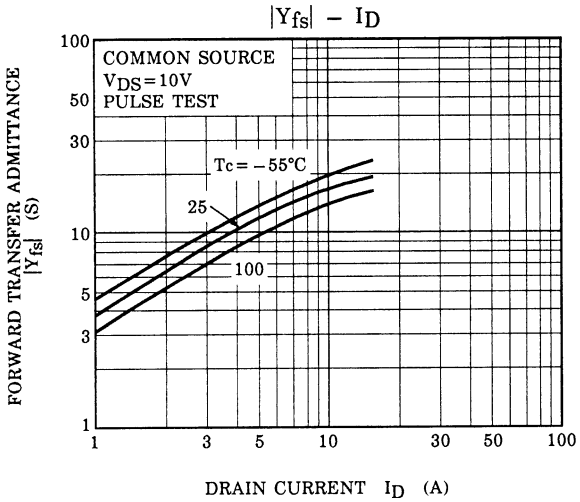


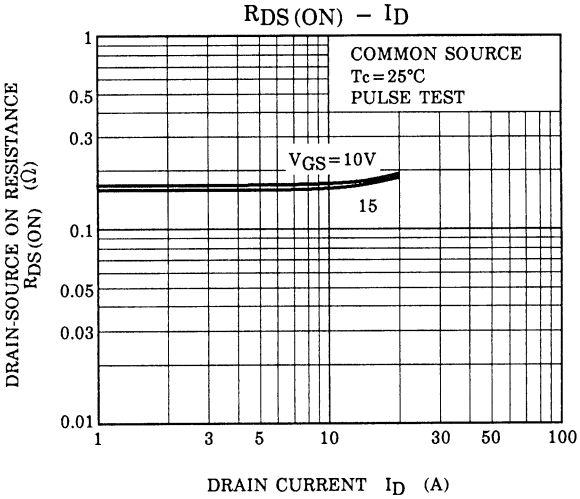

I_D – V_{DS}
COMMON SOURCE
Tc=25°C
PULSE TEST
10
15
8
6
5
4.2
4
3.8
V_{GS}=3.6V
DRAIN CURRENT I_D (A)
DRAIN-SOURCE VOLTAGE V_{DS} (V)
0
0.4
0.8
1.2
1.6
2.0
2
4
6
8
10


I_D – V_{DS}
COMMON SOURCE
Tc=25°C
PULSE TEST
10
15
8
6
5
4.6
4.4
4.2
4
V_{GS}=3.8V
DRAIN CURRENT I_D (A)
DRAIN-SOURCE VOLTAGE V_{DS} (V)
0
4
8
12
16
20

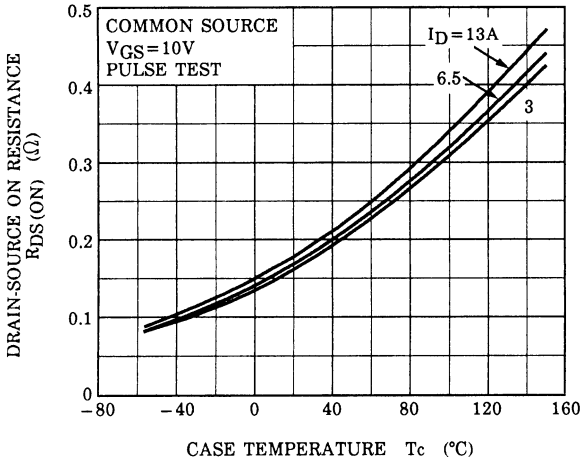

I_D – V_{GS}
COMMON
SOURCE
V_{DS}=10V
PULSE TEST
100
Tc=−55°C
25
DRAIN CURRENT I_D (A)
GATE-SOURCE VOLTAGE V_{GS} (V)
0
2
4
6
8
10
12
16
20


V_{DS} – V_{GS}
COMMON SOURCE
Tc=25°C
PULSE TEST
I_D=13A
6.5
3
DRAIN-SOURCE VOLTAGE V_{DS} (V)
GATE-SOURCE VOLTAGE V_{GS} (V)
0
4
8
12
16
20
1
2
3
4
5

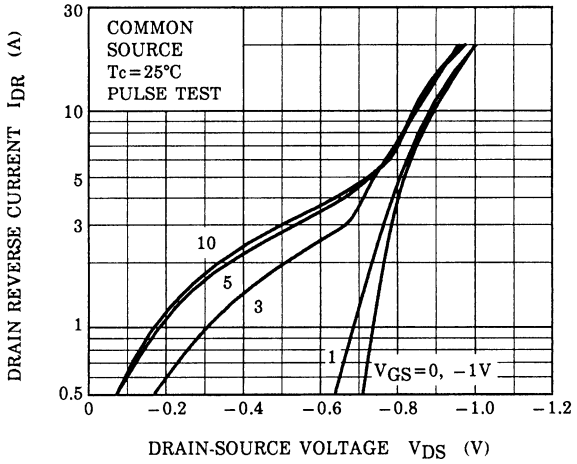

$|Y_{fs}|$ – I_D
COMMON SOURCE
V_{DS}=10V
PULSE TEST
Tc=−55°C
25
100
FORWARD TRANSFER ADMITTANCE $|Y_{fs}|$ (S)
DRAIN CURRENT I_D (A)
1
3
5
10
30
50
100


$R_{DS(ON)}$ – I_D
COMMON SOURCE
Tc=25°C
PULSE TEST
V_{GS}=10V
15
DRAIN-SOURCE ON RESISTANCE $R_{DS(ON)}$ (Ω)
DRAIN CURRENT I_D (A)
0.01
0.03
0.05
0.1
0.3
0.5
1
3
5
10
30
50
100

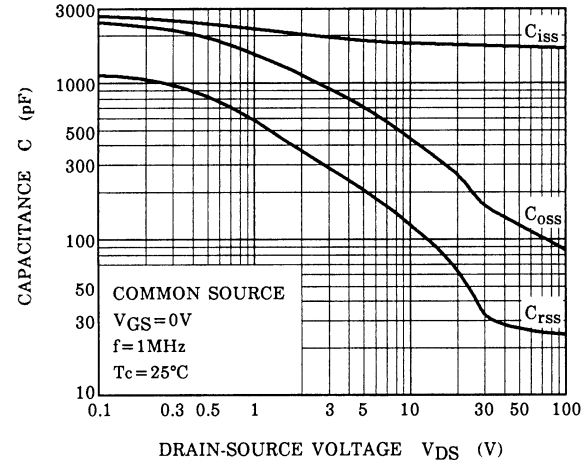
$R_{DS(ON)}$ – Tc



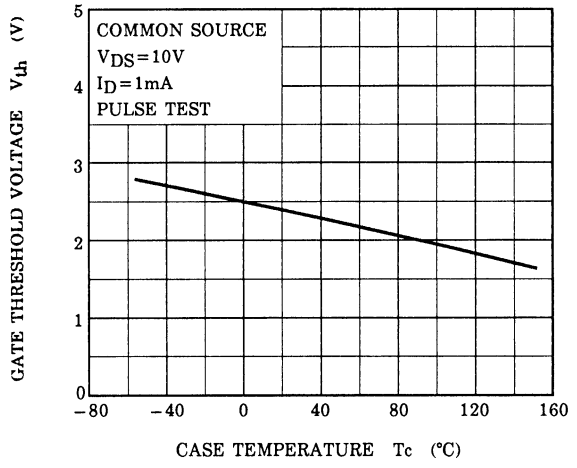
I_{DR} – V_{DS}



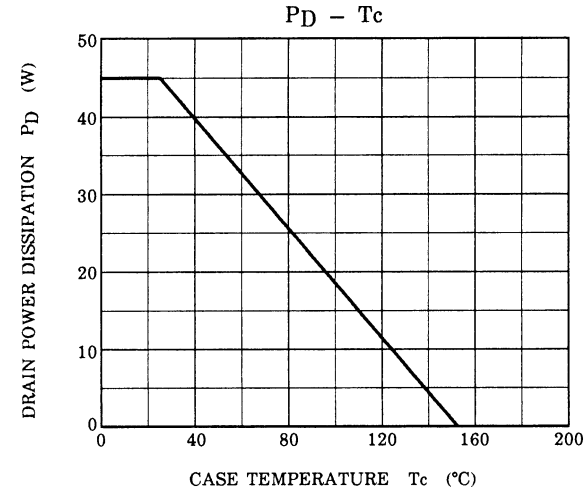
CAPACITANCE – V_{DS}



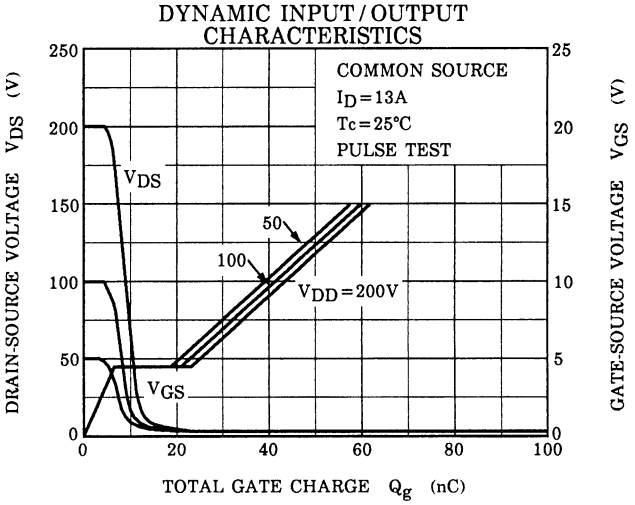
V_{th} – Tc



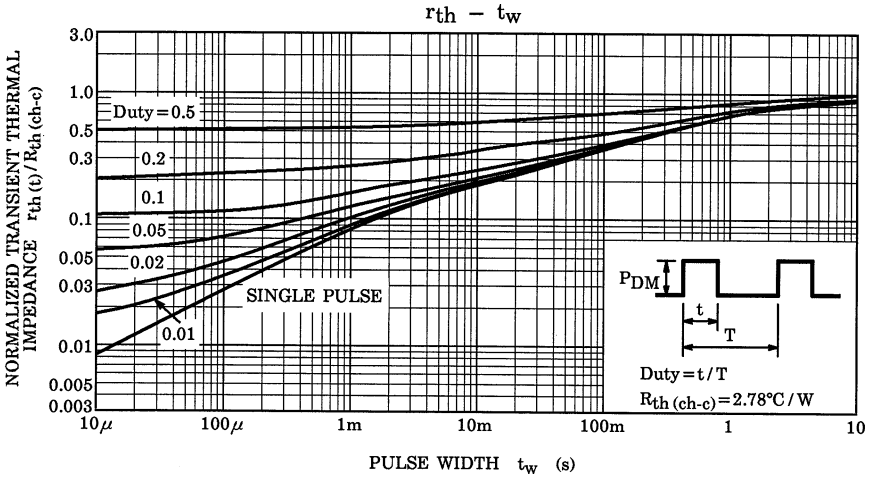
P_D – Tc



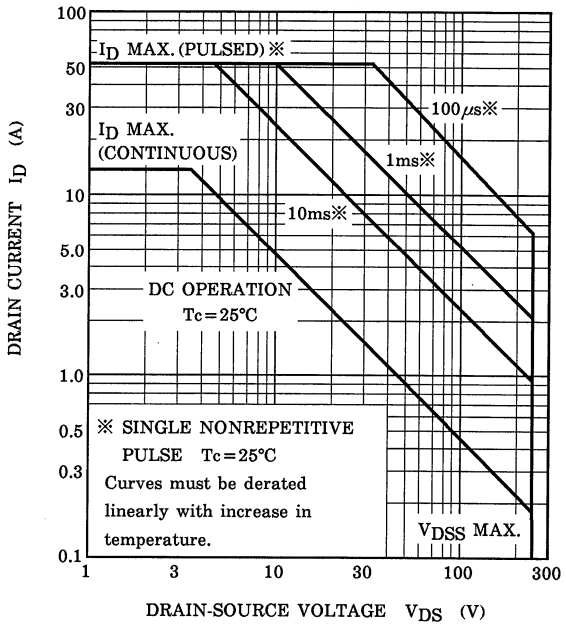
DYNAMIC INPUT / OUTPUT CHARACTERISTICS



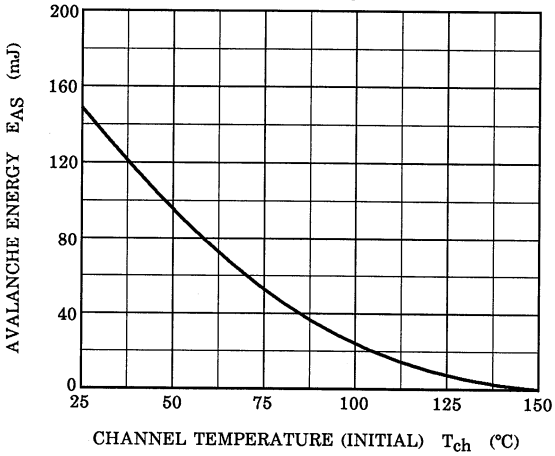
$r_{th} - t_w$

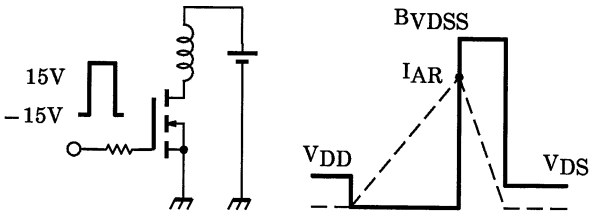


SAFE OPERATING AREA



$E_{AS} - T_{ch}$





TEST CIRCUIT WAVE FORM

$R_G = 25\ \Omega$
$V_{DD} = 50$ V, L = 1.48 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

RESTRICTIONS ON PRODUCT USE

000707EAA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
  In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the “Handling Guide for Semiconductor Devices,” or “TOSHIBA Semiconductor Reliability Handbook” etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury (“Unintended Usage”). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer’s own risk.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.