



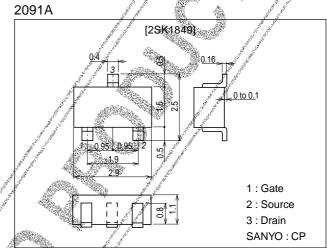
# **Ultrahigh-Speed Switching Applications**

#### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.

## Package Dimensions

unit:mm



## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	V <sub>D</sub> ss	100	V
Gate-to-Source Voltage	Vess	±15	V
Drain Current (DC)	J.D.	250	mA
Drain Current (pulse)	PW≤10ps, duty cycle≤1%	1	Α
Allowable Power Dissipation	/ PD	250	mW
Channel Temperature	// Tob	150	°C
Storage Temperature	Tstg .	-55 to +150	°C

### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
T drameter			min	typ	max	01111
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	100			V
Zero-Gate Votlage Drain Current	DSS /	V <sub>DS</sub> =100V, V <sub>GS</sub> =0			100	μA
Gate-to-Source Leakage/Current	I <sub>GSS</sub> /	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.0		2.0	V
Forward Transfer Admittance	J/yfs*	V <sub>DS</sub> =10V, I <sub>D</sub> =150mA	250	500		mS
Static Drain-to-Source On-State Resistance	RDS(on)1	I <sub>D</sub> =150mA, V <sub>GS</sub> =10V		2.7	3.5	Ω
Static Dialif-to-source Off-orate Resistance	R <sub>DS(on)</sub> 2	I <sub>D</sub> =150mA, V <sub>GS</sub> =4V		3.2	4.2	Ω

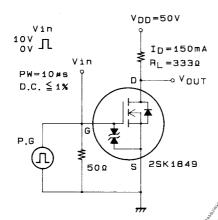
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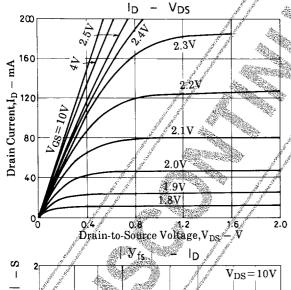
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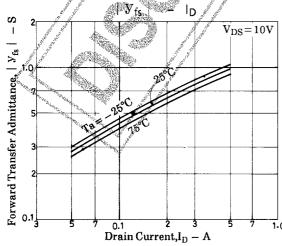
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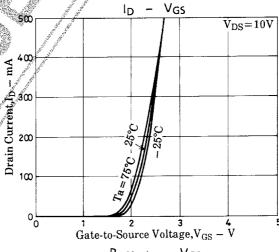
Parameter	Symbol	Conditions		Ratings		Unit
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		45		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		15		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		3		pF
Turn-ON Delay Time	<sup>t</sup> d(on)	See specified Test Circuit		5		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		10		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit	gat gat	40	Steeling St.	ns
Fall Time	t <sub>f</sub>	See specified Test Circuit	//	35	Mary Mary Mary Comment	ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =250mA, V <sub>GS</sub> =0	d si di	0.9	N. S.	V

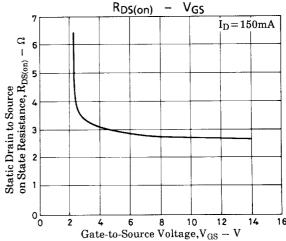
### **Switching Time Test Circuit**

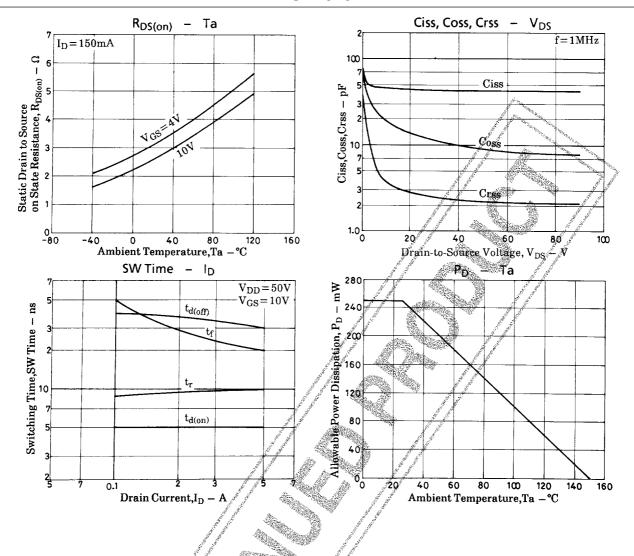












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