TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

2SK1829

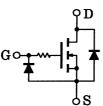
High Speed Switching Applications Analog Switch Applications

- 2.5 V gate drive
- Low threshold voltage: $V_{th} = 0.5 \sim 1.5 \text{ V}$
- High speed
- Enhancement-mode
- Small package

Marking

Equivalent Circuit



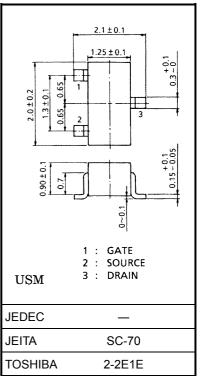


Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V _{DS}	20	V
Gate-source voltage	V _{GSS}	10	V
DC drain current	I _D	50	mA
Drain power dissipation	PD	100	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55~150	°C



Please handle with caution.



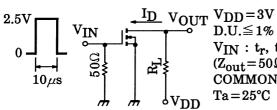
Weight: 0.006 g (typ.)

Unit: mm

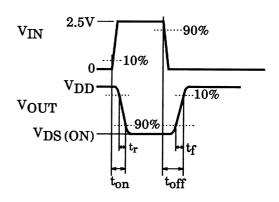
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = 10 \text{ V}, \text{ V}_{DS} = 0$			1	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0$	20		_	V
Drain cut-off curre	ent	I _{DSS}	$V_{DS}=20~V,~V_{GS}=0$	_	_	1	μA
Gate threshold vo	ltage	V _{th}	$V_{DS} = 3 V, I_D = 0.1 mA$	0.5		1.5	V
Forward transfer a	admittance	Y _{fs}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}$	20		_	mS
Drain-source ON	resistance	R _{DS (ON)}	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	_	20	40	Ω
Input capacitance		C _{iss}	$V_{DS} = 3 V, V_{GS} = 0, f = 1 MHz$	_	5.5	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 3 V, V_{GS} = 0, f = 1 MHz$	_	1.6	_	pF
Output capacitance		Coss	$V_{DS} = 3 V, V_{GS} = 0, f = 1 MHz$	_	6.5	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, \text{ V}_{GS} = 0 \sim 2.5 \text{ V}$	_	0.14	_	μS
	Turn-off time	t _{off}	$V_{DD} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}, \text{ V}_{GS} = 0 \sim 2.5 \text{ V}$	_	0.14	_	

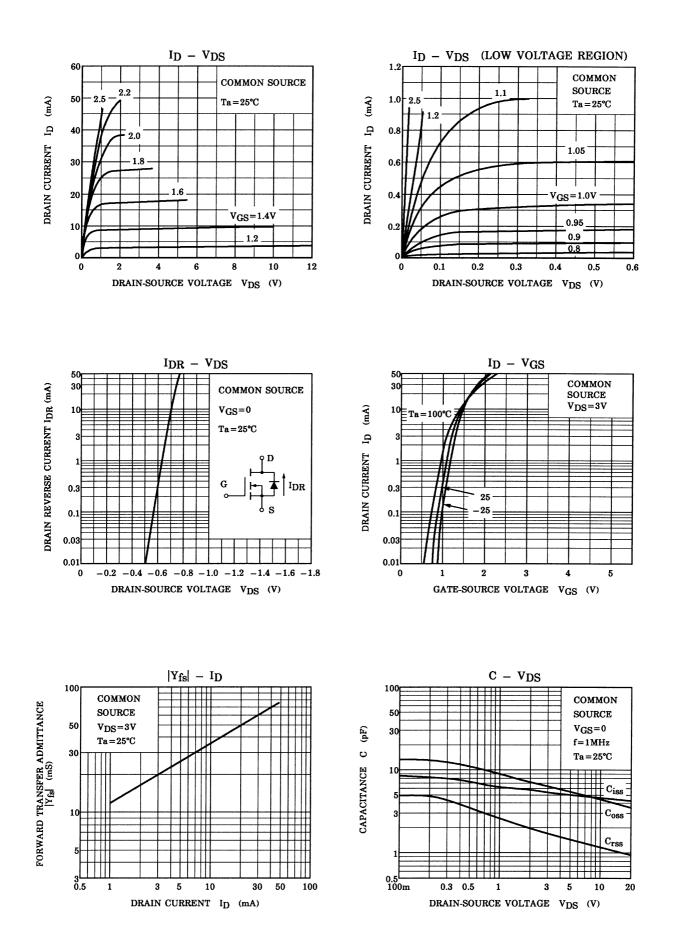
Switching Time Test Circuit



 $V_{IN} : t_r, t_r < 5ns$ ($Z_{out} = 50\Omega$) COMMON SOURCE $Ta = 25^{\circ}C$

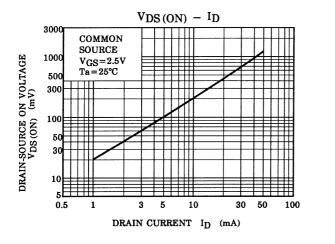


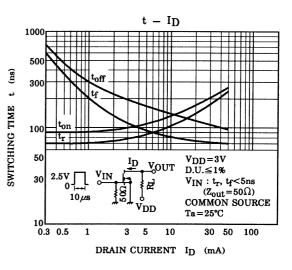
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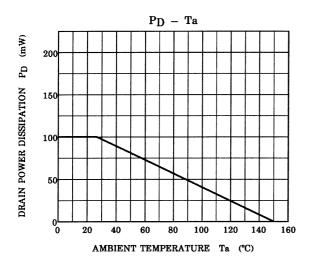


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