Ordering number:EN3447C

N-Channel Silicon MOSFET

2SK1444

Ultrahigh-Speed Switching Applications

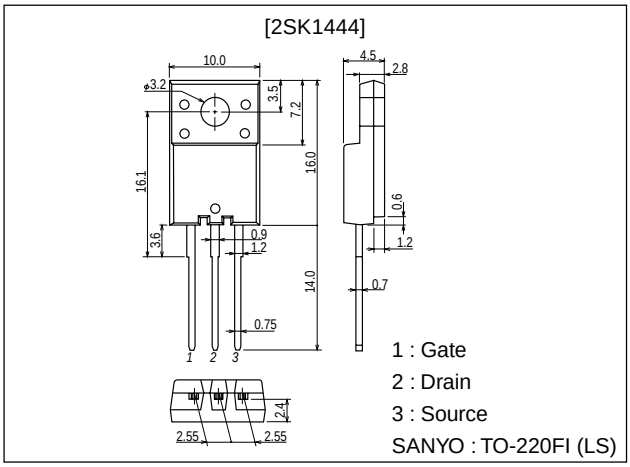
Features

- Low ON-state resistance.
- Ultrahigh-speed switching.
- Micaless package facilitating easy mounting.

Package Dimensions

unit:mm

2078B



Specifications

Absolute Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		450	V
Gate-to-Source Voltage	V_{GSS}		±30	V
Drain Current (DC)	I_D		3	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	12	A
Allowable Power Dissipation	P_D		2.0	W
		Tc=25°C	25	W
Channel Temperature	Tch		150	˚C
Storage Temperature	Tstg		–55 to +150	˚C

Electrical Characteristics at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	450			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=450V, V_{GS}=0			1.0	mA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0			±100	nA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	2.0		3.0	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=0.5A	1.1	2.2		S
Static Drain-to-Source ON-State Resistance	$R_{DS(on)}$	I_D=0.5A, V_{GS}=10V		2.0	2.6	Ω

(Note) Be careful in handling the 2SK1444 because it has no protection diode between gate and source.

Continued on next page.

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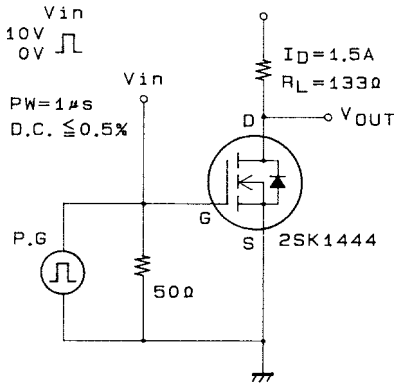
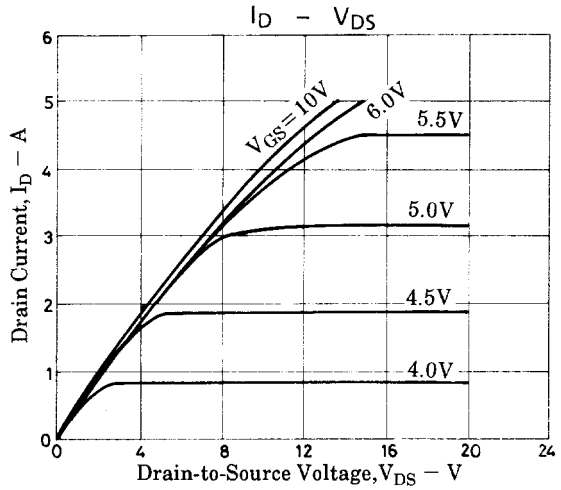
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

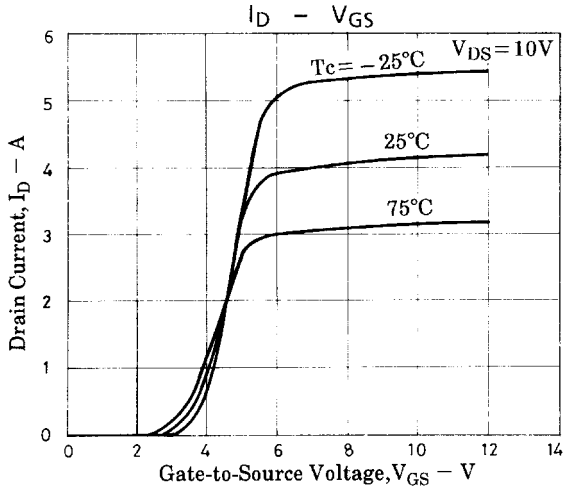
Continued from preceding page.

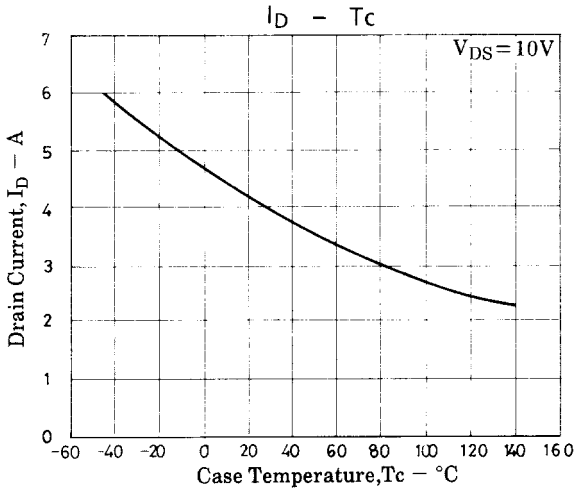
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=20V, f=1MHz		400		pF
Output Capacitance	Coss	V_{DS}=20V, f=1MHz		60		pF
Reverse Transfer Capacitance	Crss	V_{DS}=20V, f=1MHz		25		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		12		ns
Rise Time	t_r	See specified Test Circuit.		20		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit.		80		ns
Fall Time	t_f	See specified Test Circuit.		35		ns
Diode Forward Voltage	V_{SD}	I_S=3A, V_{GS}=0			1.8	V

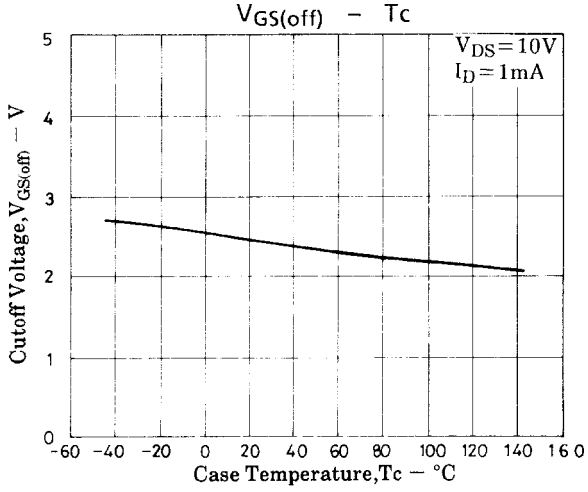
Switching Time Test Circuit



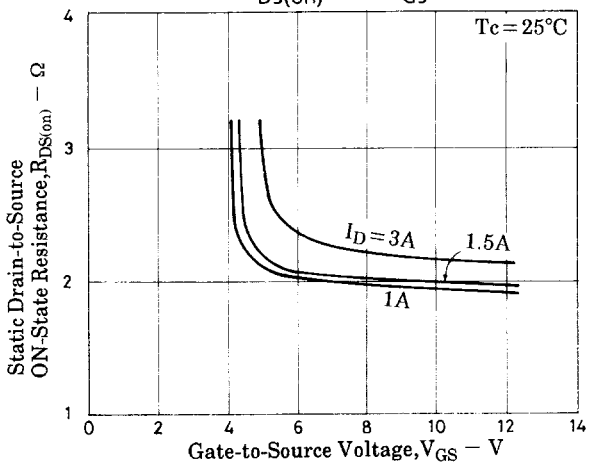




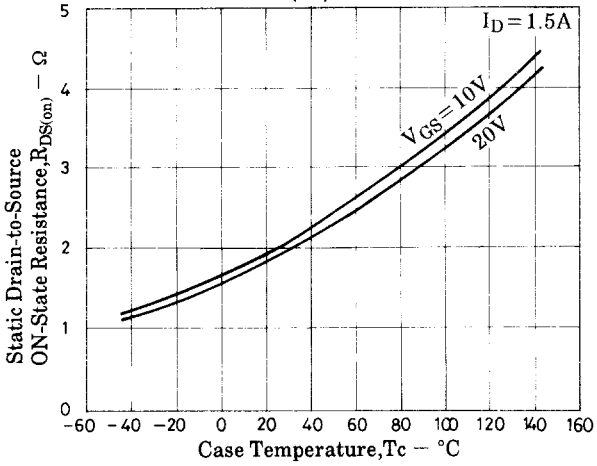




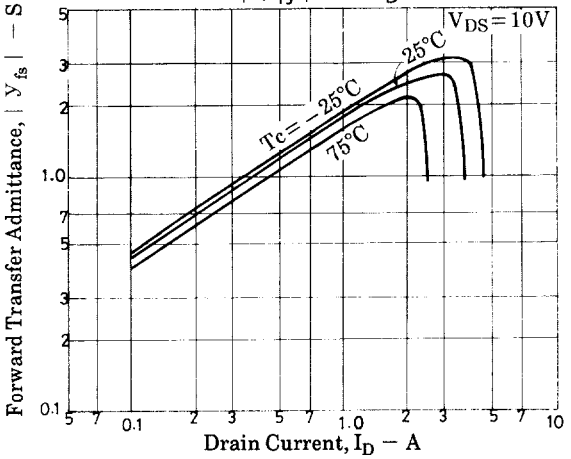
$R_{DS(on)}$ – V_{GS}



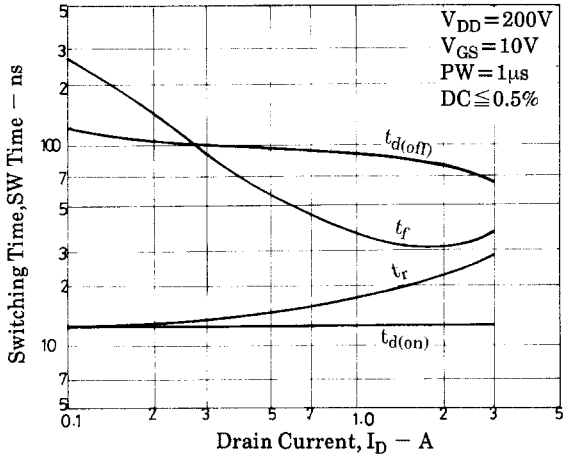
$R_{DS(on)}$ – Tc



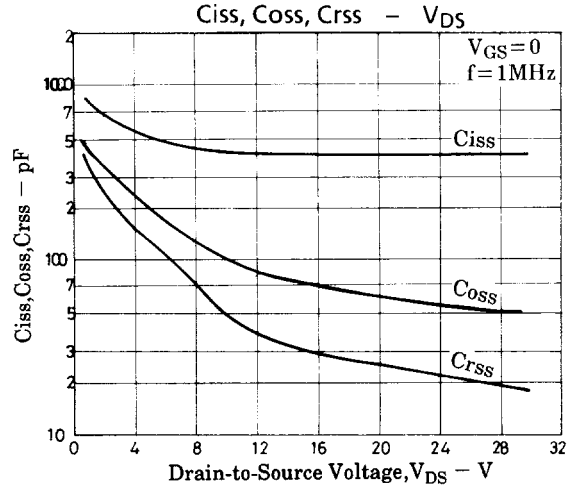
$|y_{fs}|$ – I_D



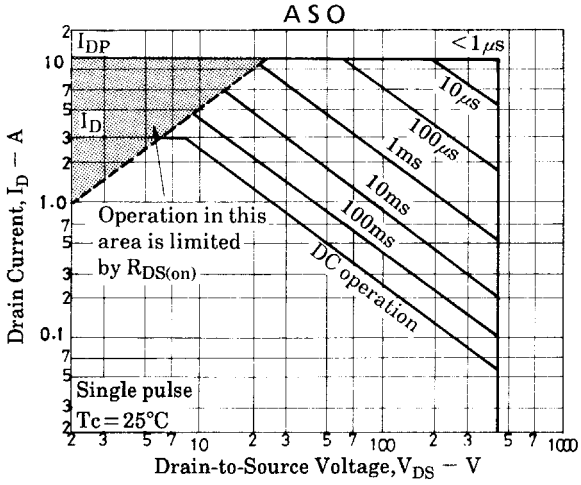
SW Time – I_D



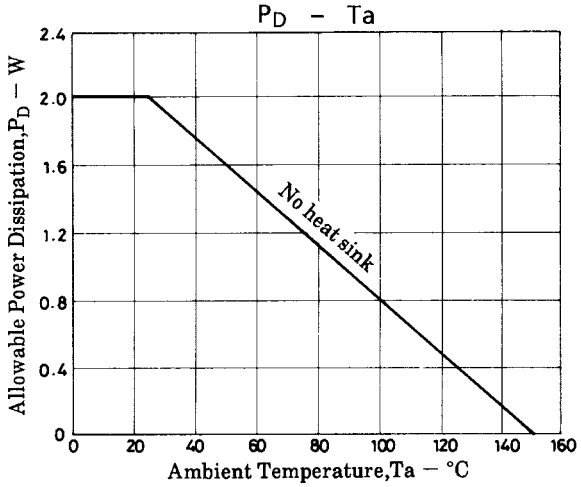
Ciss, Coss, Crss – V_{DS}



A S O



P_D – Ta



P_D – Tc