Ordering number:EN4228

N-Channel Silicon MOSFET

2SK1412

Ultrahigh-Speed Switching Applications

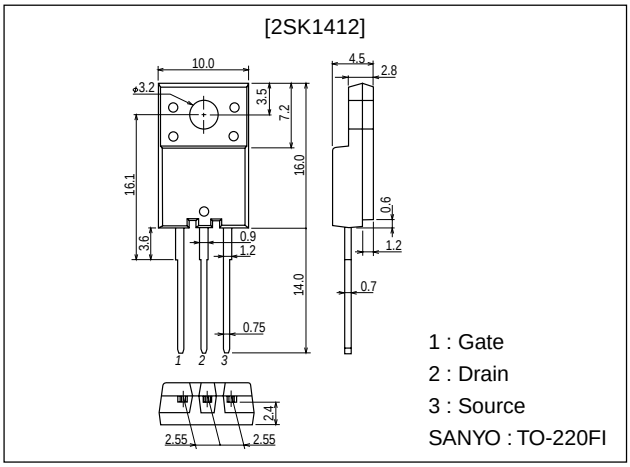
Features

- Low ON resistance, low input capacitance, Ultrahigh-speed switching.
- High reliability (Adoption of HVP process).
- Micaless package facilitating mounting.

Package Dimensions

unit:mm

2078B



Specifications

Absolute Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		1500	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		0.1	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	0.2	A
Allowable Power Dissipation	P_D		2.0	W
		Tc=25°C	20	W
Channel Temperature	Tch		150	˚C
Storage Temperature	Tstg		–55 to +150	˚C

Electrical Characteristics at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	1500			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=1200V, V_{GS}=0			100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0			±100	nA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	1.5		3.5	V
Forward Transfer Admittance	| yfs |	V_{DS}=20V, I_D=50mA	50	100		mS
Static Drain-to-Source ON-State Resistance	$R_{DS(on)}$	I_D=50mA, V_{GS}=10V		140	200	Ω

(Note) Be careful in handling the 2SK1412 because it has no protection diode between gate and source.

Continued on next page.

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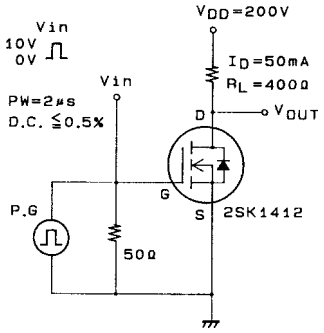
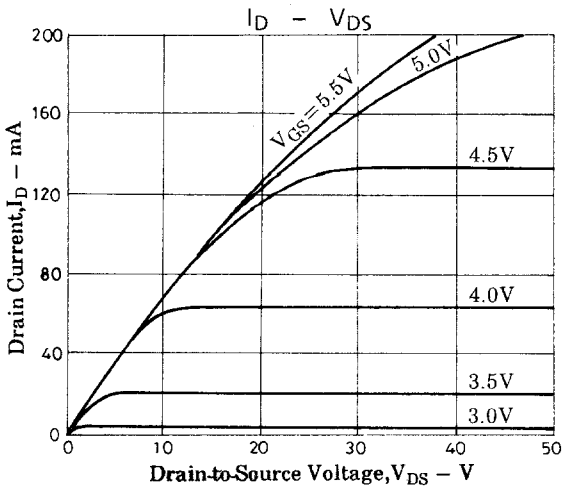
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

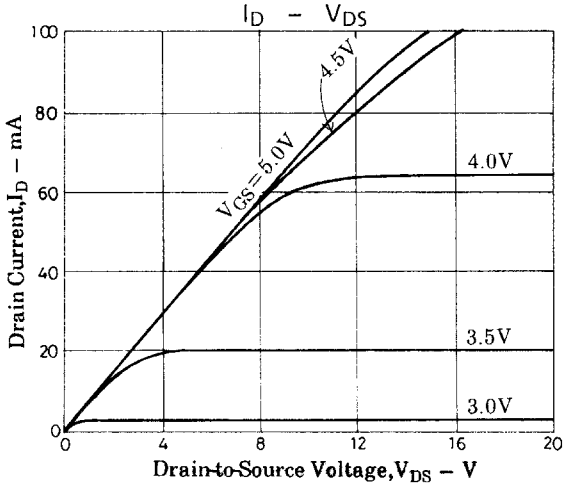
Continued from preceding page.

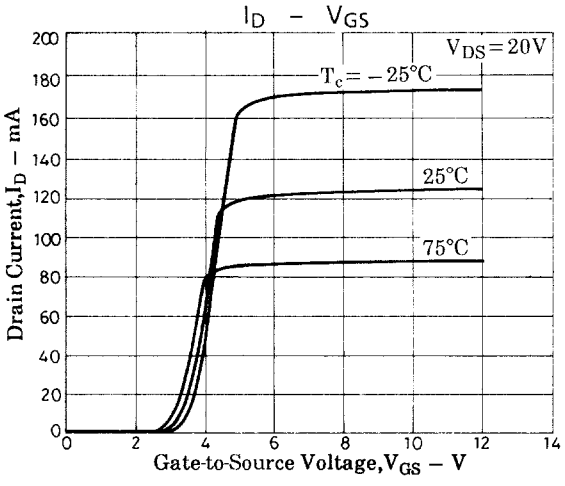
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=20V, f=1MHz		40		pF
Output Capacitance	Coss	V_{DS}=20V, f=1MHz		12		pF
Reverse Transfer Capacitance	Crss	V_{DS}=20V, f=1MHz		3.0		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		15		ns
Rise Time	t_r	See specified Test Circuit		25		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		50		ns
Fall Time	t_f	See specified Test Circuit		350		ns
Diode Forward Voltage	V_{SD}	I_S=0.1A, V_{GS}=0		1.0	1.5	V

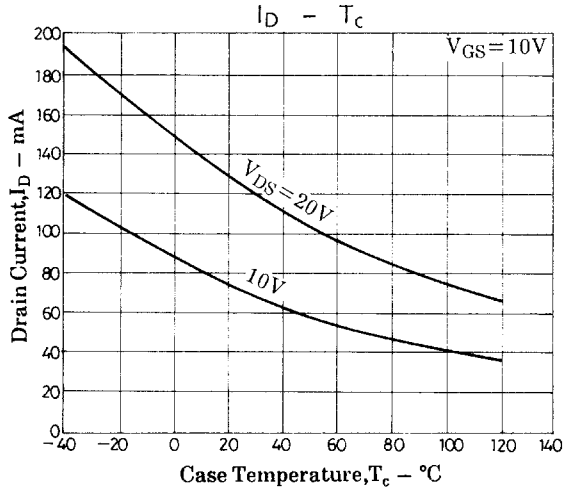
Switching Time Test Circuit

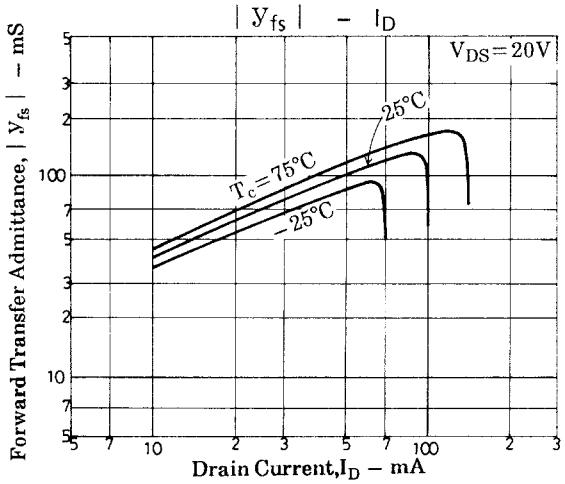


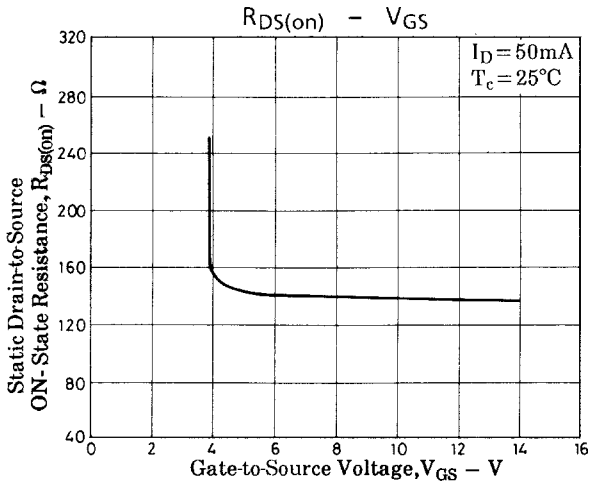


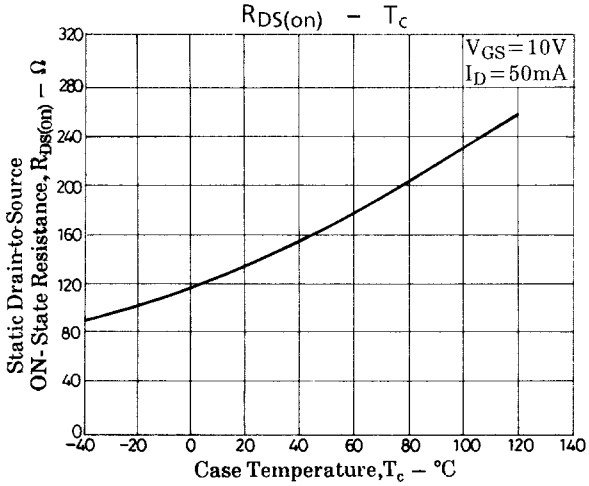


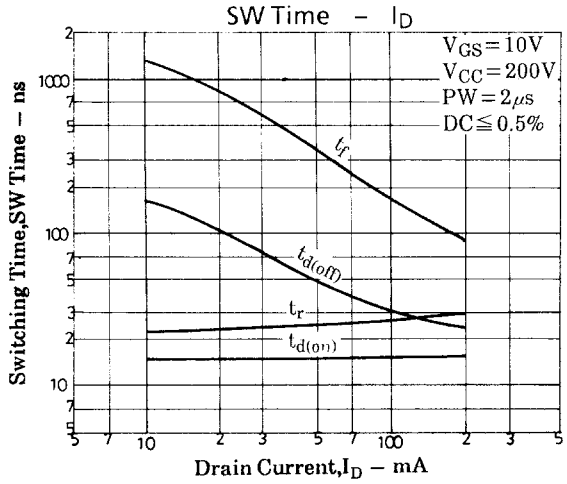


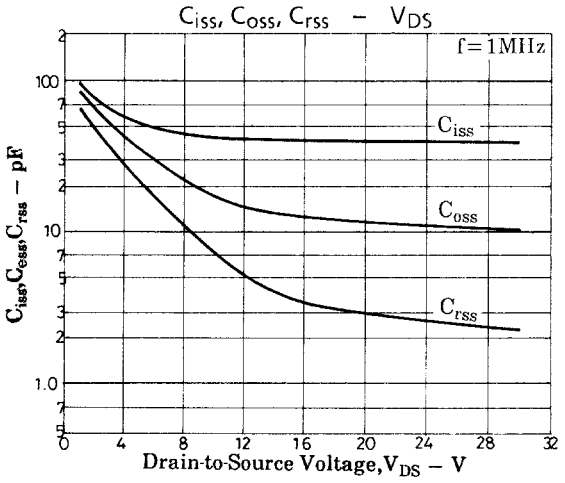


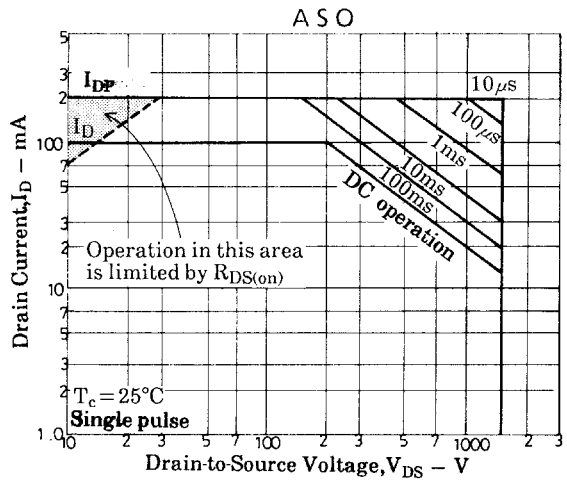

| Yfs | – ID
VDS=20V
25°C
Tc=75°C
–25°C
Forward Transfer Admittance, | Yfs | – mS
Drain Current,ID – mA

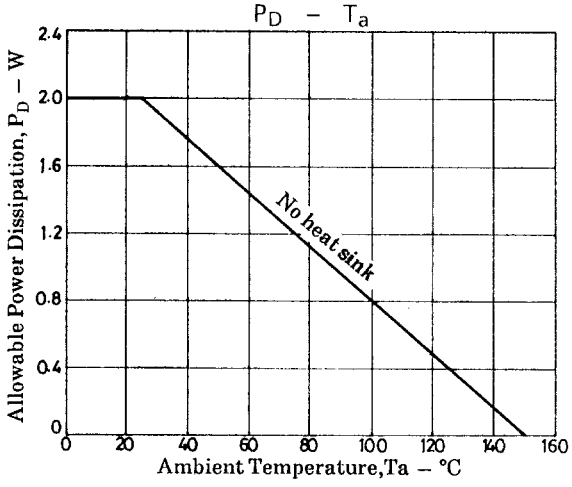

RDS(on) – VGS
ID=50mA
Tc=25°C
Static Drain-to-Source ON-State Resistance, RDS(on) – Ω
Gate-to-Source Voltage,VGS – V

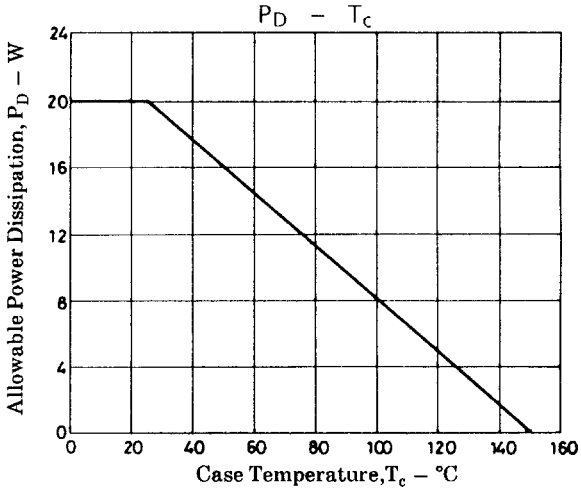

RDS(on) – Tc
VGS=10V
ID=50mA
Static Drain-to-Source ON-State Resistance, RDS(on) – Ω
Case Temperature,Tc – °C


SW Time – ID
VGS=10V
VCC=200V
PW=2μs
DC≦0.5%
tf
td(off)
tr
td(on)
Switching Time,SW Time – ns
Drain Current,ID – mA


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss,Coss,Crss – pF
Drain-to-Source Voltage,VDS – V


A S O
IDP
ID
10μs
100μs
1ms
10ms
100ms
DC operation
Operation in this area is limited by RDS(on)
Tc=25°C
Single pulse
Drain Current,ID – mA
Drain-to-Source Voltage,VDS – V


PD – Ta
No heat sink
Allowable Power Dissipation, PD – W
Ambient Temperature,Ta – °C


PD – Tc
Allowable Power Dissipation, PD – W
Case Temperature,Tc – °C