



# **Ultrahigh-Speed Switching Applications**

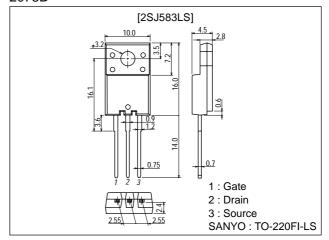
#### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · Micaless package facilitating mounting.

## **Package Dimensions**

unit:mm

2078B



# **Specifications**

### **Absolute Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-250	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±30	V
Drain Current (DC)	I <sub>D</sub>		-3.5	А
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	-14	Α
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C	20	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0	-250			V
Gate-to-Source Breakdown Voltage	V(BR)GSS	I <sub>G</sub> =±100μA, V <sub>DS</sub> =0	±30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-250V, V <sub>GS</sub> =0			-100	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-3.5		-5.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-2A	1.2	2.0		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =-2A, V <sub>GS</sub> =-10V		1.2	1.5	Ω

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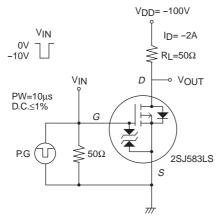
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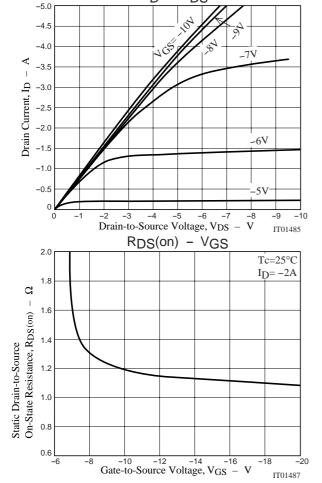
Parameter	Cumphal	Conditions	Ratings			Linit
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V <sub>DS</sub> =-20V, f=1MHz		360		pF
Output Capacitance	Coss	V <sub>DS</sub> =-20V, f=1MHz		95		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-20V, f=1MHz		40		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit		10		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		21		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		45		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		16.5		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-100V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-3.5A		18		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =-100V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-3.5A		3		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-100V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-3.5A		9		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-3.5A, V <sub>GS</sub> =0		-0.9	-1.5	V

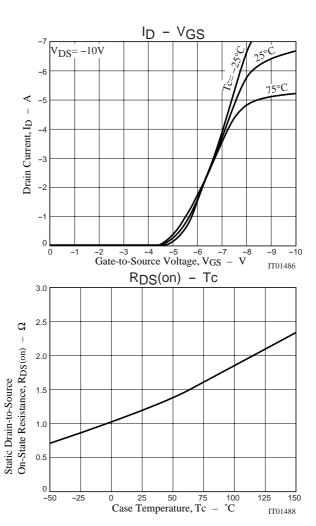
Marking : J583

## **Switching Time Test Circuit**

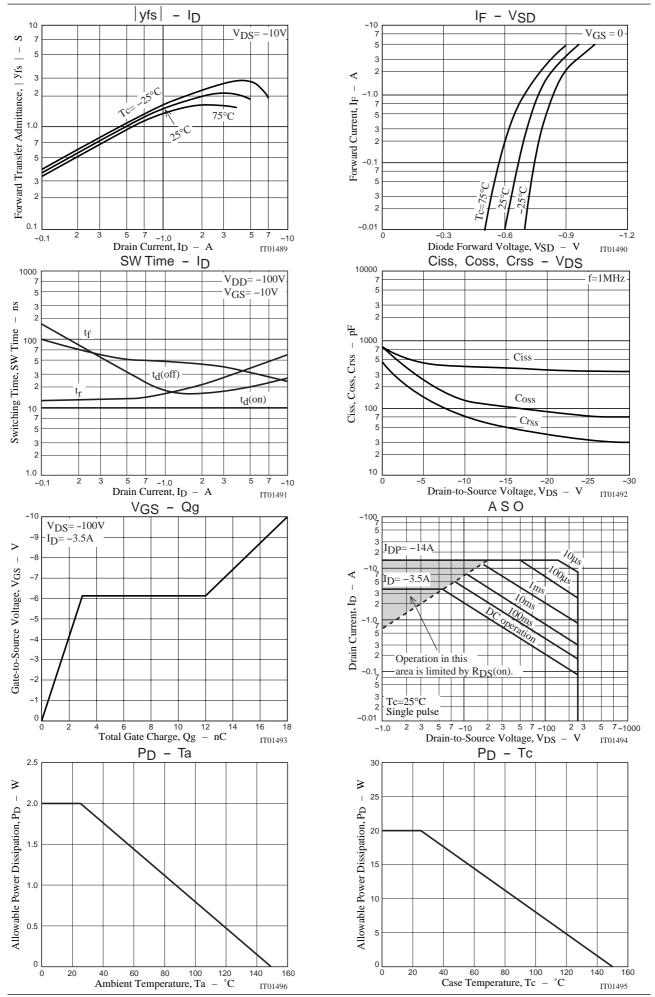


ID - VDS





## 2SJ583LS



#### 2SJ583LS

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