2SJ340



Ultrahigh-Speed Switching Applications

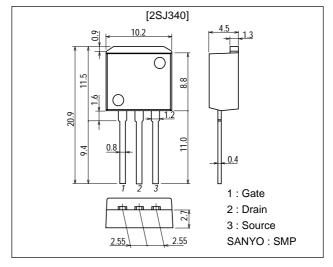
Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.
- · Enables simplified fabrication, high-density mounting, and miniaturization in end products due to the surface mountable package.

Package Dimensions

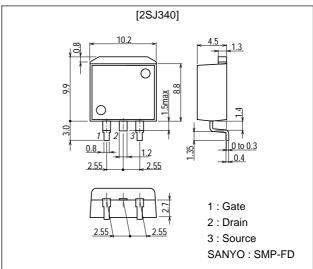
unit:mm

2093A



unit:mm

2090A



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- SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges,or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

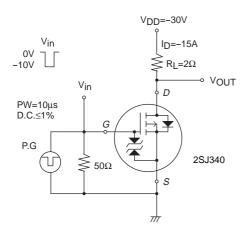
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-30	Α
Drain Current (Pulse)	I _{DP}	PW≤10µs, duty cycle≤1%	-120	Α
Allowable Power Dissipation	D-		1.65	W
	PD	Tc=25°C	70	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

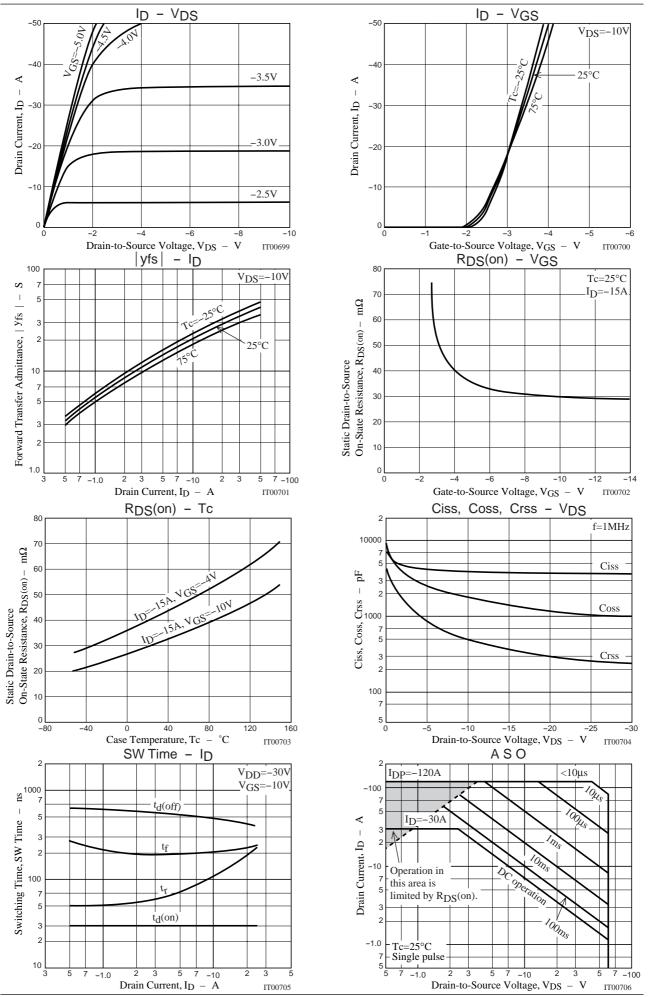
Electrical Characteristics at Ta = 25°C

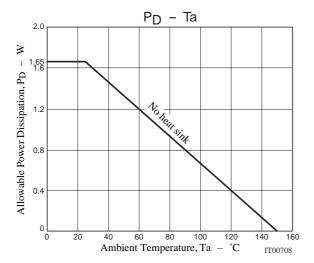
Parameter	Symbol	Conditions	Ratings			11.3
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _{GS} =0	-60			V
Gate-to-Source Breakdown Voltage	V(BR)GSS	I _G =±100μA, V _{DS} =0	±20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0			-100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} =-10V, I _D =-1mA	-1.0		-2.0	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-15A	15	25		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-15A, V _{GS} =-10V		30	40	mΩ
	R _{DS} (on)2	I _D =-15A, V _{GS} =-4V		40	55	mΩ
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		3800		pF
Output Capacitance	Coss	V _{DS} =–20V, f=1MHz		1200		pF
Reverse Transfer Capacitance	Crss	V _{DS} =–20V, f=1MHz		300		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit		30		ns
Rise Time	t _r	See specified Test Circuit		150		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		450		ns
Fall Time	t _f	See specified Test Circuit		220		ns
Diode Forward Voltage	V _{SD}	I _S =-30A, V _{GS} =0		-1.0	-1.5	V

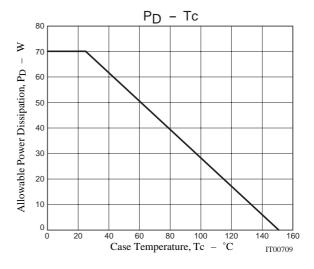
Marking:J304

Switching Time Test Circuit









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