

25V/35V, 2A Low-Frequency Power Amplifier Applications

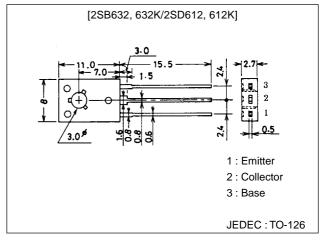
Features

· High collector dissipation and wide ASO.

Package Dimensions

unit:mm

2009B



(): 2SB632, 632K

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	2SB632, D612	2SB632K, D612K	Unit	
Collector-to-Base Voltage	V _{CBO}		(-)25	(-)25 (-)35		
Collector-to-Emitter Voltage	V _{CEO}		(-)25	(-)35	V	
Emitter-to-Base Voltage	V _{EBO}			(–)5	V	
Collector Current	I _C			(–)2		
Collector Current (Pulse)	I _{CP}			(–)3		
Collector Dissipation	PC			1		
		Tc=25°C		10		
Junction Temperature	Tj			150		
Storage Temperature	Tstg			-55 to +150		

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions			Ratings		
Falametei	Symbol			min	typ	max	Unit
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =(–)10μA, I _E =0 B632, D612		(-)25			V
			B632K, D612K	(-)35			V
Collector-to-Emitter Brakdown Voltage	V(BR)CEO	I _C =(−)1mA, R _{BE} =∞	B632, D612	(-)25			V
			B632K, D612K	(-)35			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)10μΑ, I _C =0		(-)5			V
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)20V, I _E =0				(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0				(-)1	μA

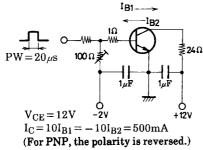
 $[\]ast$: The 2SB632/2SD612 are classified by 500mA h_{FE} as follows :

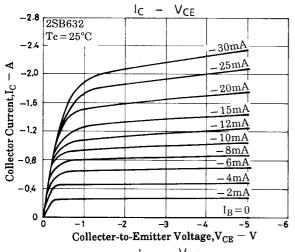
60 D 120 100 E 200 160 F 3	I	60	D	120	100	Е	200	160	F	32	0
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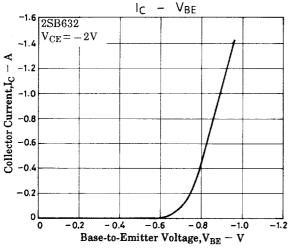
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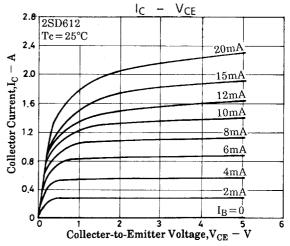
Parameter	Symbol	Conditions	Ratings			Unit
Falametei	Symbol	Conditions		typ	max	Offic
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)500mA	60*		320*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)1.5A	30			
Gain-Bandwidth Product	fT	V _{CE} =(-)10V, I _C =(-)50mA		100		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(45)30		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)1.5A, I _B =(-)0.15A		(-0.4)	(-0.9)	V
				0.3	0.8	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)1.5A, I _B =(-)0.15A		(–)1.1	(–)1.5	V
Turn-ON Time	ton	See specified Test Circuit		(60)50		ns
Fall Time	t _f	See specified Test Circuit		(80)		ns
				100		ns
Storage Time	t _{stg}	See specified Test Circuit		400		ns

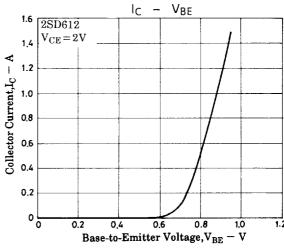
Switching Time Test Circuit

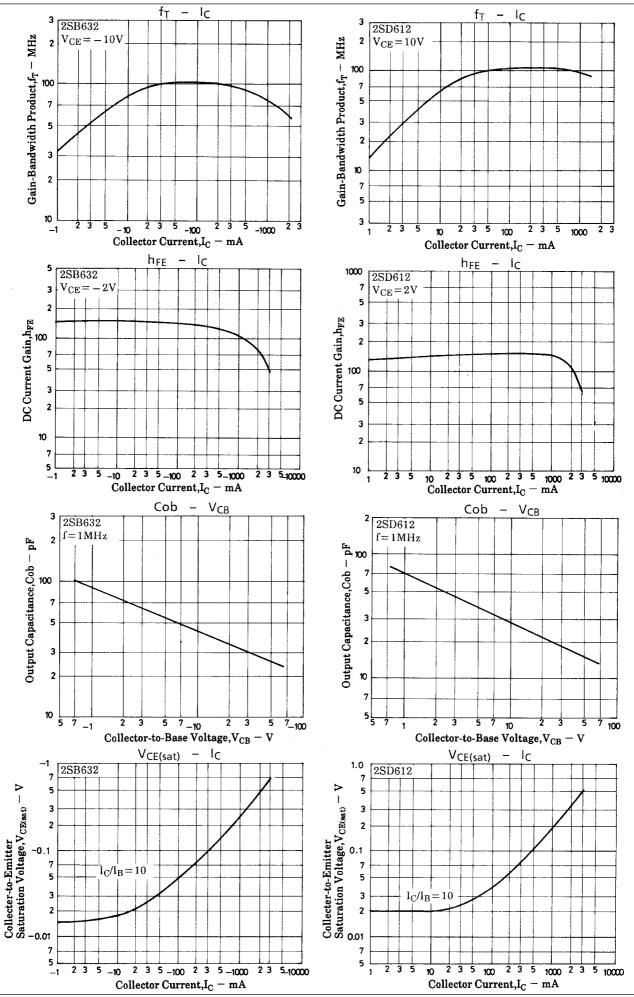


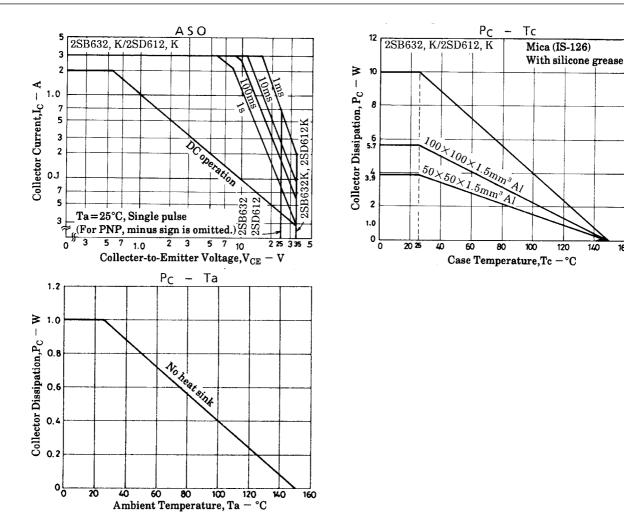








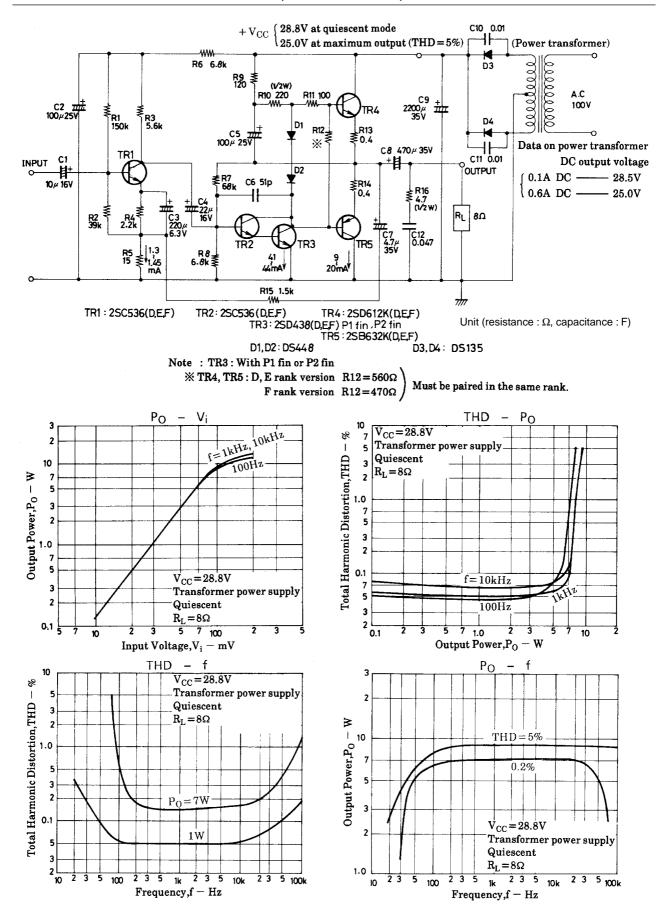


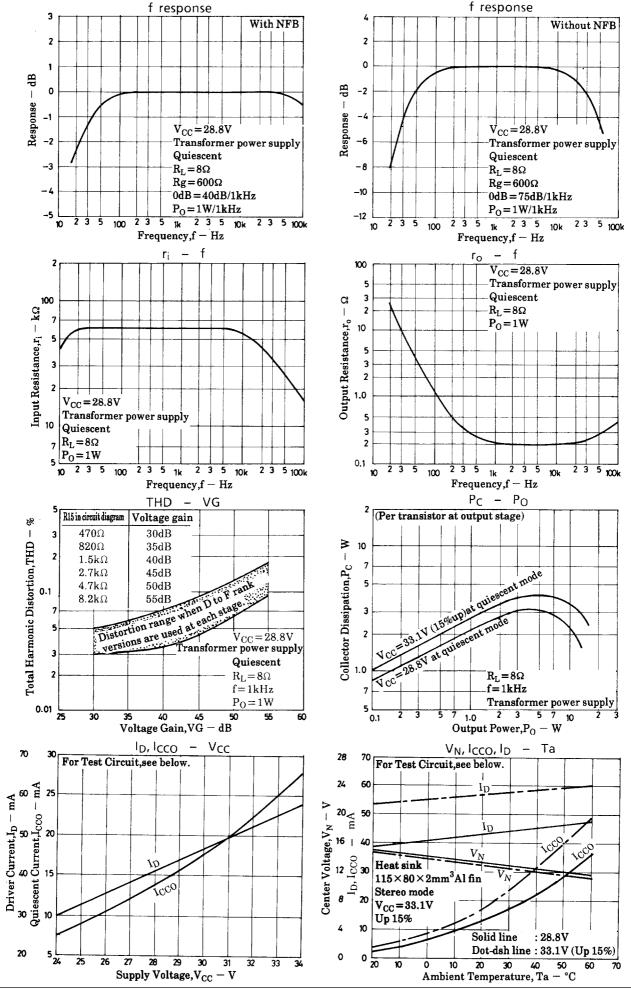


Sample Application Circuit 1:8W pure complementary amplifier using the 2SB632K/2SD612K [Specifications] Power supply: 100V AC supply transformer with no signal=28.8V. Maximum output=(THD=5%)=25V, f=1kHz, R_L =8 Ω , R_g =600 Ω .

Parameter	Symbol	Conditions	typ	Unit
Quiescent Current	Icco	Output stage	14.0	mA
(Collector Current)	I _D	Drive stage	42.0	mA
	I _C	First stage	1.4	mA
Voltage Gain	٧ _G	Without NFB	75	dB
	V _G	With NFB	40	dB
Output Power	PO	THD=5%	8.7	W
Total Harmonic Distortion	THD	P _O =1W	0.05	%
Input Resistance	rį	P _O =1W	60	kΩ
Output Resistance	ro	P _O =1W	0.2	Ω

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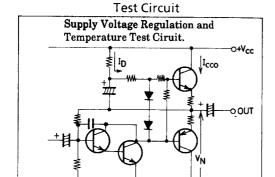


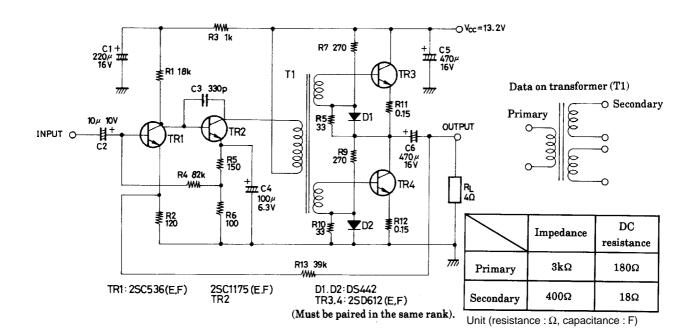
Sample Application Circuit 2: 2SD612-Used

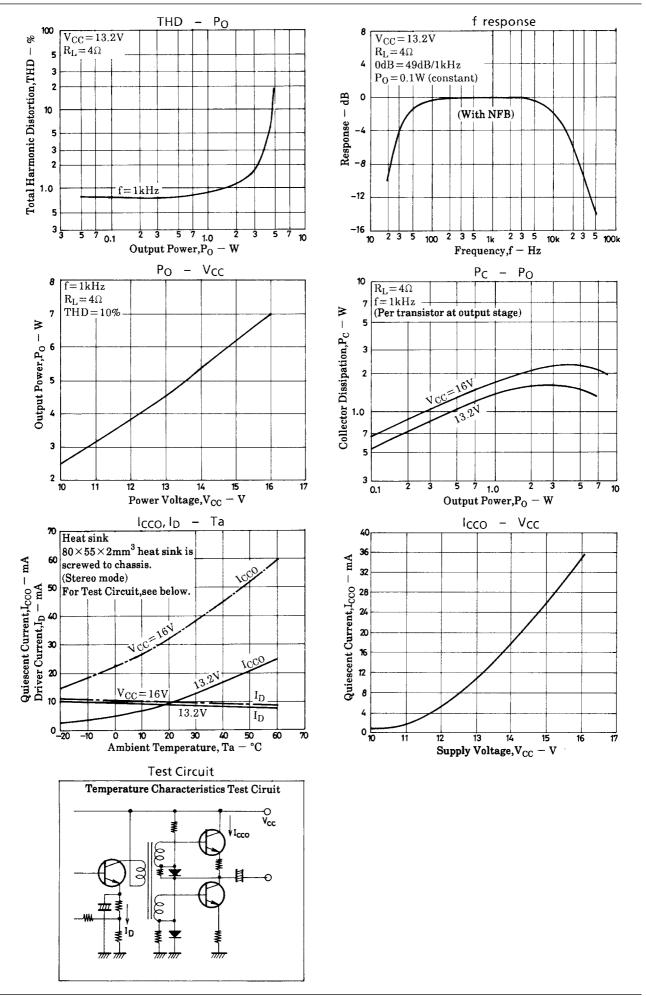
4W Input Transtformer coupling Amplifier for Car Use.

[Specifications] V_{CC}=13.2V, R_L=4 Ω , R_g=600 Ω , f=1kHz.

Parameter	Symbol	Conditions	typ	Unit
Quiescent Current	Icco	Output stage	12.0	mA
(Collector Current) Voltage Gain	ΙD	Drive stage	9.0	mA
Voltage Gain	٧ _G	Without NFB	66	dB
	٧G	With NFB	49	dB
Output Power	PO	THD=10%	4.7	W
Total Harmonic Distortion	THD	P _O =0.5W	0.8	%
Input Impedance	rį	P _O =0.5W	60	kΩ







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