Unit: mm

TOSHIBA Field Effect Transistor Silicon NPN Epitaxial Type (PCT process) (Darlington)

# 2SD1784

Micro Motor Drive, Hammer Drive Applications Switching Applications Power Amplifier Applications

- High DC current gain:  $h_{FE} = 4000$  (min) ( $V_{CE} = 2$  V,  $I_{C} = 150$  mA)
- Low saturation voltage:  $V_{CE (sat)} = 1.5 \text{ V (max) (IC} = 1 \text{ A, IB} = 1 \text{ mA)}$

#### **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit	
Collector-base voltage	$V_{CBO}$	30	V	
Collector-emitter voltage	V <sub>CEO</sub>	30	V	
Emitter-base voltage	V <sub>EBO</sub>	10	V	
Collector current	IC	1.5	Α	
Base current	Ι <sub>Β</sub>	50	mA	
Collector power dissipation	P <sub>C</sub> (Note)	1000	mW	
Junction temperature	Tj	150	°C	
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C	

Note: 2SD1784 mounted on ceramic substrate (250  $\text{mm}^2 \times 0.8 \text{ t}$ )

4.6MAX. 1.6MAX. 1.6MAX. 1.6MAX. 1.7MAX. 0.4±0.05 1.7MAX. 0.4±0.05 1.7MAX. 0.4±0.05 1.5±0.1 1.5

2-5K1A

Weight: 0.05 g (typ.)

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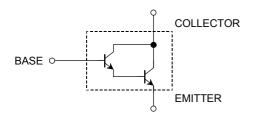
## Electrical Characteristics (Ta = 25°C)

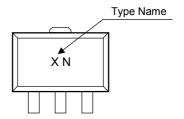
Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off cu	urrent	I <sub>CBO</sub>	V <sub>CB</sub> = 30 V, I <sub>E</sub> = 0		_	10	μΑ
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	_	_	10	μΑ
Collector-emitter b	reakdown voltage	V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	30	_	_	V
DC current gain		h <sub>FE</sub>	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 150 mA	4000	_	_	_
Collector-emitter saturation voltage V <sub>CE</sub> (		V <sub>CE</sub> (sat)	I <sub>C</sub> = 1 A, I <sub>B</sub> = 1 mA	_	_	1.5	V
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	I <sub>C</sub> = 1 A, I <sub>B</sub> = 1 mA	_	_	2.2	V
Switching time	Turn-on time	t <sub>on</sub>	20 μs OUTPUT INPUTO INPUTO G S S S S S S S S S S S S S S S S S S	_	0.20	_	
	Storage time	t <sub>stg</sub>		_	0.6	_	μs
	Fall time	t <sub>f</sub>	I <sub>B (1)</sub> = I <sub>B (2)</sub> = 1 mA V <sub>CC</sub> = 15 V DUTY CYCLE ≤ 1%	_	0.3	_	

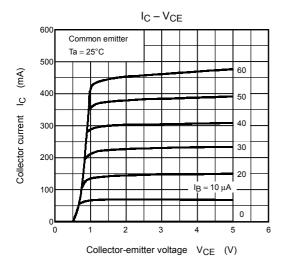
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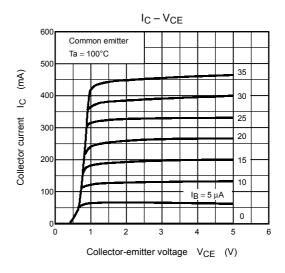
## **Equivalent Circuit**

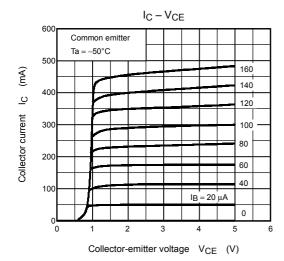
## Marking

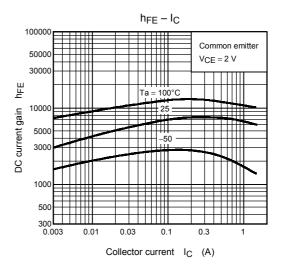


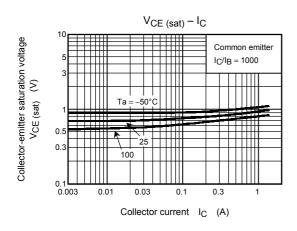


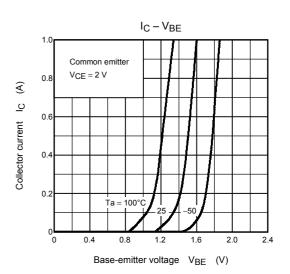


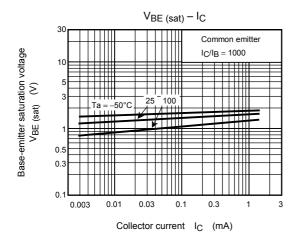


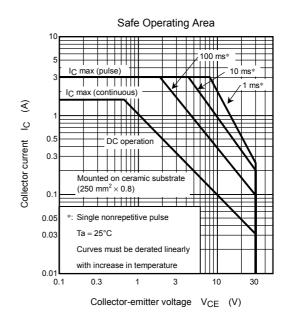


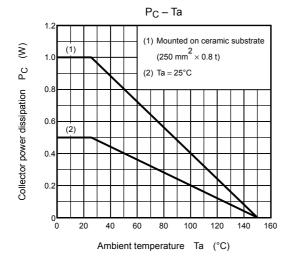












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