Unit: mm

TOSHIBA Transistor Silicon NPN Triple Diffused Type (Darlington power transistor)

# 2SD1314

# High Power Switching Applications Motor Control Applications

- High DC current gain:  $h_{FE} = 100$  (min) ( $V_{CE} = 5$  V,  $I_{C} = 15$  A)
- Low saturation voltage:  $V_{CE (sat)} = 2 V (max) (I_{C} = 15 A, I_{B} = 0.4 A)$
- High speed:  $t_f = 3 \mu s \text{ (max) (IC} = 15 \text{ A)}$

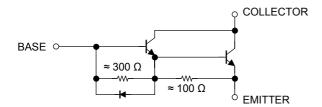
#### **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	600	V	
Collector-emitter voltage		$V_{CEO}$	450	V	
Emitter-base voltage		V <sub>EBO</sub>	6	V	
Collector current	DC	IC	15	Α	
	Pulse	I <sub>CP</sub>	30		
Base current		Ι <sub>Β</sub>	1.0	Α	
Collector power dissipation		D-	150	W	
(Tc = 25°C)		PC	150		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	−55 to 150	°C	

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Weight: 9.75 g (typ.)

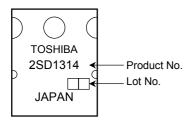
#### **Equivalent Circuit**



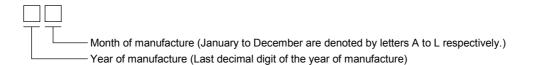
# Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	V <sub>CB</sub> = 600 V, I <sub>E</sub> = 0	_	_	1.0	mA
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = 6 V, I <sub>C</sub> = 0	_	_	200	mA
Collector-emitter	sustaining voltage	V <sub>CEO</sub> (SUS)	I <sub>C</sub> = 0.5 A, L = 40 mH	450	_	_	V
DC current gain		h <sub>FE</sub> V <sub>CE</sub> = 5 V, I <sub>C</sub> = 15 A		100	_	_	
Collector-emitter	itter saturation voltage V <sub>CE</sub> (sat)		_	_	2.0	V	
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	I <sub>C</sub> = 15 A, I <sub>B</sub> = 0.4 A	_	_	2.5	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0, f = 1 MHz	_	150	_	pF
Switching time	Turn-on time	t <sub>on</sub>	$V_{CC} = 300 \text{ V}$ Input $V_{CC} = 300 \text{ V}$ $V_{CC} = 300 \text{ V}$	_	_	1.0	
	Storage time	t <sub>stg</sub>		_	_	12	μs
	Fall time	t <sub>f</sub>		_	_	3.0	

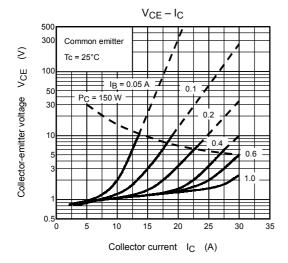
### Marking

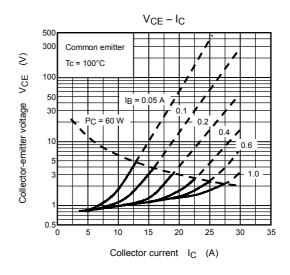


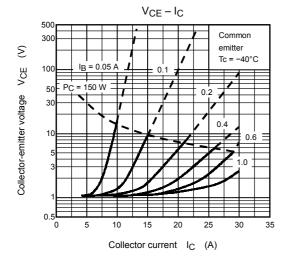
# **Explanation of Lot No.**

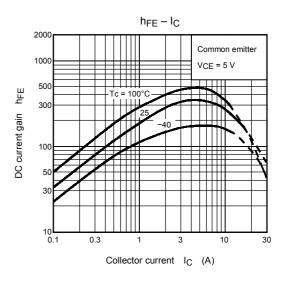


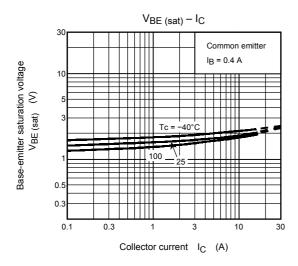
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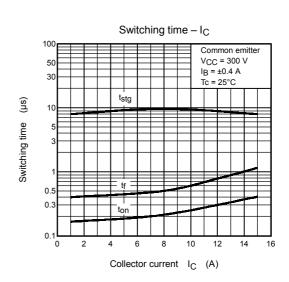


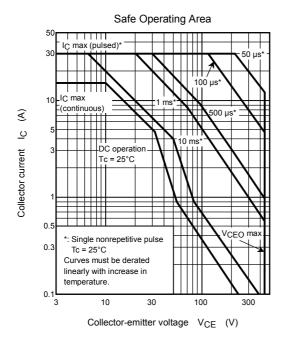












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5

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