TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (darlington)

# 2SD1224

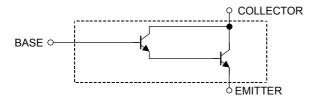
Pulse Motor Drive, Hammer Drive Applications Switching Applications Power Amplifier Applications

- High DC current gain:  $h_{FE} = 4000$  (min) ( $V_{CE} = 2$  V,  $I_{C} = 150$  mA)
- Low saturation voltage:  $V_{CE (sat)} = 1.5 \text{ V (max) (IC} = 1 \text{ A, IB} = 1 \text{ mA)}$

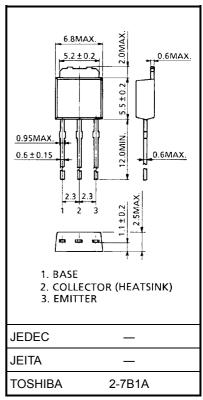
#### **Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	30	V	
Collector-emitter voltage		V <sub>CEO</sub>	30	V	
Emitter-base voltage		V <sub>EBO</sub>	10	V	
Collector current		I <sub>C</sub>	1.5	Α	
Base current		Ι <sub>Β</sub>	0.15	Α	
Collector power dissipation	Ta = 25°C	Pc	1.0	W	
	Tc = 25°C	FC	10		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

#### **Equivalent Circuit**



Unit: mm

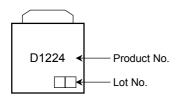


Weight: 0.36 g (typ.)

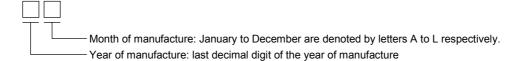
# Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off of	current	I <sub>CBO</sub>	V <sub>CB</sub> = 30 V, I <sub>E</sub> = 0	_	_	10	μA
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	_	_	10	μΑ
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	30	_	_	V
DC current gain		h <sub>FE</sub>	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 150 mA	4000	_	_	
Collector-emitter saturation voltage		V <sub>CE (sat)</sub>	I <sub>C</sub> = 1 A, I <sub>B</sub> = 1 mA	_	_	1.5	V
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	I <sub>C</sub> = 1 A, I <sub>B</sub> = 1 mA	_	_	2.2	V
Switching time	Turn-on time	t <sub>on</sub>	OUTPUT  20 μs  B1   C   C    INPUTO   H2   F    VCC ≈ 15 V	_	0.18	_	
	Storage time	t <sub>stg</sub>		_	0.6	_	μs
	Fall time	t <sub>f</sub>	I <sub>B1</sub> = -I <sub>B2</sub> = 1 mA, DUTY CYCLE ≤ 1%	_	0.3	_	

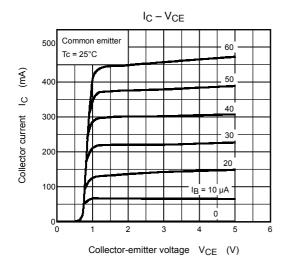
# Marking

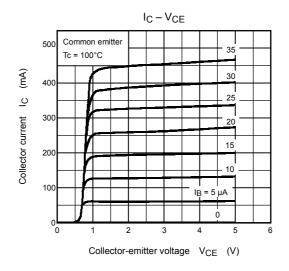


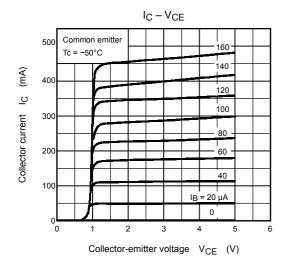
# **Explanation of Lot No.**

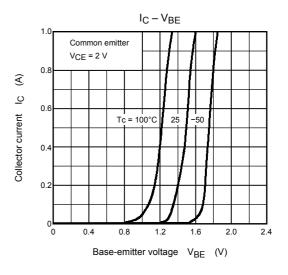


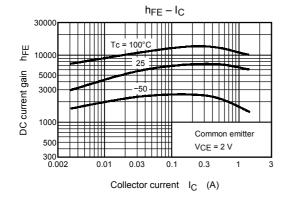
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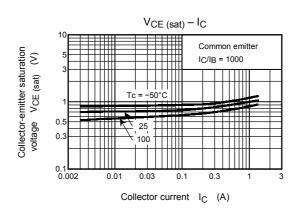




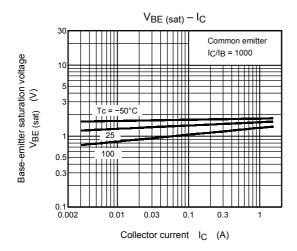


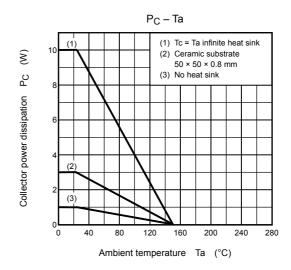


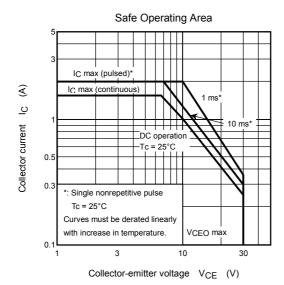




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