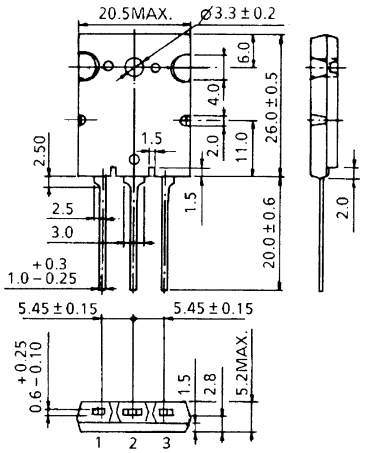
TOSHIBA Transistor Silicon NPN Triple Diffused Mesa Type

2SC5695

Horizontal Deflection Output for High Resolution Display, Color TV

- High voltage: V_{CBO} = 1500 V
- Low saturation voltage: $V_{CE\ (sat)}$ = 3 V (max)
- High speed: t_f (2) = 0.1 μs (typ.)

Unit: mm



1. Base
2. Collector (Heat sink)
3. Emitter

JEDEC	—
JEITA	—
TOSHIBA	2-21F2A

Weight: 9.75 g (typ.)

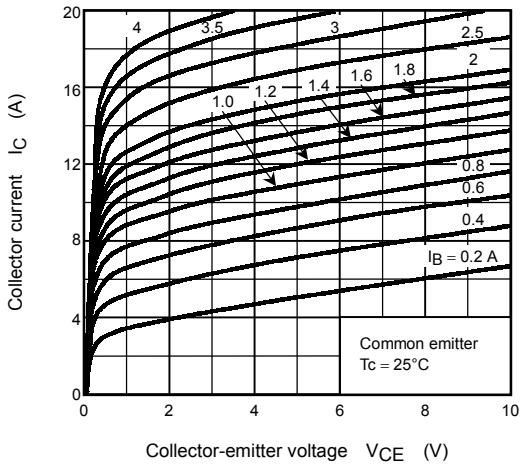
Maximum Ratings (Tc = 25°C)

Characteristics		Symbol	Rating	Unit
Collector-base voltage		V_{CBO}	1500	V
Collector-emitter voltage		V_{CEO}	700	V
Emitter-base voltage		V_{EBO}	5	V
Collector current	DC	I_C	22	A
	Pulse	I_{CP}	44	
Base current		I_B	11	A
Collector power dissipation		P_C	200	W
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−55~150	°C

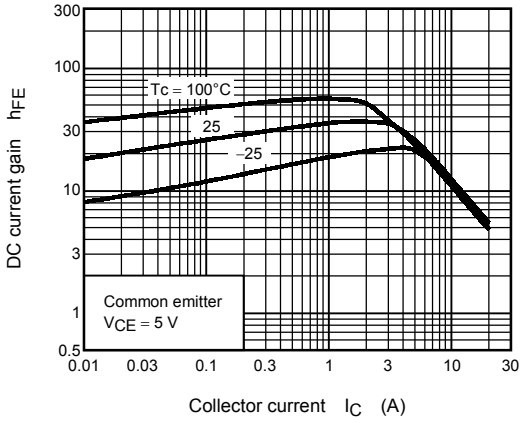
Electrical Characteristics (Tc = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		I_{CBO}	V_{CB} = 1500 V, I_E = 0	—	—	1	mA
Emitter cut-off current		I_{EBO}	V_{EB} = 5 V, I_C = 0	—	—	10	μA
Collector-emitter breakdown voltage		$V_{(BR)\ CEO}$	I_C = 10 mA, I_B = 0	700	—	—	V
DC current gain		h_{FE} (1)	V_{CE} = 5 V, I_C = 2 A	20	—	50	—
		h_{FE} (2)	V_{CE} = 5 V, I_C = 10 A	8	—	17	
		h_{FE} (3)	V_{CE} = 5 V, I_C = 17 A	4.8	—	8.3	
Collector-emitter saturation voltage		$V_{CE\ (sat)}$	I_C = 17 A, I_B = 4.25 A	—	—	3	V
Base-emitter saturation voltage		$V_{BE\ (sat)}$	I_C = 17 A, I_B = 4.25 A	—	1.0	1.5	V
Transition frequency		f_T	V_{CE} = 10 V, I_C = 0.1 A	—	2	—	MHz
Collector output capacitance		C_{ob}	V_{CB} = 10 V, I_E = 0, f = 1 MHz	—	280	—	pF
Switching time	Storage time	t_{stg} (1)	I_{CP} = 8 A, $I_{B1\ (end)}$ = 1.4 A, f_H = 64 kHz	—	2.5	3	μs
	Fall time	t_f (1)		—	0.15	0.3	
	Storage time	t_{stg} (2)	I_{CP} = 8 A, $I_{B1\ (end)}$ = 1.1 A, f_H = 100 kHz		1.6	1.8	
	Fall time	t_f (2)		—	0.1	0.15	

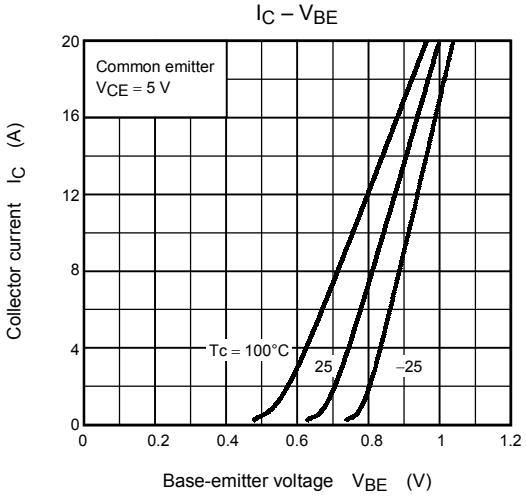
$I_C - V_{CE}$



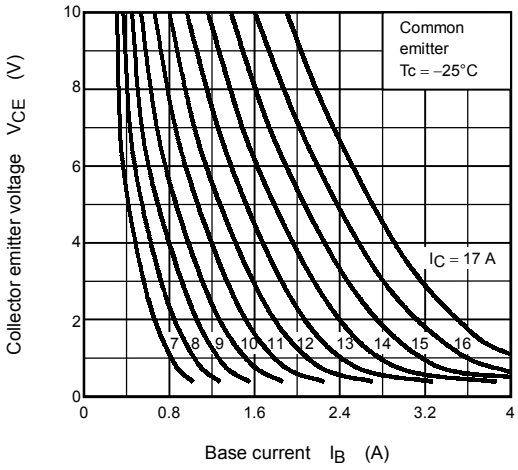
$h_{FE} - I_C$



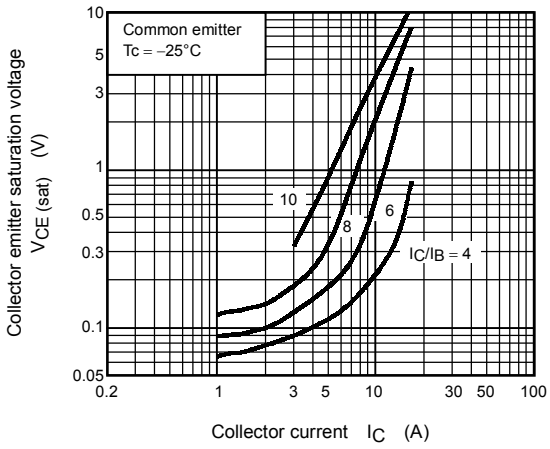
$I_C - V_{BE}$



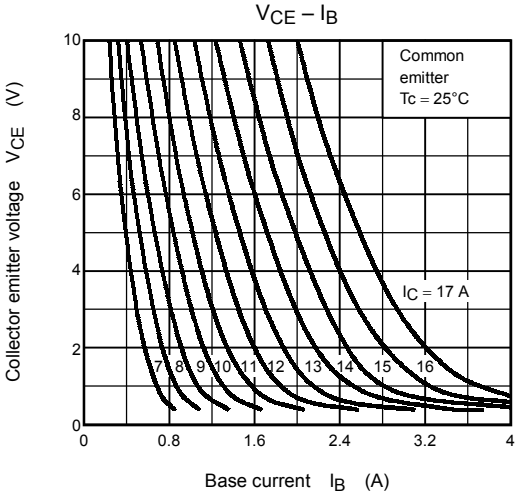
$V_{CE} - I_B$



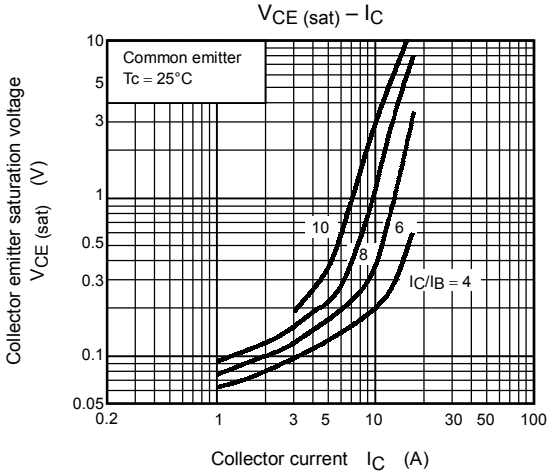
$V_{CE\ (sat)} - I_C$



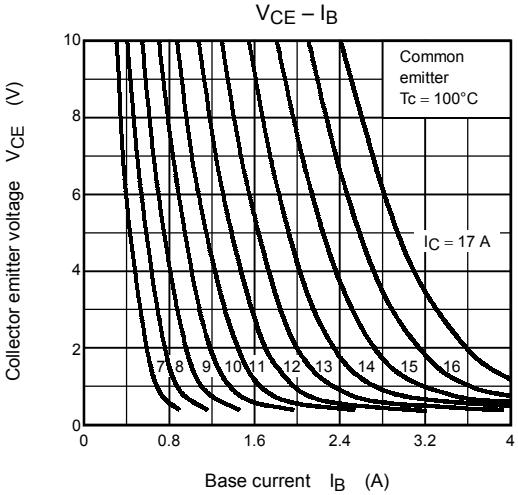
$V_{CE} - I_B$



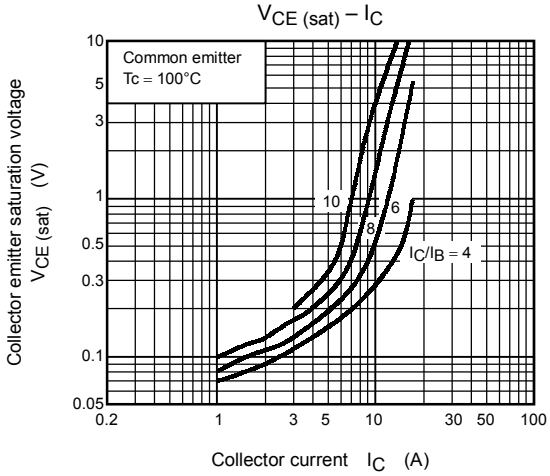
$V_{CE\ (sat)} - I_C$



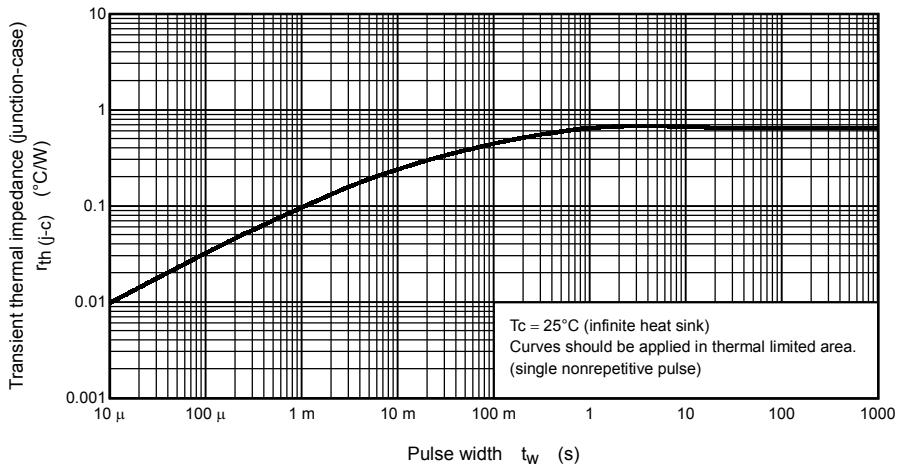
$V_{CE} - I_B$



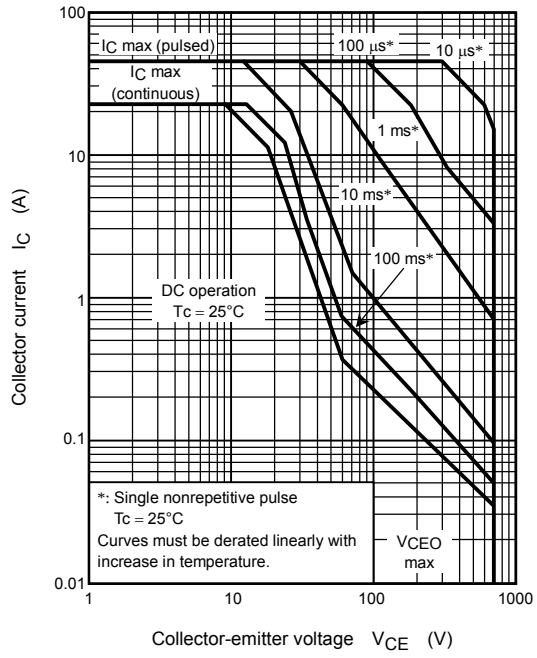
$V_{CE\ (sat)} - I_C$



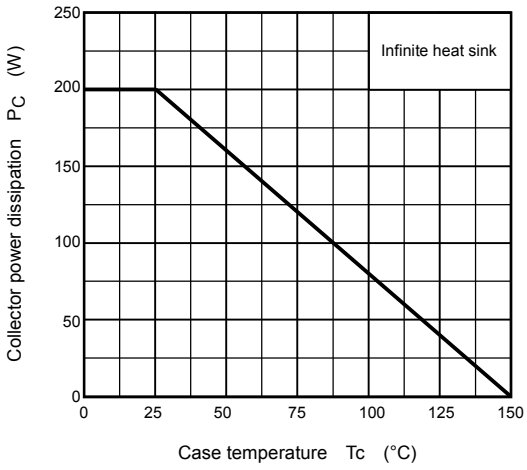
$r_{th\ (j\text{-}c)} - t_w$



Safe Operating Area



$P_C - Tc$



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