TOSHIBA TRANSISTOR SILICON NPN TRIPLE DIFFUSED MESA TYPE

2SC5588

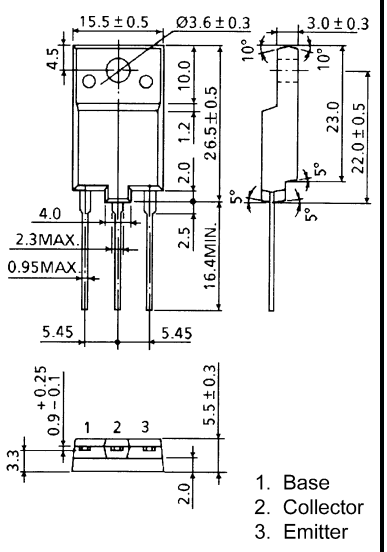
HORIZONTAL DEFLECTION OUTPUT FOR SUPER HIGH RESOLUTION DISPLAY

COLOR TV FOR DIGITAL TV & HDTV

HIGH SPEED SWITCHING APPLICATIONS

- High Voltage : V_{CBO} = 1700 V
- Low Saturation Voltage : $V_{CE\ (sat)}$ = 3 V (Max.)
- High Speed : t_f (2) = 0.1μs (Typ.)

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	2-16E3A

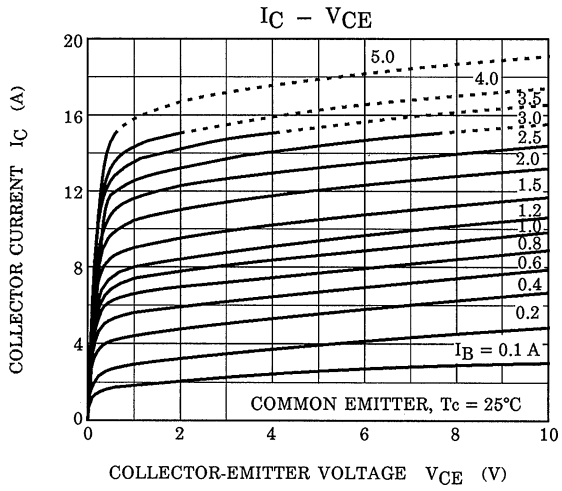
Weight: 5.5 g (typ.)

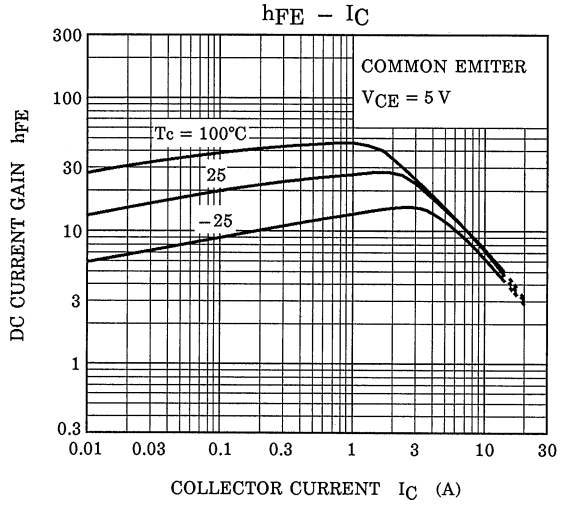
MAXIMUM RATINGS (Tc = 25°C)

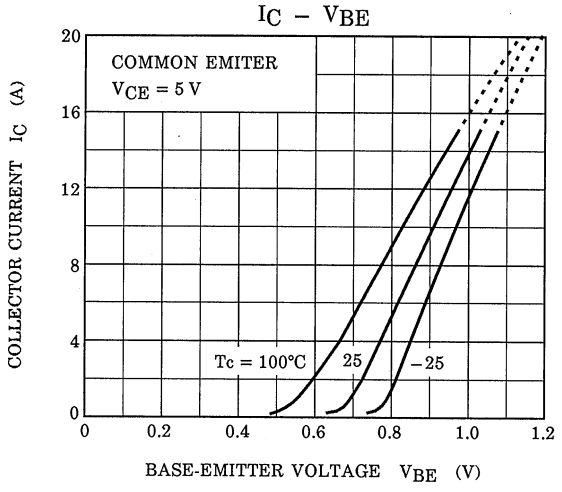
CHARACTERISTIC		SYMBOL	RATING	UNIT
Collector−Base Voltage		V_{CBO}	1700	V
Collector−Emitter Voltage		V_{CEO}	800	V
Emitter−Base Voltage		V_{EBO}	5	V
Collector Current	DC	I_C	15	A
	Pulse	I_{CP}	30	
Base Current		I_B	7.5	A
Collector Power Dissipation		P_C	75	W
Junction Temperature		T_j	150	°C
Storage Temperature Range		T_{stg}	−55~150	°C

ELECTRICAL CHARACTERISTICS (Tc = 25°C)

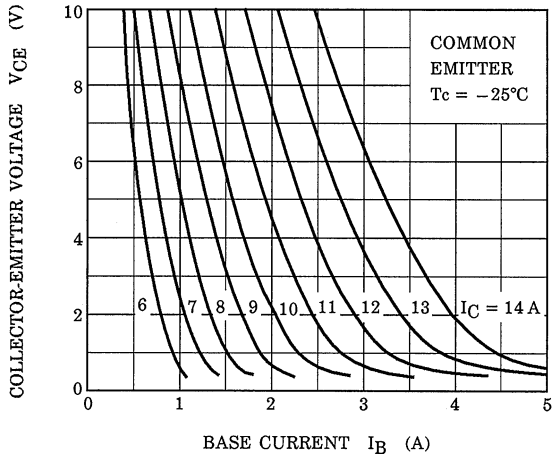
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Collector Cut−off Current		I_{CBO}	V_{CB} = 1700 V, I_E = 0	—	—	1	mA
Emitter Cut−off Current		I_{EBO}	V_{EB} = 5 V, I_C = 0	—	—	100	μA
Emitter−Base Breakdown Voltage		$V_{(BR)\ CEO}$	I_C = 10 mA, I_B = 0	800	—	—	V
DC Current Gain		$h_{FE\ (1)}$	V_{CE} = 5 V, I_C = 2 A	22	—	45	—
		$h_{FE\ (2)}$	V_{CE} = 5 V, I_C = 9 A	6.5	—	12	
		$h_{FE\ (3)}$	V_{CE} = 5 V, I_C = 12 A	4.8	—	8.0	
Collector−Emitter Saturation Voltage		$V_{CE\ (sat)}$	I_C = 12 A, I_B = 3 A	—	—	3	V
Base−Emitter Saturation Voltage		$V_{BE\ (sat)}$	I_C = 12 A, I_B = 3 A	—	1.0	1.5	V
Transition Frequency		f_T	V_{CE} = 10 V, I_C = 0.1 A	—	2	—	MHz
Collector Output Capacitance		C_{ob}	V_{CB} = 10 V, I_E = 0, f = 1 MHz	—	240	—	pF
Switching Time	Storage Time	$t_{stg\ (1)}$	I_{CP} = 9 A, I_{B1} (end) = 1.1 A, f_H = 32 kHz	—	3.5	4	μs
	Fall Time	$t_{f\ (1)}$		—	0.25	0.35	
	Storage Time	$t_{stg\ (2)}$	I_{CP} = 6.5 A, I_{B1} (end) = 1 A, f_H = 100 kHz	—	1.8	2	μs
	Fall Time	$t_{f\ (2)}$		—	0.1	0.15	


I_C – V_{CE}
COLLECTOR CURRENT I_C (A)
COLLECTOR-EMITTER VOLTAGE V_{CE} (V)
5.0
4.0
3.5
3.0
2.5
2.0
1.5
1.2
1.0
0.8
0.6
0.4
0.2
I_B = 0.1 A
COMMON EMITTER, Tc = 25°C
0
2
4
6
8
10
0
4
8
12
16
20

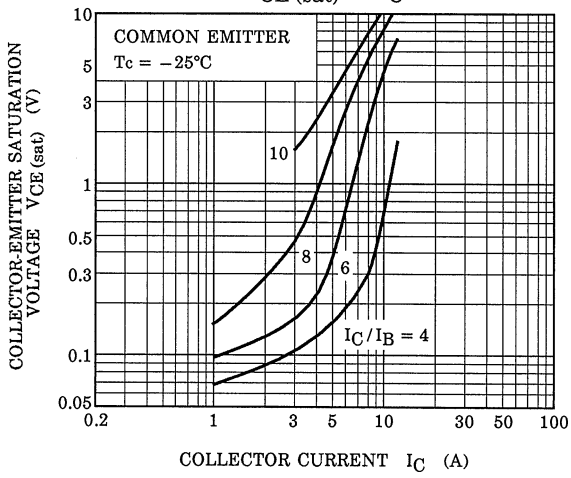

h_{FE} – I_C
DC CURRENT GAIN h_{FE}
COLLECTOR CURRENT I_C (A)
COMMON EMITER
V_{CE} = 5 V
Tc = 100°C
25
−25
300
100
30
10
3
1
0.3
0.01
0.03
0.1
0.3
1
3
10
30


I_C – V_{BE}
COLLECTOR CURRENT I_C (A)
BASE-EMITTER VOLTAGE V_{BE} (V)
COMMON EMITER
V_{CE} = 5 V
Tc = 100°C
25
−25
0
0.2
0.4
0.6
0.8
1.0
1.2
0
4
8
12
16
20

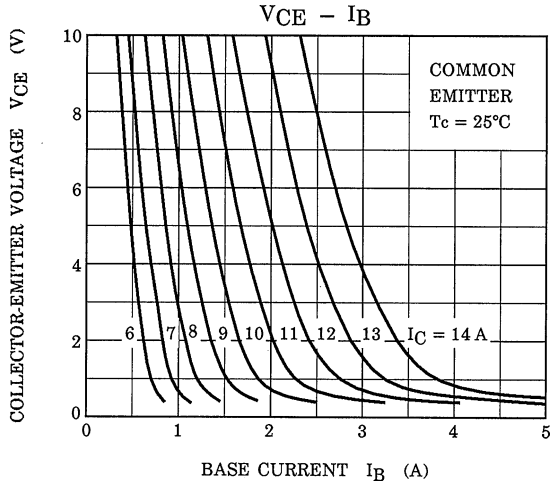
V_{CE} – I_B



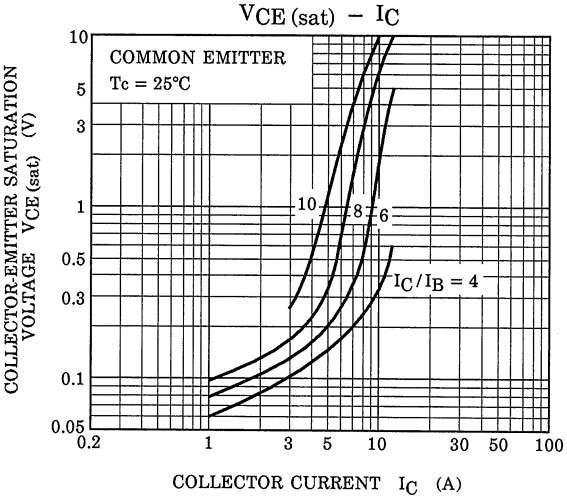
$V_{CE\,(sat)}$ – I_C



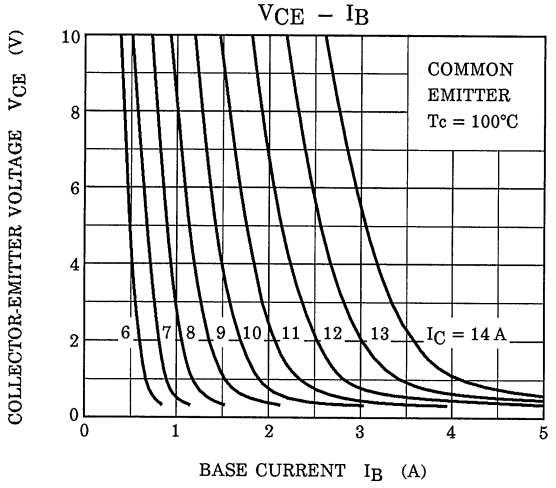
V_{CE} – I_B



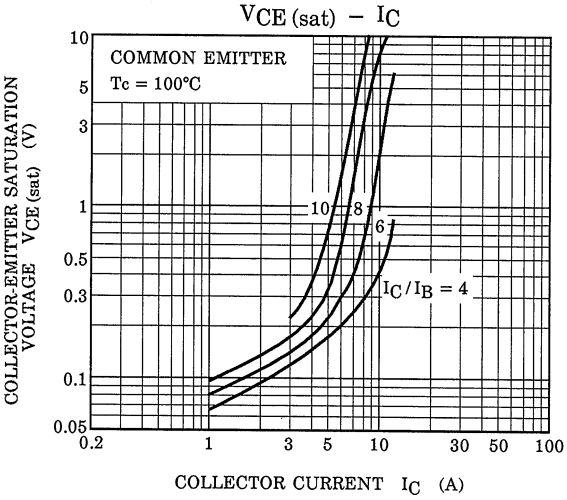
$V_{CE\,(sat)}$ – I_C



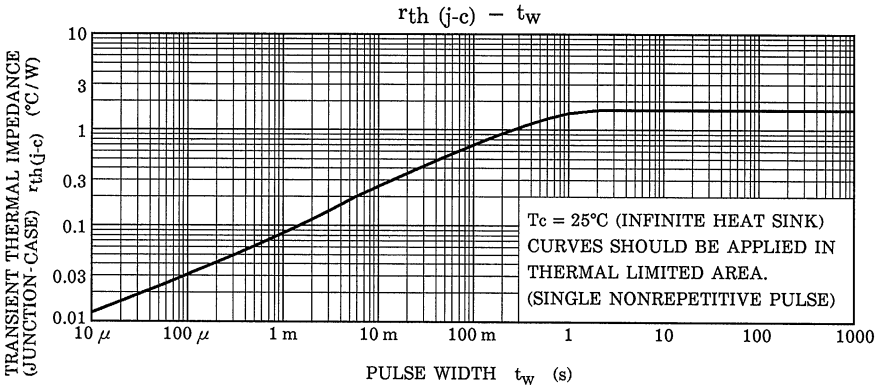
V_{CE} – I_B



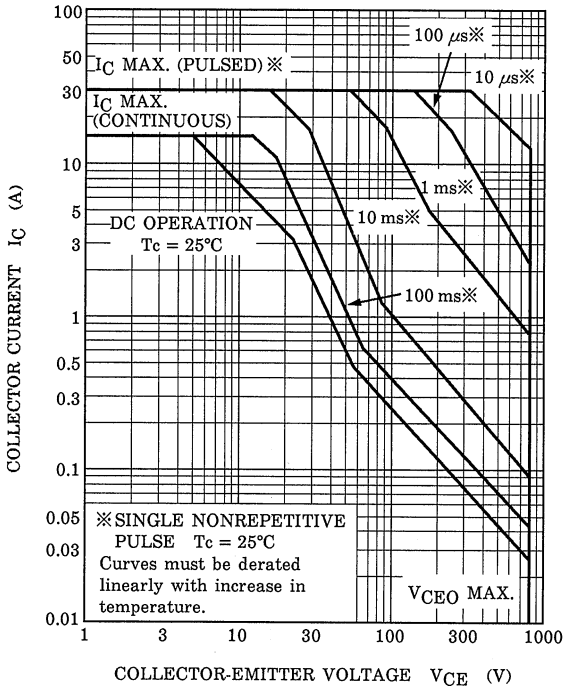
$V_{CE\,(sat)}$ – I_C



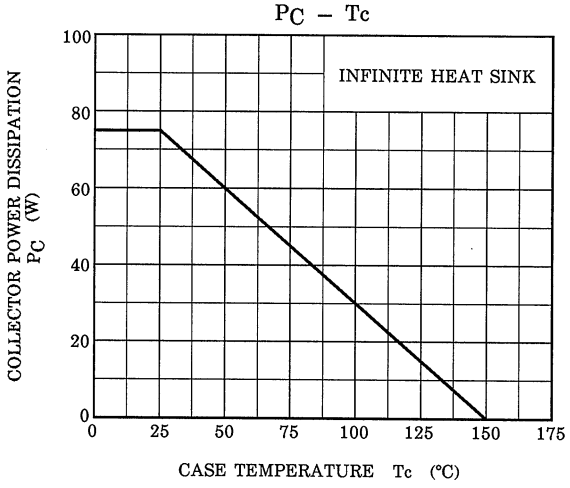
r_{th} (j-c) – t_w



SAFE OPERATING AREA



P_C – Tc



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