TOSHIBA Transistor Silicon NPN Triple Diffused Type

2SC5548

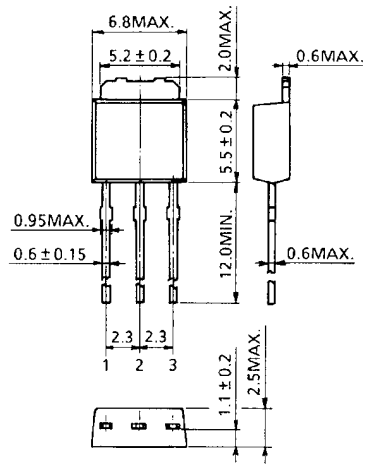
High Voltage Switching Applications
Switching Regulator Applications
DC-DC Converter Applications

- High speed switching: t_r = 0.5 μs (max), t_f = 0.3 μs (max) (I_C = 0.8 A)
- High collector breakdown voltage: V_{CEO} = 370 V
- High DC current gain: h_{FE} = 60 (min) (I_C = 0.2 A)

Maximum Ratings (Ta = 25°C)

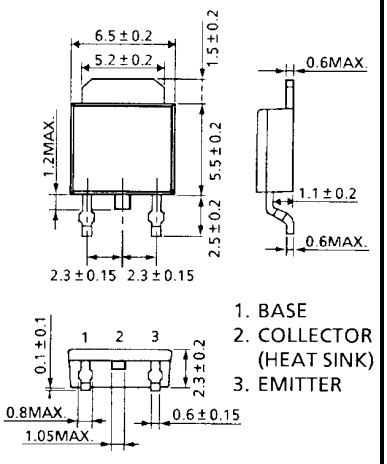
Characteristics		Symbol	Rating	Unit
Collector-base voltage		V_{CBO}	600	V
Collector-emitter voltage		V_{CEO}	370	V
Emitter-base voltage		V_{EBO}	7	V
Collector current	DC	I_C	2	A
	Pulse	I_{CP}	4	
Base current		I_B	0.5	A
Collector power dissipation	Ta = 25°C	P_C	1.0	W
	Tc = 25°C		15	
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	2-7B1A

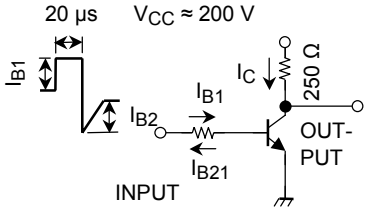
Weight: 0.36 g (typ.)



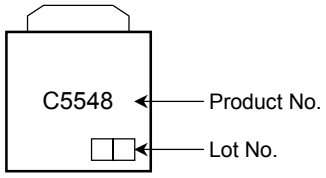
JEDEC	—
JEITA	—
TOSHIBA	2-7J1A

Weight: 0.36 g (typ.)

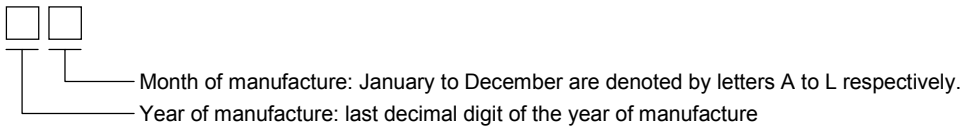
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		I_{CBO}	V_{CB} = 480 V, I_E = 0	—	—	20	μA
Emitter cut-off current		I_{EBO}	V_{EB} = 7 V, I_C = 0	—	—	10	μA
Collector-base breakdown voltage		$V_{(BR)\ CBO}$	I_C = 1 mA, I_E = 0	600	—	—	V
Collector-emitter breakdown voltage		$V_{(BR)\ CEO}$	I_C = 10 mA, I_B = 0	370	—	—	V
DC current gain		$h_{FE\ (1)}$	V_{CE} = 5 V, I_C = 1 mA	50	—	120	
		$h_{FE\ (2)}$	V_{CE} = 5 V, I_C = 0.2 A	60	—	120	
Collector emitter saturation voltage		$V_{CE\ (sat)}$	I_C = 0.8 A, I_B = 0.1 A	—	—	1.0	V
Base-emitter saturation voltage		$V_{BE\ (sat)}$	I_C = 0.8 A, I_B = 0.1 A	—	—	1.3	V
Switching time	Rise time	t_r	 I_{B1} = 0.1 A, I_{B2} = −0.2 A DUTY CYCLE ≤ 1%	—	—	0.5	μs
	Storage time	t_{stg}		—	—	3.0	
	Fall time	t_f		—	—	0.3	

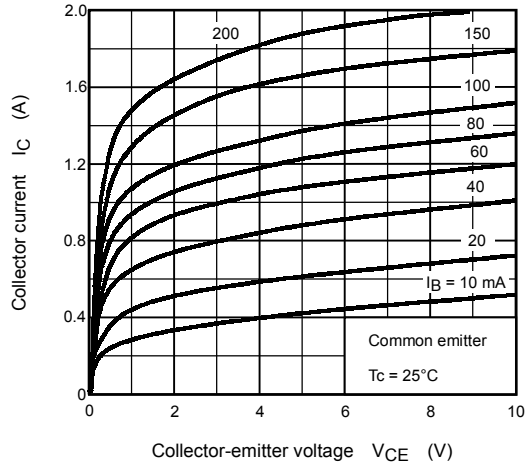
Marking



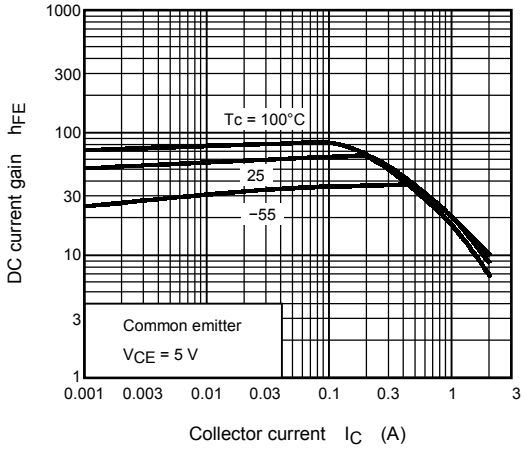
Explanation of Lot No.



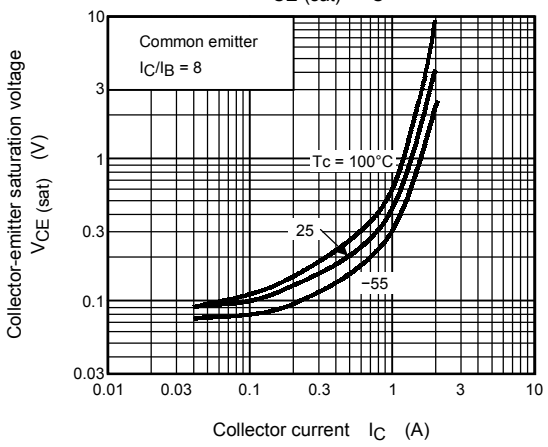
$I_C - V_{CE}$



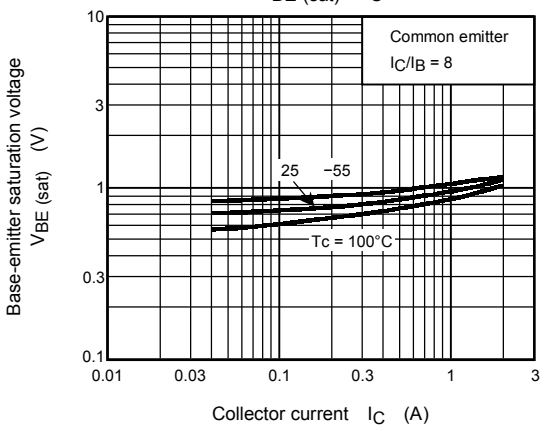
$h_{FE} - I_C$



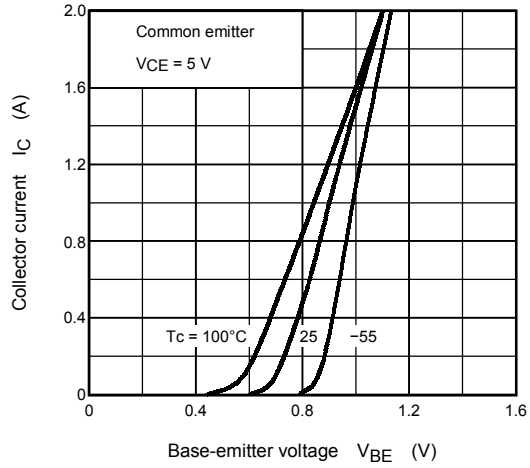
$V_{CE\ (sat)} - I_C$



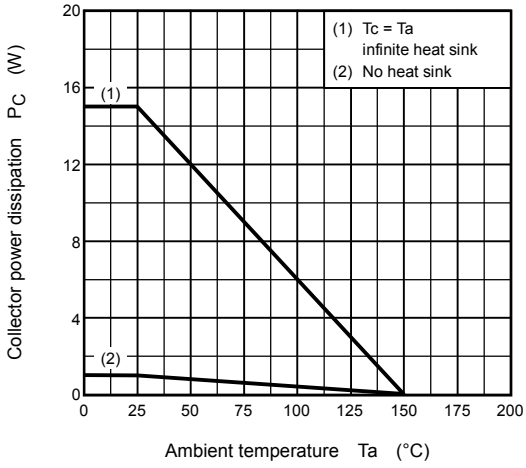
$V_{BE\ (sat)} - I_C$



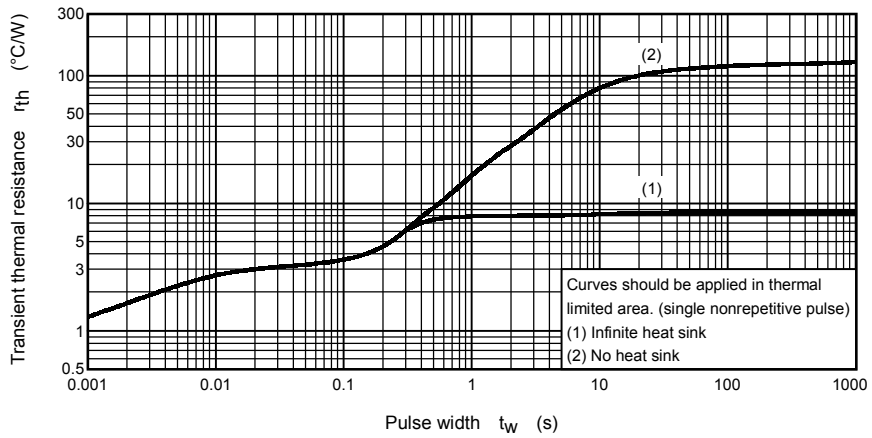
$I_C - V_{BE}$



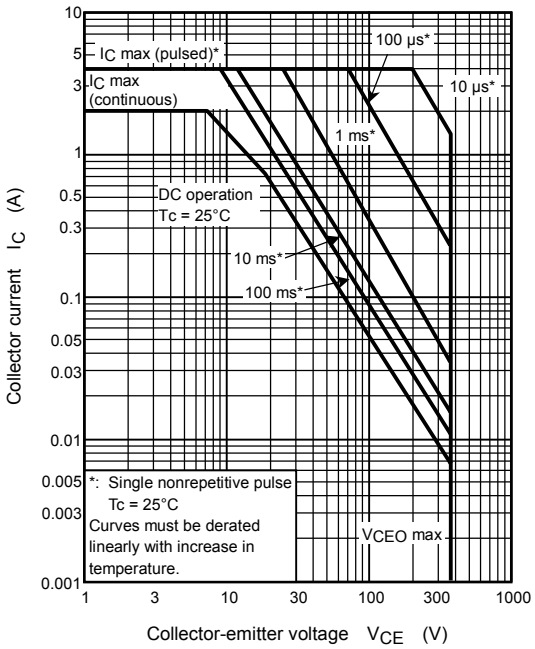
$P_C - Ta$



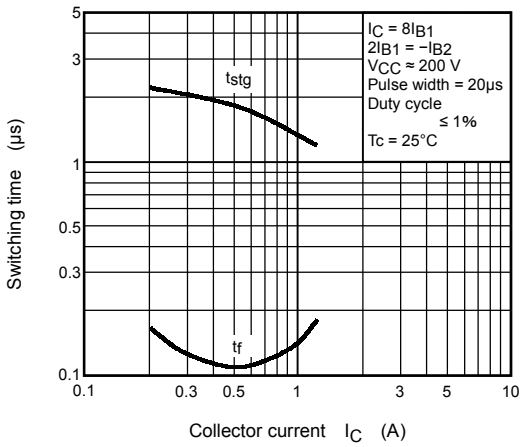
$r_{th} - t_w$



Safe Operating Area



Switching Characteristics – I_C



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