2SA1855/2SC4837



50V/4A Switching Applications

Applications

· Power supplies, relay drivers, lamp drivers.

Features

- · Adoption of FBET and MBIT processes.
- · Large allowable collector dissipation.
- · Low saturation voltage.
- · Wide ASO and large current capacity.
- · Usage of radial taping to meet automatic mounting.

(): 2SA1855

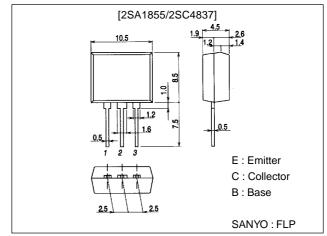
Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm

2084B



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)60	V
Collector-to-Emitter Voltage	VCEO		(–)50	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	IC		(-)4	Α
Colletor Current (Pulse)	I _{CP}		(–)6	Α
Collector Dissipation	PC		1.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	mbol Conditions	Ratings			Unit
Farameter	Symbol		min	typ	max	Offic
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0			(–)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)1	μA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)10mA	100*		400*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)3A	40			
Gain Bandwidth Product	f _T	V _{CE} =(-)10V, I _C =(-)50mA		150		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(39)25		pF

^{* :} The 2SA1855/2SC4837 are classified by 100mA $h_{\rm FE}$ as follows :

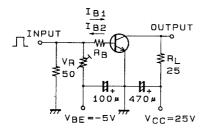
	100	R	200	140	S	280	200	Т	400
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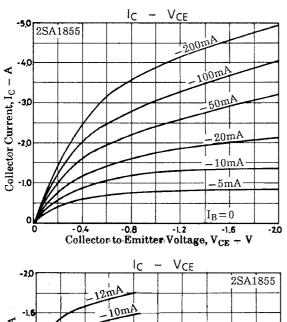
Parameter	Symbol	Conditions		Unit		
Falametei	Symbol	Conditions	min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =(-)2A, I _B =(-)100mA		(-350)	(-700)	mV
				190	500	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)2A, I _B =(-)100mA		(-)0.94	(-)1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =-1mA, R _{BE} =∞	(-)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =-10μA, I _C =0	(–)6			V
Turn-ON Time	t _{on}	See specified Test Clrcuit		70		ns
Storage Time	t _{stg}	See specified Test CIrcuit		(450)		ns
				650		ns
Fall Time	t _f	See specified Test Circuit		(30)35		ns

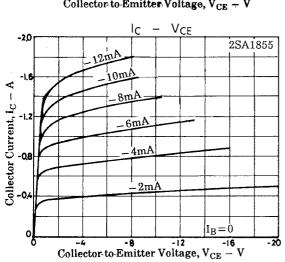
Switching Time Test Circui

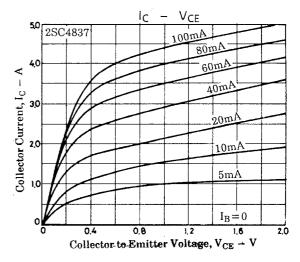
 $PW = 20\mu s$ $DC \le 1\%$

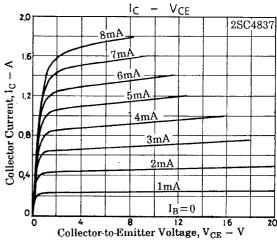


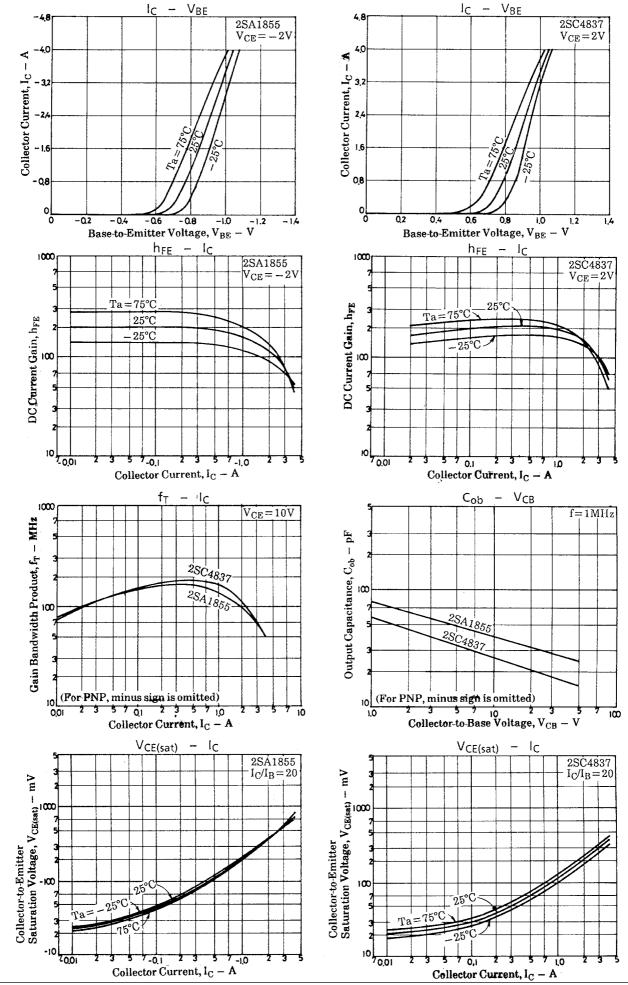
 $\begin{array}{c} I_C \!=\! 10I_B1 \!=\! -10I_B2 \!=\! 1A & \text{Access} \\ \text{Unit (resistance : } \Omega, \text{ capacitance : } F) \end{array}$

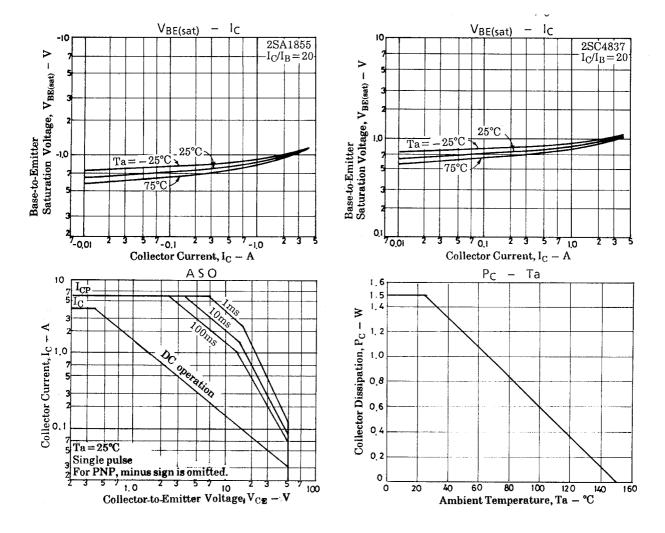












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